

Hoi-Jun Yoo

List of Publications by Citations

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173
papers

2,726
citations

25
h-index

46
g-index

236
ext. papers

3,609
ext. citations

4.6
avg. IF

5.61
L-index

#	Paper	IF	Citations
173	1.25-Gb/s regulated cascode CMOS transimpedance amplifier for Gigabit Ethernet applications. <i>IEEE Journal of Solid-State Circuits</i> , 2004 , 39, 112-121	5.5	199
172	14.2 DNPU: An 8.1TOPS/W reconfigurable CNN-RNN processor for general-purpose deep neural networks 2017 ,		157
171	UNPU: A 50.6TOPS/W unified deep neural network accelerator with 1b-to-16b fully-variable weight bit-precision 2018 ,		151
170	Neuro-inspired computing chips. <i>Nature Electronics</i> , 2020 , 3, 371-382	28.4	139
169	UNPU: An Energy-Efficient Deep Neural Network Accelerator With Fully Variable Weight Bit Precision. <i>IEEE Journal of Solid-State Circuits</i> , 2019 , 54, 173-185	5.5	103
168	Electrical Characterization of Screen-Printed Circuits on the Fabric. <i>IEEE Transactions on Advanced Packaging</i> , 2010 , 33, 196-205		101
167	Toward all-day wearable health monitoring: An ultralow-power, reflective organic pulse oximetry sensing patch. <i>Science Advances</i> , 2018 , 4, eaas9530	14.3	93
166	. <i>IEEE Journal of Solid-State Circuits</i> , 2011 , 46, 353-364	5.5	89
165	. <i>IEEE Journal of Solid-State Circuits</i> , 2010 , 45, 32-45	5.5	82
164	. <i>IEEE Journal of Solid-State Circuits</i> , 2015 , 50, 245-257	5.5	69
163	2019 ,		59
162	Power and Area-Efficient Unified Computation of Vector and Elementary Functions for Handheld 3D Graphics Systems. <i>IEEE Transactions on Computers</i> , 2008 , 57, 490-504	2.5	54
161	An Impedance and Multi-Wavelength Near-Infrared Spectroscopy IC for Non-Invasive Blood Glucose Estimation. <i>IEEE Journal of Solid-State Circuits</i> , 2015 , 50, 1025-1037	5.5	50
160	4.6 A1.93TOPS/W scalable deep learning/inference processor with tetra-parallel MIMD architecture for big-data applications 2015 ,		41
159	A Low-Power Convolutional Neural Network Face Recognition Processor and a CIS Integrated With Always-on Face Detector. <i>IEEE Journal of Solid-State Circuits</i> , 2018 , 53, 115-123	5.5	39
158	. <i>IEEE Journal of Solid-State Circuits</i> , 2012 , 47, 2678-2692	5.5	39
157	. <i>IEEE Journal of Solid-State Circuits</i> , 2011 , 46, 42-51	5.5	39

156	A Planar MICS Band Antenna Combined With a Body Channel Communication Electrode for Body Sensor Network. <i>IEEE Transactions on Microwave Theory and Techniques</i> , 2009 , 57, 2515-2522	4.1	39
155	A 82-nW Chaotic Map True Random Number Generator Based on a Sub-Ranging SAR ADC. <i>IEEE Journal of Solid-State Circuits</i> , 2017 , 52, 1953-1965	5.5	37
154	. <i>IEEE Journal of Solid-State Circuits</i> , 2009 , 44, 136-147	5.5	33
153	An EEG-NIRS Multimodal SoC for Accurate Anesthesia Depth Monitoring. <i>IEEE Journal of Solid-State Circuits</i> , 2018 , 53, 1830-1843	5.5	32
152	The Effects of Electrode Configuration on Body Channel Communication Based on Analysis of Vertical and Horizontal Electric Dipoles. <i>IEEE Transactions on Microwave Theory and Techniques</i> , 2015 , 63, 1409-1420	4.1	31
151	A 10.4 mW Electrical Impedance Tomography SoC for Portable Real-Time Lung Ventilation Monitoring System. <i>IEEE Journal of Solid-State Circuits</i> , 2015 , 50, 2501-2512	5.5	29
150	A 45 μ W Injection-Locked FSK Wake-Up Receiver With Frequency-to-Envelope Conversion for Crystal-Less Wireless Body Area Network. <i>IEEE Journal of Solid-State Circuits</i> , 2015 , 50, 1351-1360	5.5	27
149	A 37.5 μ W Body Channel Communication Wake-Up Receiver With Injection-Locking Ring Oscillator for Wireless Body Area Network. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , 2013 , 60, 1200-1208	3.9	26
148	. <i>IEEE Journal of Solid-State Circuits</i> , 2015 , 50, 2549-2559	5.5	25
147	24-GOPS 4.5- mm ² digital cellular neural network for rapid visual attention in an object-recognition SoC. <i>IEEE Transactions on Neural Networks</i> , 2011 , 22, 64-73		25
146	7.4 GANPU: A 135TFLOPS/W Multi-DNN Training Processor for GANs with Speculative Dual-Sparsity Exploitation 2020 ,		24
145	A 1.4-m Ω -Sensitivity 94-dB Dynamic-Range Electrical Impedance Tomography SoC and 48-Channel Hub-SoC for 3-D Lung Ventilation Monitoring System. <i>IEEE Journal of Solid-State Circuits</i> , 2017 , 52, 2829-2842	5.5	24
144	1-Gb/s 80-dB/spl Ω / fully differential CMOS transimpedance amplifier in multichip on oxide technology for optical interconnects. <i>IEEE Journal of Solid-State Circuits</i> , 2004 , 39, 971-974	5.5	24
143	DNPU: An Energy-Efficient Deep-Learning Processor with Heterogeneous Multi-Core Architecture. <i>IEEE Micro</i> , 2018 , 38, 85-93	1.8	24
142	A 320 mW 342 GOPS Real-Time Dynamic Object Recognition Processor for HD 720p Video Streams. <i>IEEE Journal of Solid-State Circuits</i> , 2013 , 48, 33-45	5.5	23
141	A low power ECG signal processor for ambulatory arrhythmia monitoring system 2010 ,		23
140	A Wearable Fabric Computer by Planar-Fashionable Circuit Board Technique 2009 ,		23
139	A Low-Power Deep Neural Network Online Learning Processor for Real-Time Object Tracking Application. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , 2019 , 66, 1794-1804	3.9	22

138	2018,			20
137	Packet-switched on-chip interconnection network for system-on-chip applications. <i>IEEE Transactions on Circuits and Systems Part 2: Express Briefs</i> , 2005 , 52, 308-312			20
136	A 2.1TFLOPS/W Mobile Deep RL Accelerator with Transposable PE Array and Experience Compression 2019,			17
135	14.1 A 126.1mW real-time natural UI/UX processor with embedded deep-learning core for low-power smart glasses 2016,			17
134	. <i>IEEE Journal of Solid-State Circuits</i> , 2017 , 52, 139-150	5.5		16
133	A 7.1-GB/s low-power rendering engine in 2-D array-embedded memory logic CMOS for portable multimedia system. <i>IEEE Journal of Solid-State Circuits</i> , 2001 , 36, 944-955	5.5		16
132	A 200-Mbps 0.02-nJ/b Dual-Mode Inductive Coupling Transceiver for cm-Range Multimedia Application. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , 2009 , 56, 1063-1072	3.9		15
131	A 1.32 TOPS/W Energy Efficient Deep Neural Network Learning Processor with Direct Feedback Alignment based Heterogeneous Core Architecture 2019,			14
130	. <i>IEEE Journal of Solid-State Circuits</i> , 2019 , 54, 1185-1195	5.5		14
129	A 1.22 TOPS and 1.52 mW/MHz Augmented Reality Multicore Processor With Neural Network NoC for HMD Applications. <i>IEEE Journal of Solid-State Circuits</i> , 2015 , 50, 113-124	5.5		13
128	4-Camera VGA-resolution capsule endoscope with 80Mb/s body-channel communication transceiver and Sub-cm range capsule localization 2018,			13
127	The Hardware and Algorithm Co-Design for Energy-Efficient DNN Processor on Edge/Mobile Devices. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , 2020 , 67, 3458-3470	3.9		13
126	DT-CNN: Dilated and Transposed Convolution Neural Network Accelerator for Real-Time Image Segmentation on Mobile Devices 2019,			12
125	A 2.71 nJ/Pixel Gaze-Activated Object Recognition System for Low-Power Mobile Smart Glasses. <i>IEEE Journal of Solid-State Circuits</i> , 2016 , 51, 45-55	5.5		11
124	An 87- $\mu\text{A} \cdot \text{min}$ Iontophoresis Controller IC With Dual-Mode Impedance Sensor for Patch-Type Transdermal Drug Delivery System. <i>IEEE Journal of Solid-State Circuits</i> , 2014 , 49, 167-178	5.5		11
123	1.2-mW Online Learning Mixed-Mode Intelligent Inference Engine for Low-Power Real-Time Object Recognition Processor. <i>IEEE Transactions on Very Large Scale Integration (VLSI) Systems</i> , 2013 , 21, 921-933 ^{2,6}			11
122	A Low-Energy Inductive Coupling Transceiver With Cm-Range 50-Mbps Data Communication in Mobile Device Applications. <i>IEEE Journal of Solid-State Circuits</i> , 2010,	5.5		11
121	. <i>Proceedings of the IEEE</i> , 2020 , 108, 1245-1260	14.3		11

120	A 21mW low-power recurrent neural network accelerator with quantization tables for embedded deep learning applications 2017 ,		10
119	A Vocabulary Forest Object Matching Processor With 2.07 M-Vector/s Throughput and 13.3 nJ/Vector Per-Vector Energy for Full-HD 60 fps Video Object Recognition. <i>IEEE Journal of Solid-State Circuits</i> , 2015 , 50, 1059-1069	5.5	10
118	A 92-mW Real-Time Traffic Sign Recognition System With Robust Illumination Adaptation and Support Vector Machine. <i>IEEE Journal of Solid-State Circuits</i> , 2012 , 47, 2711-2723	5.5	10
117	Z-PIM: An Energy-Efficient Sparsity Aware Processing-In-Memory Architecture with Fully-Variable Weight Precision 2020 ,		10
116	HNPU: An Adaptive DNN Training Processor Utilizing Stochastic Dynamic Fixed-Point and Active Bit-Precision Searching. <i>IEEE Journal of Solid-State Circuits</i> , 2021 , 56, 2858-2869	5.5	10
115	A 17.5-fJ/bit Energy-Efficient Analog SRAM for Mixed-Signal Processing. <i>IEEE Transactions on Very Large Scale Integration (VLSI) Systems</i> , 2017 , 25, 2714-2723	2.6	9
114	A Power-Efficient CNN Accelerator With Similar Feature Skipping for Face Recognition in Mobile Devices. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , 2020 , 67, 1181-1193	3.9	9
113	A 82nW chaotic-map true random number generator based on sub-ranging SAR ADC 2016 ,		9
112	An ultra-low-power and mixed-mode event-driven face detection SoC for always-on mobile applications 2017 ,		9
111	A 57 mW 12.5 pJ/Epoch Embedded Mixed-Mode Neuro-Fuzzy Processor for Mobile Real-Time Object Recognition. <i>IEEE Journal of Solid-State Circuits</i> , 2013 , 48, 2894-2907	5.5	9
110	A 30fps stereo matching processor based on belief propagation with disparity-parallel PE array architecture 2010 ,		9
109	A Low Power 16-bit RISC with Lossless Compression Accelerator for Body Sensor Network System 2006 ,		9
108	SRNPU: An Energy-Efficient CNN-Based Super-Resolution Processor With Tile-Based Selective Super-Resolution in Mobile Devices. <i>IEEE Journal on Emerging and Selected Topics in Circuits and Systems</i> , 2020 , 10, 320-334	5.2	9
107	An Energy-Efficient Embedded Deep Neural Network Processor for High Speed Visual Attention in Mobile Vision Recognition SoC. <i>IEEE Journal of Solid-State Circuits</i> , 2016 , 1-9	5.5	9
106	A 27 mW Reconfigurable Marker-Less Logarithmic Camera Pose Estimation Engine for Mobile Augmented Reality Processor. <i>IEEE Journal of Solid-State Circuits</i> , 2015 , 50, 2513-2523	5.5	8
105	A 0.5-V Sub-10-mW 15.28-mV/Hz Bio-Impedance Sensor IC With Sub-1° Phase Error. <i>IEEE Journal of Solid-State Circuits</i> , 2020 , 55, 2161-2173	5.5	8
104	Design and implementation of CMOS LVDS 2.5 Gb/s transmitter and 1.3 Gb/s receiver for optical interconnections		8
103	A 146.52 TOPS/W Deep-Neural-Network Learning Processor with Stochastic Coarse-Fine Pruning and Adaptive Input/Output/Weight Skipping 2020 ,		8

102	DT-CNN: An Energy-Efficient Dilated and Transposed Convolutional Neural Network Processor for Region of Interest Based Image Segmentation. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , 2020 , 67, 3471-3483	3.9	7
101	An energy-efficient body channel communication based on Maxwell's equations analysis of on-body transmission mechanism 2012 ,		7
100	A 0.5- μW V_{rms} 12- μW W Wirelessly Powered Patch-Type Healthcare Sensor for Wearable Body Sensor Network. <i>IEEE Journal of Solid-State Circuits</i> , 2010 ,	5.5	7
99	Dual-V _T /self-timed CMOS logic for low subthreshold current multigigabit synchronous DRAM. <i>IEEE Transactions on Circuits and Systems Part 2: Express Briefs</i> , 1998 , 45, 1263-1271		7
98	Networks-on-chip and Networks-in-Package for High-Performance SoC Platforms 2005 ,		7
97	A 141.4 mW Low-Power Online Deep Neural Network Training Processor for Real-time Object Tracking in Mobile Devices 2018 ,		7
96	A 1GHz fault tolerant processor with dynamic lockstep and self-recovering cache for ADAS SoC complying with ISO26262 in automotive electronics 2017 ,		6
95	Intelligent Network-on-Chip With Online Reinforcement Learning for Portable HD Object Recognition Processor. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , 2014 , 61, 476-484	3.9	6
94	A 57mW embedded mixed-mode neuro-fuzzy accelerator for intelligent multi-core processor 2011 ,		6
93	A 195 mW, 9.1 MVertices/s Fully Programmable 3-D Graphics Processor for Low-Power Mobile Devices. <i>IEEE Journal of Solid-State Circuits</i> , 2008 , 43, 2370-2380	5.5	6
92	A low energy bio sensor node processor for continuous healthcare monitoring system 2008 ,		6
91	A small ripple regulated charge pump with automatic pumping control schemes		6
90	Wireless Body-Area-Network Transceiver and Low-Power Receiver With High Application Expandability. <i>IEEE Journal of Solid-State Circuits</i> , 2020 , 55, 2781-2789	5.5	6
89	A 1.02- μW STT-MRAM-Based DNN ECG Arrhythmia Monitoring SoC With Leakage-Based Delay MAC Unit. <i>IEEE Solid-State Circuits Letters</i> , 2020 , 3, 390-393	2	6
88	A 36-Channel Auto-Calibrated Front-End ASIC for a pMUT-Based Miniaturized 3-D Ultrasound System. <i>IEEE Journal of Solid-State Circuits</i> , 2021 , 56, 1910-1923	5.5	6
87	A multimodal drowsiness monitoring ear-module system with closed-loop real-time alarm 2016 ,		6
86	A 9.6 mW/Ch 10 MHz Wide-bandwidth Electrical Impedance Tomography IC with Accurate Phase Compensation for Breast Cancer Detection 2020 ,		5
85	A 20 μW contact impedance sensor for wireless body-area-network transceiver 2011 ,		5

84	A low energy crystal-less double-FSK transceiver for wireless body-area-network 2011 ,		5
83	Race logic architecture (RALA): a novel logic concept using the race scheme of input variables. <i>IEEE Journal of Solid-State Circuits</i> , 2002 , 37, 191-201	5.5	5
82	A 120-mW 3-D rendering engine with 6-Mb embedded DRAM and 3.2-GB/s runtime reconfigurable bus for PDA chip. <i>IEEE Journal of Solid-State Circuits</i> , 2002 , 37, 1352-1355	5.5	5
81	. <i>IEEE Journal of Solid-State Circuits</i> , 2021 , 56, 887-898	5.5	5
80	GANPU: An Energy-Efficient Multi-DNN Training Processor for GANs With Speculative Dual-Sparsity Exploitation. <i>IEEE Journal of Solid-State Circuits</i> , 2021 , 56, 2845-2857	5.5	5
79	An Energy-Efficient Deep Reinforcement Learning Accelerator With Transposable PE Array and Experience Compression. <i>IEEE Solid-State Circuits Letters</i> , 2019 , 2, 228-231	2	4
78	The Development of Silicon for AI: Different Design Approaches. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , 2020 , 67, 4719-4732	3.9	4
77	A 0.5 V 54 μW Ultra-Low-Power Object Matching Processor for Micro Air Vehicle Navigation. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , 2016 , 63, 359-369	3.9	4
76	CNNP-v2: An Energy Efficient Memory-Centric Convolutional Neural Network Processor Architecture 2019 ,		4
75	A 4.9 mW neural network task scheduler for congestion-minimized network-on-chip in multi-core systems 2014 ,		4
74	Intelligent NoC with neuro-fuzzy bandwidth regulation for a 51 IP object recognition processor 2010 ,		4
73	A 92mW real-time traffic sign recognition system with robust light and dark adaptation 2011 ,		4
72	A 152-mW Mobile Multimedia SoC With Fully Programmable 3-D Graphics and MPEG4/H.264/JPEG. <i>IEEE Transactions on Very Large Scale Integration (VLSI) Systems</i> , 2009 , 17, 1260-1266	2.6	4
71	A 231MHz, 2.18mW 32-bit Logarithmic Arithmetic Unit for Fixed-Point 3D Graphics System 2005 ,		4
70	A 670 ps, 64 bit dynamic low-power adder design		4
69	A 64.1mW Accurate Real-Time Visual Object Tracking Processor With Spatial Early Stopping on Siamese Network. <i>IEEE Transactions on Circuits and Systems II: Express Briefs</i> , 2021 , 68, 1675-1679	3.5	4
68	An energy-efficient deep learning processor with heterogeneous multi-core architecture for convolutional neural networks and recurrent neural networks 2017 ,		3
67	A 15.2 TOPS/W CNN Accelerator with Similar Feature Skipping for Face Recognition in Mobile Devices 2019 ,		3

66	A 1.15 TOPS/W Energy-Efficient Capsule Network Accelerator for Real-Time 3D Point Cloud Segmentation in Mobile Environment. <i>IEEE Transactions on Circuits and Systems II: Express Briefs</i> , 2020 , 67, 1594-1598	3.5	3
65	A 0.22W/89 mW Low-Power and Highly-Secure Always-On Face Recognition Processor With Adversarial Attack Prevention. <i>IEEE Transactions on Circuits and Systems II: Express Briefs</i> , 2020 , 67, 846-850	3.5	3
64	A CMOS Image Sensor-Based Stereo Matching Accelerator With Focal-Plane Sparse Rectification and Analog Census Transform. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , 2016 , 63, 2180-2188	3.9	3
63	A Fault-Tolerant Cache System of Automotive Vision Processor Complying With ISO26262. <i>IEEE Transactions on Circuits and Systems II: Express Briefs</i> , 2016 , 63, 1146-1150	3.5	3
62	A fabric wrist patch sensor for continuous and comprehensive monitoring of the cardiovascular system. <i>Annual International Conference of the IEEE Engineering in Medicine and Biology Society IEEE Engineering in Medicine and Biology Society Annual International Conference</i> , 2016 , 2016, 6070-6073	0.9	3
61	A 33W/node Duty Cycle Controlled HBC Transceiver system for medical BAN with 64 sensor nodes 2014 ,		3
60	An 1.61mW mixed-signal column processor for BRISK feature extraction in CMOS image sensor 2014 ,		3
59	Wireless fabric patch sensors for wearable healthcare. <i>Annual International Conference of the IEEE Engineering in Medicine and Biology Society IEEE Engineering in Medicine and Biology Society Annual International Conference</i> , 2010 , 2010, 5254-7	0.9	3
58	A 92m W 76.8GOPS vector matching processor with parallel Huffman decoder and query re-ordering buffer for real-time object recognition 2010 ,		3
57	A 76.8 GB/s 46 mW low-latency network-on-chip for real-time object recognition processor 2008 ,		3
56	The brain mimicking Visual Attention Engine: An 80W digital Cellular Neural Network for rapid global feature extraction 2008 ,		3
55	An arbitration look-ahead scheme for reducing end-to-end latency in networks on chip		3
54	Optimization of portable system architecture for real-time 3D graphics		3
53	A reconfigurable multilevel parallel texture cache memory with 75-GB/s parallel cache replacement bandwidth. <i>IEEE Journal of Solid-State Circuits</i> , 2002 , 37, 612-623	5.5	3
52	An Overview of Energy-Efficient Hardware Accelerators for On-Device Deep-Neural-Network Training. <i>IEEE Open Journal of the Solid-State Circuits Society</i> , 2021 , 1, 115-128		3
51	A 0.5W/10 mW CMOS Image Sensor-Based Gaze Estimation Processor. <i>IEEE Journal of Solid-State Circuits</i> , 2016 , 51, 1032-1040	5.5	3
50	CNNP-v2: A Memory-Centric Architecture for Low-Power CNN Processor on Domain-Specific Mobile Devices. <i>IEEE Journal on Emerging and Selected Topics in Circuits and Systems</i> , 2019 , 9, 598-611	5.2	3
49	Simultaneous Electrical Bio-Impedance Plethysmography at Different Body Parts: Continuous and Non-Invasive Monitoring of Pulse Wave Velocity. <i>IEEE Transactions on Biomedical Circuits and Systems</i> , 2021 , 15, 1027-1038	5.1	3

48	A 0.54-mW duty controlled RSSI with current reusing technique for human body communication 2015 ,		2
47	A 540-[Formula: see text] Duty Controlled RSSI With Current Reusing Technique for Human Body Communication. <i>IEEE Transactions on Biomedical Circuits and Systems</i> , 2016 , 10, 893-901	5.1	2
46	Custom Sub-Systems and Circuits for Deep Learning: Guest Editorial Overview. <i>IEEE Journal on Emerging and Selected Topics in Circuits and Systems</i> , 2019 , 9, 247-252	5.2	2
45	3.8 mW electrocardiogram (ECG) filtered electrical impedance tomography IC using I/Q homodyne architecture for breast cancer diagnosis 2014 ,		2
44	A 31.2pJ/disparity-pixel stereo matching processor with stereo SRAM for mobile UI application 2017 ,		2
43	Intelligent task scheduler with high throughput NoC for real-time mobile object recognition SoC 2015 ,		2
42	A 46 μ W motion artifact reduction bio-signal sensor with ICA based adaptive DC level control for sleep monitoring system 2012 ,		2
41	A low-noise folded bit-line sensing architecture for multigigabit DRAM with ultrahigh-density 6F/sup 2/ cell [CMOS design]. <i>IEEE Journal of Solid-State Circuits</i> , 1998 , 33, 1096-1102	5.5	2
40	Diffusion of zinc into GaAs through Al _{0.3} Ga _{0.7} As. <i>Journal of Electronic Materials</i> , 1988 , 17, 337-339	1.9	2
39	An Overview of Sparsity Exploitation in CNNs for On-Device Intelligence With Software-Hardware Cross-Layer Optimizations. <i>IEEE Journal on Emerging and Selected Topics in Circuits and Systems</i> , 2021 , 11, 634-648	5.2	2
38	A 4.45 ms Low-Latency 3D Point-Cloud-Based Neural Network Processor for Hand Pose Estimation in Immersive Wearable Devices 2020 ,		2
37	DF-LNPU: A Pipelined Direct Feedback Alignment-Based Deep Neural Network Learning Processor for Fast Online Learning. <i>IEEE Journal of Solid-State Circuits</i> , 2021 , 56, 1630-1640	5.5	2
36	A 43.7 mW 94 fps CMOS image sensor-based stereo matching accelerator with focal-plane rectification and analog census transformation 2016 ,		2
35	A 635 μ W non-contact compensation IC for body channel communication 2016 ,		2
34	30-fps SNR equalized electrical impedance tomography IC with fast-settle filter and adaptive current control for lung monitoring 2016 ,		2
33	Direct Feedback Alignment Based Convolutional Neural Network Training for Low-Power Online Learning Processor 2019 ,		2
32	An 802.15.6 HBC Standard Compatible Transceiver and 90 pJ/b Full-Duplex Transceiver for Body Channel Communication 2019 ,		2
31	ECIM: Exponent Computing in Memory for an Energy Efficient Heterogeneous Floating-Point DNN Training Processor. <i>IEEE Micro</i> , 2021 , 1-1	1.8	2

30	. <i>IEEE Solid-State Circuits Letters</i> , 2021 , 4, 22-25	2	2
29	An Energy-Efficient GAN Accelerator With On-Chip Training for Domain-Specific Optimization. <i>IEEE Journal of Solid-State Circuits</i> , 2021 , 56, 2968-2980	5.5	2
28	K-glass: Real-time markerless augmented reality smart glasses platform 2015 ,		1
27	A keypoint-level parallel pipelined object recognition processor with gaze activation image sensor for mobile smart glasses system 2015 ,		1
26	A 0.53mW ultra-low-power 3D face frontalization processor for face recognition with human-level accuracy in wearable devices 2017 ,		1
25	A 46.1 fps Global Matching Optical Flow Estimation Processor for Action Recognition in Mobile Devices 2018 ,		1
24	Understanding Body Channel Communication : A review: from history to the future applications 2019 ,		1
23	A 1.5nJ/pixel super-resolution enhanced FAST corner detection processor for high accuracy AR 2014 ,		1
22	A multimodal headpatch system for patient brain monitoring in OR and PACU 2017 ,		1
21	A 590MDE/s semi-global matching processor with lossless data compression 2017 ,		1
20	79pJ/b 80Mb/s full-duplex transceiver and 42.5jW 100kb/s super-regenerative transceiver for body channel communication 2015 ,		1
19	Energy-efficient Mixed-mode support vector machine processor with analog Gaussian kernel 2014 ,		1
18	Online Reinforcement Learning NoC for portable HD object recognition processor 2012 ,		1
17	A high-throughput 16 \times super resolution processor for real-time object recognition SoC 2013 ,		1
16	A wirelessly-powered electro-acupuncture based on Adaptive Pulse Width Mono-Phase stimulation 2010 ,		1
15	Live demonstration: A real-time compensated inductive transceiver for wearable MP3 player system on multi-layered planar fashionable circuit board 2010 ,		1
14	An asynchronous mixed-mode neuro-fuzzy controller for energy efficient machine intelligence SoC 2011 ,		1
13	A 145 μ W 88 parallel multiplier based on optimized bypassing architecture 2011 ,		1

12	A 54.7 fps 3D Point Cloud Semantic Segmentation Processor with Sparse Grouping Based Dilated Graph Convolutional Network for Mobile Devices 2020 ,		1
11	A Pipelined Point Cloud Based Neural Network Processor for 3-D Vision With Large-Scale Max Pooling Layer Prediction. <i>IEEE Journal of Solid-State Circuits</i> , 2021 , 1-1	5.5	1
10	Design of Sub-10-W Sub-0.1% THD Sinusoidal Current Generator IC for Bio-Impedance Sensing. <i>IEEE Journal of Solid-State Circuits</i> , 2021 , 1-1	5.5	1
9	A 49.5 mW Multi-scale Linear Quantized Online Learning Processor for Real-Time Adaptive Object Detection. <i>IEEE Transactions on Circuits and Systems II: Express Briefs</i> , 2022 , 1-1	3.5	1
8	A Low-Power Graph Convolutional Network Processor With Sparse Grouping for 3D Point Cloud Semantic Segmentation in Mobile Devices. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , 2022 , 1-12	3.9	0
7	. <i>IEEE Transactions on Circuits and Systems II: Express Briefs</i> , 2021 , 68, 1700-1704	3.5	0
6	A 43.1TOPS/W Energy-Efficient Absolute-Difference-Accumulation Operation Computing-In-Memory With Computation Reuse. <i>IEEE Transactions on Circuits and Systems II: Express Briefs</i> , 2021 , 68, 1605-1609	3.5	0
5	A 36.2 dB High SNR and PVT/Leakage-robust eDRAM Computing-In-Memory Macro with Segmented BL and Reference Cell Array. <i>IEEE Transactions on Circuits and Systems II: Express Briefs</i> , 2022 , 1-1	3.5	0
4	OmniDRL: An Energy-Efficient Deep Reinforcement Learning Processor With Dual-Mode Weight Compression and Sparse Weight Transposer. <i>IEEE Journal of Solid-State Circuits</i> , 2022 , 1-1	5.5	
3	TSUNAMI: Triple Sparsity-Aware Ultra Energy-Efficient Neural Network Training Accelerator With Multi-Modal Iterative Pruning. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , 2022 , 1-13	3.9	
2	A Mobile DNN Training Processor with Automatic Bit-precision Search and Fine-grained Sparsity Exploitation. <i>IEEE Micro</i> , 2021 , 1-1	1.8	
1	Circuits and Systems for Wireless Body Area Network 2016 , 375-403		