

Ryutaro Doi

List of Publications by Year in descending order

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6
papers

25
citations

2682572

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2917675

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all docs

6
docs citations

6
times ranked

13
citing authors

#	ARTICLE	IF	CITATIONS
1	Sneak Path Free Reconfiguration With Minimized Programming Steps for Via-Switch Crossbar-Based FPGA. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 2020, 39, 2572-2587.	2.7	5
2	A Fault Detection and Diagnosis Method for Via-Switch Crossbar in Non-Volatile FPGA. IEICE Transactions on Fundamentals of Electronics, Communications and Computer Sciences, 2020, E103.A, 1447-1455.	0.3	1
3	Fault Diagnosis of Via-Switch Crossbar in Non-volatile FPGA. , 2020, , .		2
4	Via-Switch FPGA: Highly Dense Mixed-Grained Reconfigurable Architecture With Overlay Via-Switch Crossbars. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2018, 26, 2723-2736.	3.1	13
5	Sneak path free reconfiguration of via-switch crossbars based FPGA. , 2018, , .		3
6	Via-switch FPGA with transistor-free programmability enabling energy-efficient near-memory parallel computation. Japanese Journal of Applied Physics, 0, , .	1.5	1