Ryutaro Doi

List of Publications by Year
in descending order

6 papers	25 citations	2682572 2 h-index	2917675 2 g-index
6 all docs	6 docs citations	6 times ranked	13 citing authors

#	Article	IF	Citations
1	Via-Switch FPGA: Highly Dense Mixed-Grained Reconfigurable Architecture With Overlay Via-Switch Crossbars. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2018, 26, 2723-2736.	3.1	13
2	Sneak Path Free Reconfiguration With Minimized Programming Steps for Via-Switch Crossbar-Based FPGA. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 2020, 39, 2572-2587.	2.7	5
3	Sneak path free reconfiguration of via-switch crossbars based FPGA. , 2018, , .		3
4	Fault Diagnosis of Via-Switch Crossbar in Non-volatile FPGA. , 2020, , .		2
5	A Fault Detection and Diagnosis Method for Via-Switch Crossbar in Non-Volatile FPGA. IEICE Transactions on Fundamentals of Electronics, Communications and Computer Sciences, 2020, E103.A, 1447-1455.	0.3	1
6	Via-switch FPGA with transistor-free programmability enabling energy-efficient near-memory parallel computation. Japanese Journal of Applied Physics, 0, , .	1.5	1