

Xinhua Wang

List of Publications by Year in descending order

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Version: 2024-02-01

41
papers

857
citations

430874

18
h-index

501196

28
g-index

41
all docs

41
docs citations

41
times ranked

718
citing authors

#	ARTICLE	IF	CITATIONS
1	Ultrathin-Barrier AlGaIn/GaN Heterostructure: A Recess-Free Technology for Manufacturing High-Performance GaN-on-Si Power Devices. IEEE Transactions on Electron Devices, 2018, 65, 207-214.	3.0	87
2	High Uniformity Normally-OFF GaN MIS-HEMTs Fabricated on Ultra-Thin-Barrier AlGaIn/GaN Heterostructure. IEEE Electron Device Letters, 2016, 37, 1617-1620.	3.9	72
3	A 5.8-GHz High-Power and High-Efficiency Rectifier Circuit With Lateral GaN Schottky Diode for Wireless Power Transfer. IEEE Transactions on Power Electronics, 2020, 35, 2247-2252.	7.9	60
4	Robust SiN ₂ /AlGaIn Interface in GaN HEMTs Passivated by Thick LPCVD-Grown SiN ₂ Layer. IEEE Electron Device Letters, 2015, 36, 666-668.	3.9	58
5	O ₃ -sourced atomic layer deposition of high quality Al ₂ O ₃ gate dielectric for normally-off GaN metal-insulator-semiconductor high-electron-mobility transistors. Applied Physics Letters, 2015, 106, .	3.3	58
6	Effect of GaN Channel Layer Thickness on DC and RF Performance of GaN HEMTs With Composite AlGaIn/GaN Buffer. IEEE Transactions on Electron Devices, 2014, 61, 1341-1346.	3.0	55
7	High RF Performance Enhancement-Mode Al ₂ O ₃ /AlGaIn MIS-HEMTs Fabricated With High-Temperature Gate-Recess Technique. IEEE Electron Device Letters, 2015, 36, 754-756.	3.9	49
8	Ultralow-Contact-Resistance Au-Free Ohmic Contacts With Low Annealing Temperature on AlGaIn/GaN Heterostructures. IEEE Electron Device Letters, 2018, 39, 847-850.	3.9	42
9	Millimeter-Wave AlGaIn/GaN HEMTs With 43.6% Power-Added-Efficiency at 40 GHz Fabricated by Atomic Layer Etching Gate Recess. IEEE Electron Device Letters, 2020, 41, 701-704.	3.9	31
10	Investigation of the interface between LPCVD-SiN _x gate dielectric and III-nitride for AlGaIn/GaN MIS-HEMTs. Journal of Vacuum Science and Technology B: Nanotechnology and Microelectronics, 2016, 34, .	1.2	29
11	Insight into the Near-Conduction Band States at the Crystallized Interface between GaN and SiN _x Grown by Low-Pressure Chemical Vapor Deposition. ACS Applied Materials & Interfaces, 2018, 10, 21721-21729.	8.0	24
12	Capture and emission mechanisms of defect states at interface between nitride semiconductor and gate oxides in GaN-based metal-oxide-semiconductor power transistors. Journal of Applied Physics, 2019, 126, .	2.5	24
13	Suppression of Gate Leakage Current in <i>Ka</i> -Band AlGaIn/GaN HEMT With 5-nm SiN Gate Dielectric Grown by Plasma-Enhanced ALD. IEEE Transactions on Electron Devices, 2021, 68, 49-52.	3.0	22
14	Suppression of interface states between nitride-based gate dielectrics and ultrathin-barrier AlGaIn/GaN heterostructure with <i>in situ</i> remote plasma pretreatments. Applied Physics Letters, 2021, 118, .	3.3	22
15	High-Temperature-Recessed Millimeter-Wave AlGaIn/GaN HEMTs With 42.8% Power-Added-Efficiency at 35 GHz. IEEE Electron Device Letters, 2018, 39, 727-730.	3.9	21
16	Monolithic integration of E/D-mode GaN MIS-HEMTs on ultrathin-barrier AlGaIn/GaN heterostructure on Si substrates. Applied Physics Express, 2019, 12, 024001.	2.4	21
17	Interface charge engineering in down-scaled AlGaIn (<math>\approx 6\text{\%}</math>nm)/GaN heterostructure for fabrication of GaN-based power HEMTs and MIS-HEMTs. Applied Physics Letters, 2020, 116, .	3.3	20
18	Effect of interface and bulk traps on the <i>Câ€“V</i> characterization of a LPCVD-SiN _x /AlGaIn/GaN metal-insulator-semiconductor structure. Semiconductor Science and Technology, 2016, 31, 065014.	2.0	19

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19	Recess-free AlGaIn/GaN lateral Schottky barrier controlled Schottky rectifier with low turn-on voltage and high reverse blocking. , 2018, , .		19
20	An Enhancement-Mode GaN p-FET With Improved Breakdown Voltage. IEEE Electron Device Letters, 2022, 43, 1191-1194.	3.9	14
21	Suppression and characterization of interface states at low-pressure-chemical-vapor-deposited SiN _x /III-nitride heterostructures. Applied Surface Science, 2021, 542, 148530.	6.1	13
22	Identification of Semi-ON-State Current Collapse in AlGaIn/GaN HEMTs by Drain Current Deep Level Transient Spectroscopy. IEEE Electron Device Letters, 2022, 43, 200-203.	3.9	11
23	Effects of Fluorine Plasma Treatment on Au-Free Ohmic Contacts to Ultrathin-Barrier AlGaIn/GaN Heterostructure. IEEE Transactions on Electron Devices, 2019, 66, 2932-2936.	3.0	9
24	Interface Charge Effects on 2-D Electron Gas in Vertical-Scaled Ultrathin-Barrier AlGaIn/GaN Heterostructure. IEEE Transactions on Electron Devices, 2021, 68, 36-41.	3.0	9
25	7.05 W/mm Power Density Millimeter-Wave GaN MIS-HEMT With Plasma Enhanced Atomic Layer Deposition SiN ₂ Dielectric Layer. IEEE Electron Device Letters, 2021, 42, 1436-1439.	3.9	8
26	Effect of alloying temperature on the capacitance-voltage and current-voltage characteristics of low-pressure chemical vapor deposition SiN _x /GaN MIS structures. Physica Status Solidi (A) Applications and Materials Science, 2015, 212, 2928-2935.	1.8	7
27	Low-thermal-budget Au-free ohmic contact to an ultrathin barrier AlGaIn/GaN heterostructure utilizing a micro-patterned ohmic recess. Journal of Semiconductors, 2021, 42, 092801.	3.7	7
28	Identification of bulk and interface state-induced threshold voltage instability in metal/SiN _x (insulator)/AlGaIn/GaN high-electron-mobility transistors using deep-level transient spectroscopy. Applied Physics Letters, 2021, 119, .	3.3	7
29	An ultrathin-barrier AlGaIn/GaN heterostructure: a recess-free technology for the fabrication and integration of GaN-based power devices and power-driven circuits. Semiconductor Science and Technology, 2021, 36, 044002.	2.0	6
30	Instability of parasitic capacitance in T-shape-gate enhancement-mode AlGaIn/GaN MIS-HEMTs. Journal of Semiconductors, 2022, 43, 032801.	3.7	6
31	A large-signal Pspice modeling of GaN-based MIS-HEMTs. Superlattices and Microstructures, 2019, 130, 499-511.	3.1	4
32	Impact of V_{th} Instability on Time-Resolved Characteristics of MIS-HEMT-Based GaN Power IC. IEEE Electron Device Letters, 2021, 42, 1440-1443.	3.9	4
33	Evolution of traps in TiN/O ₃ -sourced Al ₂ O ₃ /GaN gate structures with thermal annealing temperature. Journal of Vacuum Science and Technology B: Nanotechnology and Microelectronics, 2018, 36, 022202.	1.2	3
34	Revealing the Positive Bias Temperature Instability in Normally-OFF AlGaIn/GaN MIS-HFETs by Constant-Capacitance DLTS. , 2019, , .		3
35	Partially Crystallized Ultrathin Interfaces between GaN and SiN _x Grown by Low-Pressure Chemical Vapor Deposition and Interface Editing. ACS Applied Materials & Interfaces, 2021, 13, 7725-7734.	8.0	3
36	Monolithic Integrated Normally OFF GaN Power Device With Antiparallel Lateral Schottky Barrier Controlled Schottky Rectifier. IEEE Transactions on Electron Devices, 2021, 68, 1778-1783.	3.0	3

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37	Evolution of Deep Traps in GaN-Based RF High Electron Mobility Transistors under High Voltage OFF-State Stress. <i>Physica Status Solidi - Rapid Research Letters</i> , 2022, 16, .	2.4	3
38	Investigation of current collapse mechanism of LPCVD Si ₃ N ₄ passivated AlGaIn/GaN HEMTs by fast soft-switched current-DLTS and CC-DLTS. , 2017, , .		2
39	Implementation of RTCVD-SiN _x Gate Dielectric Into Enhancement-Mode GaN MIS-HEMTs Fabricated on Ultrathin-Barrier AlGaIn/GaN-on-Si Platform. <i>IEEE Transactions on Electron Devices</i> , 2021, 68, 4274-4277.	3.0	1
40	Investigation of Dynamic-Q _{GD} on Enhancement-Mode AlGaIn/GaN MIS-HEMTs with SiN _x Passivation Dielectric. , 2022, , .		1
41	Ultrathin-barrier AlGaIn/GaN heterostructure: An AlGaIn-recess-free technology for fabrication of lateral GaN-based power devices. , 2022, , .		0