

Debajit Bhattacharya

List of Publications by Year in descending order

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Version: 2024-02-01

6
papers

185
citations

2258059

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h-index

2053705

5
g-index

6
all docs

6
docs citations

6
times ranked

163
citing authors

#	ARTICLE	IF	CITATIONS
1	Analytical Modeling of the SMART NoC. IEEE Transactions on Multi-Scale Computing Systems, 2017, 3, 242-254.	2.4	3
2	Ultra-High Density Monolithic 3-D FinFET SRAM With Enhanced Read Stability. IEEE Transactions on Circuits and Systems I: Regular Papers, 2016, 63, 1176-1187.	5.4	14
3	TCAD-Assisted Capacitance Extraction of FinFET SRAM and Logic Arrays. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2016, 24, 329-333.	3.1	3
4	Design of Efficient Content Addressable Memories in High-Performance FinFET Technology. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2015, 23, 963-967.	3.1	16
5	FinFETs: From Devices to Architectures. Advances in Electronics, 2014, 2014, 1-21.	1.9	137
6	FinFETs: from devices to architectures. , 0, , 21-55.		12