

# Debajit Bhattacharya

## List of Publications by Year in descending order

Source: <https://exaly.com/author-pdf/5519584/publications.pdf>

Version: 2024-02-01

6  
papers

185  
citations

2258059

3  
h-index

2053705

5  
g-index

6  
all docs

6  
docs citations

6  
times ranked

163  
citing authors

#	ARTICLE	IF	CITATIONS
1	FinFETs: From Devices to Architectures. <i>Advances in Electronics</i> , 2014, 2014, 1-21.	1.9	137
2	Design of Efficient Content Addressable Memories in High-Performance FinFET Technology. <i>IEEE Transactions on Very Large Scale Integration (VLSI) Systems</i> , 2015, 23, 963-967.	3.1	16
3	Ultra-High Density Monolithic 3-D FinFET SRAM With Enhanced Read Stability. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , 2016, 63, 1176-1187.	5.4	14
4	FinFETs: from devices to architectures. , 0, , 21-55.		12
5	TCAD-Assisted Capacitance Extraction of FinFET SRAM and Logic Arrays. <i>IEEE Transactions on Very Large Scale Integration (VLSI) Systems</i> , 2016, 24, 329-333.	3.1	3
6	Analytical Modeling of the SMART NoC. <i>IEEE Transactions on Multi-Scale Computing Systems</i> , 2017, 3, 242-254.	2.4	3