

# Shubham Rai

## List of Publications by Year in descending order

Source: <https://exaly.com/author-pdf/5517049/publications.pdf>

Version: 2024-02-01

12  
papers

137  
citations

2258059

3  
h-index

2053705

5  
g-index

12  
all docs

12  
docs citations

12  
times ranked

86  
citing authors

#	ARTICLE	IF	CITATIONS
1	Utilizing XMG-Based Synthesis to Preserve Self-Duality for RFET-Based Circuits. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 2023, 42, 914-927.	2.7	3
2	Improving Technology Mapping for And-Inverter-Cones. , 2022, , .		0
3	A Survey of FPGA Logic Cell Designs in the Light of Emerging Technologies. IEEE Access, 2021, 9, 91564-91574.	4.2	6
4	Efficient Privacy-Aware Federated Learning by Elimination of Downstream Redundancy. IEEE Design and Test, 2021, , 1-1.	1.2	1
5	Preserving Self-Duality During Logic Synthesis for Emerging Reconfigurable Nanotechnologies. , 2021, , .		6
6	Perspectives on Emerging Computation-in-Memory Paradigms. , 2021, , .		9
7	Correction to "RECON: Resource-Efficient CORDIC-Based Neuron Architecture" IEEE Open Journal of Circuits and Systems, 2021, 2, 292-292.	1.9	0
8	RECON: Resource-Efficient CORDIC-Based Neuron Architecture. IEEE Open Journal of Circuits and Systems, 2021, 2, 170-181.	1.9	20
9	Metastability with Emerging Reconfigurable Transistors: Exploiting Ambipolarity for Throughput. , 2021, , .		2
10	Exploring Physical Synthesis for Circuits based on Emerging Reconfigurable Nanotechnologies. , 2021, , .		2
11	Designing Efficient Circuits Based on Runtime-Reconfigurable Field-Effect Transistors. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2019, 27, 560-572.	3.1	64
12	A physical synthesis flow for early technology evaluation of silicon nanowire based reconfigurable FETs. , 2018, , .		24