Shubham Rai

List of Publications by Year in descending order

Source: https://exaly.com/author-pdf/5517049/publications.pdf

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		2258059	2053705	
12	137	3	5	
papers	citations	h-index	g-index	
12	12	12	86	
all docs	docs citations	times ranked	citing authors	

#	Article	IF	CITATIONS
1	Designing Efficient Circuits Based on Runtime-Reconfigurable Field-Effect Transistors. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2019, 27, 560-572.	3.1	64
2	A physical synthesis flow for early technology evaluation of silicon nanowire based reconfigurable FETs. , $2018, , .$		24
3	RECON: Resource-Efficient CORDIC-Based Neuron Architecture. IEEE Open Journal of Circuits and Systems, 2021, 2, 170-181.	1.9	20
4	Perspectives on Emerging Computation-in-Memory Paradigms. , 2021, , .		9
5	A Survey of FPGA Logic Cell Designs in the Light of Emerging Technologies. IEEE Access, 2021, 9, 91564-91574.	4.2	6
6	Preserving Self-Duality During Logic Synthesis for Emerging Reconfigurable Nanotechnologies. , 2021, , .		6
7	Utilizing XMG-Based Synthesis to Preserve Self-Duality for RFET-Based Circuits. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 2023, 42, 914-927.	2.7	3
8	Metastability with Emerging Reconfigurable Transistors: Exploiting Ambipolarity for Throughput. , 2021, , .		2
9	Exploring Physical Synthesis for Circuits based on Emerging Reconfigurable Nanotechnologies. , 2021, , .		2
10	Efficient Privacy-Aware Federated Learning by Elimination of Downstream Redundancy. IEEE Design and Test, 2021, , 1-1.	1.2	1
11	Correction to "RECON: Resource-Efficient CORDIC-Based Neuron Architecture― IEEE Open Journal of Circuits and Systems, 2021, 2, 292-292.	1.9	0
12	Improving Technology Mapping for And-Inverter-Cones. , 2022, , .		0