

Il-Min Yi

List of Publications by Year in descending order

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#	ARTICLE	IF	CITATIONS
1	A 4-GS/s 11.3-mW 7-bit Time-Based ADC With Folding Voltage-to-Time Converter and Pipelined TDC in 65-nm CMOS. IEEE Journal of Solid-State Circuits, 2021, 56, 465-475.	5.4	19
2	A 15.1-mW 6-GS/s 6-bit Single-Channel Flash ADC With Selectively Activated 8 μ s— Time-Domain Latch Interpolation. IEEE Journal of Solid-State Circuits, 2021, 56, 455-464.	5.4	14
3	A Time-Based Receiver With 2-Tap Decision Feedback Equalizer for Single-Ended Mobile DRAM Interface. IEEE Journal of Solid-State Circuits, 2018, 53, 144-154.	5.4	13
4	A 15.1-mW 6-GS/s 6-bit Flash ADC with Selectively Activated 8 μ s— Time-Domain Interpolation. , 2018, , .		7