

# Rajesh K Gupta

## List of Publications by Citations

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The third column is the impact factor (IF) of the journal, and the fourth column is the number of citations of the article.

38  
papers

885  
citations

16  
h-index

29  
g-index

45  
ext. papers

1,137  
ext. citations

3.5  
avg, IF

4.29  
L-index

#	Paper	IF	Citations
38	Moneta: A High-Performance Storage Array Architecture for Next-Generation, Non-volatile Memories <b>2010</b> ,		163
37	Optimal Speed Control of Mobile Node for Data Collection in Sensor Networks. <i>IEEE Transactions on Mobile Computing</i> , <b>2010</b> , 9, 127-139	4.6	106
36	Underdesigned and Opportunistic Computing in Presence of Hardware Variability. <i>IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems</i> , <b>2013</b> , 32, 8-23	2.5	88
35	Understanding the Impact of Emerging Non-Volatile Memories on High-Performance, IO-Intensive Computing <b>2010</b> ,		80
34	Brick : Metadata schema for portable smart building applications. <i>Applied Energy</i> , <b>2018</b> , 226, 1273-1292	10.7	60
33	SnaPEA: Predictive Early Activation for Reducing Computation in Deep Convolutional Neural Networks <b>2018</b> ,		60
32	Translation Validation of High-Level Synthesis. <i>IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems</i> , <b>2010</b> , 29, 566-579	2.5	35
31	Spatial Memoization: Concurrent Instruction Reuse to Correct Timing Errors in SIMD Architectures. <i>IEEE Transactions on Circuits and Systems II: Express Briefs</i> , <b>2013</b> , 60, 847-851	3.5	28
30	Application-Adaptive Guardbanding to Mitigate Static and Dynamic Variability. <i>IEEE Transactions on Computers</i> , <b>2014</b> , 63, 2160-2173	2.5	26
29	. <i>Proceedings of the IEEE</i> , <b>2016</b> , 104, 1410-1448	14.3	25
28	Energy-efficient neural networks using approximate computation reuse <b>2018</b> ,		22
27	Sensor localization with deterministic accuracy guarantee <b>2011</b> ,		19
26	Analysis of instruction-level vulnerability to dynamic voltage and temperature variations <b>2012</b> ,		18
25	CLIM: A Cross-Level Workload-Aware Timing Error Prediction Model for Functional Units. <i>IEEE Transactions on Computers</i> , <b>2018</b> , 67, 771-783	2.5	16
24	QoS-Aware Scheduling of Heterogeneous Servers for Inference in Deep Neural Networks <b>2017</b> ,		16
23	Associative Memristive Memory for Approximate Computing in GPUs. <i>IEEE Journal on Emerging and Selected Topics in Circuits and Systems</i> , <b>2016</b> , 6, 222-234	5.2	16
22	A variability-aware OpenMP environment for efficient execution of accuracy-configurable computation on shared-FPU processor clusters <b>2013</b> ,		15

21	SLoT: A supervised learning model to predict dynamic timing errors of functional units <b>2017</b> ,		15
20	Binarized Convolutional Neural Networks with Separable Filters for Efficient Hardware Acceleration <b>2017</b> ,		11
19	A Wearable, Extensible, Open-Source Platform for Hearing Healthcare Research. <i>IEEE Access</i> , <b>2019</b> , 7, 162083-162101	3.5	7
18	Formalizing Tag-Based Metadata With the Brick Ontology. <i>Frontiers in Built Environment</i> , <b>2020</b> , 6,	2.2	7
17	Supervised learning based model for predicting variability-induced timing errors <b>2015</b> ,		6
16	CIRCA-GPUs: Increasing Instruction Reuse Through Inexact Computing in GP-GPUs. <i>IEEE Design and Test</i> , <b>2016</b> , 33, 85-92	1.4	6
15	Serving deep neural networks at the cloud edge for vision applications on mobile platforms <b>2019</b> ,		5
14	Improving Resilience to Timing Errors by Exposing Variability Effects to Software in Tightly-Coupled Processor Clusters. <i>IEEE Journal on Emerging and Selected Topics in Circuits and Systems</i> , <b>2014</b> , 4, 216-229	5.2	5
13	Multi-tenant mobile offloading systems for real-time computer vision applications <b>2019</b> ,		5
12	Accelerating Local Binary Pattern Networks with Software-Programmable FPGAs <b>2019</b> ,		4
11	Beyond a House of Sticks <b>2019</b> ,		4
10	ACES. <i>ACM Transactions on Sensor Networks</i> , <b>2020</b> , 16, 1-31	2.9	4
9	Variability Expeditions: A Retrospective. <i>IEEE Design and Test</i> , <b>2019</b> , 36, 65-67	1.4	2
8	Hardware/Software Codesign for Energy Efficiency and Robustness: From Error-Tolerant Computing to Approximate Computing. <i>Embedded Systems</i> , <b>2021</b> , 527-543		2
7	Critical Risk Indicators (CRIs) for the electric power grid: a survey and discussion of interconnected effects. <i>Environment Systems and Decisions</i> , <b>2021</b> , 1-22	4.1	2
6	Mitigating multi-tenant interference on mobile offloading servers <b>2017</b> ,		1
5	Underdesigned and Opportunistic Computing <b>2011</b> ,		1
4	Synthesis and Optimization of Combinational Interface Circuits. <i>Journal of Signal Processing Systems</i> , <b>2002</b> , 31, 243-261		1

3	Mitigating Multi-tenant Interference in Continuous Mobile Offloading. <i>Lecture Notes in Computer Science</i> , <b>2018</b> , 20-36	0.9	1
2	Performance Analysis of Timing-Speculative Processors. <i>IEEE Transactions on Computers</i> , <b>2021</b> , 1-1	2.5	0
1	Spatial and Temporal Memoization <b>2017</b> , 181-190		