Jigang Wu

List of Publications by Year in Descending Order

Source: https://exaly.com/author-pdf/5492542/jigang-wu-publications-by-year.pdf

Version: 2024-04-25

This document has been generated based on the publications and citations recorded by exaly.com. For the latest version of this publication list, visit the link given above.

The third column is the impact factor (IF) of the journal, and the fourth column is the number of citations of the article.

89	1,079	16	30
papers	citations	h-index	g-index
111	1,467 ext. citations	3.9	5.26
ext. papers		avg, IF	L-index

#	Paper	IF	Citations
89	Exploring Fine-grained Cluster Structure Knowledge for Unsupervised Domain Adaptation. <i>IEEE Transactions on Circuits and Systems for Video Technology</i> , 2022 , 1-1	6.4	1
88	Reconfiguration algorithms for synchronous communication on switch based degradable arrays. <i>Parallel Computing</i> , 2022 , 111, 102901	1	
87	Continuous Influence-based Community Partition for Social Networks. <i>IEEE Transactions on Network Science and Engineering</i> , 2021 , 1-1	4.9	3
86	Generalized Multi-view Collaborative Subspace Clustering. <i>IEEE Transactions on Circuits and Systems for Video Technology</i> , 2021 , 1-1	6.4	О
85	Task Offloading Algorithms for Novel Load Balancing in Homogeneous Fog Network 2021 ,		2
84	Context switch cost aware joint task merging and scheduling for deep learning applications. <i>Parallel Computing</i> , 2021 , 102, 102753	1	1
83	Combinatorial Double Auction for Resource Allocation in Mobile Blockchain Network. <i>Wireless Networks</i> , 2021 , 27, 3299-3312	2.5	
82	Defect Analysis and Parallel Testing for 3D Hybrid CMOS-Memristor Memory. <i>IEEE Transactions on Emerging Topics in Computing</i> , 2021 , 9, 745-758	4.1	2
81	Fog Computing Model and Efficient Algorithms for Directional Vehicle Mobility in Vehicular Network. <i>IEEE Transactions on Intelligent Transportation Systems</i> , 2021 , 22, 2599-2614	6.1	11
80	Coupled Knowledge Transfer for Visual Data Recognition. <i>IEEE Transactions on Circuits and Systems for Video Technology</i> , 2021 , 31, 1776-1789	6.4	5
79	Feature-transfer network and local background suppression for microaneurysm detection. <i>Machine Vision and Applications</i> , 2021 , 32, 1	2.8	19
78	Asymmetric Supervised Consistent and Specific Hashing for Cross-Modal Retrieval. <i>IEEE Transactions on Image Processing</i> , 2021 , 30, 986-1000	8.7	10
77	Long-term optimization for MEC-enabled HetNets with deviceBdgeEloud collaboration. <i>Computer Communications</i> , 2021 , 166, 66-80	5.1	3
76	. IEEE Transactions on Multimedia, 2021 , 23, 2930-2942	6.6	18
75	Load Balance Guaranteed Vehicle-to-Vehicle Computation Offloading for Min-Max Fairness in VANETs. <i>IEEE Transactions on Intelligent Transportation Systems</i> , 2021 , 1-20	6.1	O
74	Fault Modeling and Efficient Testing of Memristor-Based Memory. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , 2021 , 1-12	3.9	3
73	Blockchain-based Secure Key Management for Mobile Edge Computing. <i>IEEE Transactions on Mobile Computing</i> , 2021 , 1-1	4.6	4

(2020-2021)

72	Search-free Inference Acceleration for Sparse Convolutional Neural Networks. <i>IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems</i> , 2021 , 1-1	2.5	
71	Robust Discriminant Projection Via Joint Margin and Locality Structure Preservation. <i>Neural Processing Letters</i> , 2021 , 53, 959-982	2.4	1
70	Integrating Two Logics Into One Crossbar Array for Logic Gate Design. <i>IEEE Transactions on Circuits and Systems II: Express Briefs</i> , 2021 , 68, 2987-2991	3.5	1
69	Structure preservation adversarial network for visual domain adaptation. <i>Information Sciences</i> , 2021 , 579, 266-280	7.7	О
68	Dependency-Aware Computation Offloading for Mobile Edge Computing with Edge-Cloud Cooperation. <i>IEEE Transactions on Cloud Computing</i> , 2021 , 1-1	3.3	7
67	Transferable Linear Discriminant Analysis. <i>IEEE Transactions on Neural Networks and Learning Systems</i> , 2020 , 31, 5630-5638	10.3	6
66	SODNet: small object detection using deconvolutional neural network. <i>IET Image Processing</i> , 2020 , 14, 1662-1669	1.7	5
65	Smart Contract-Based Long-Term Auction for Mobile Blockchain Computation Offloading. <i>IEEE Access</i> , 2020 , 8, 36029-36042	3.5	8
64	Visual Sentiment Prediction with Attribute Augmentation and Multi-attention Mechanism. <i>Neural Processing Letters</i> , 2020 , 51, 2403-2416	2.4	5
63	Joint discriminative attributes and similarity embeddings modeling for zero-shot recognition. <i>Neurocomputing</i> , 2020 , 399, 117-128	5.4	4
62	Learning Multi-Part Attention Neural Network for Zero-shot Classification. <i>IEEE Transactions on Cognitive and Developmental Systems</i> , 2020 , 1-1	3	0
61	Efficient task scheduling for servers with dynamic states in vehicular edge computing. <i>Computer Communications</i> , 2020 , 150, 245-253	5.1	13
60	Blockchain-based public auditing for big data in cloud storage. <i>Information Processing and Management</i> , 2020 , 57, 102382	6.3	62
59	Projective Double Reconstructions Based Dictionary Learning Algorithm for Cross-Domain Recognition. <i>IEEE Transactions on Image Processing</i> , 2020 , PP,	8.7	6
58	Multiview Consensus Structure Discovery. <i>IEEE Transactions on Cybernetics</i> , 2020 , PP,	10.2	3
57	Double Relaxed Regression for Image Classification. <i>IEEE Transactions on Circuits and Systems for Video Technology</i> , 2020 , 30, 307-319	6.4	11
56	Group Low-Rank Representation-Based Discriminant Linear Regression. <i>IEEE Transactions on Circuits and Systems for Video Technology</i> , 2020 , 30, 760-770	6.4	2
55	Multiple-Choice Hardware/Software Partitioning for Tree Task-Graph on MPSoC. <i>Computer Journal</i> , 2020 , 63, 688-700	1.3	1

54	Constrained Discriminative Projection Learning for Image Classification. <i>IEEE Transactions on Image Processing</i> , 2020 , 29, 186-198	8.7	11
53	Flexible robust principal component analysis. <i>International Journal of Machine Learning and Cybernetics</i> , 2020 , 11, 603-613	3.8	3
52	Robust Multi-View Hashing for Cross-Modal Retrieval 2019 ,		3
51	Supervised Consistent and Specific Hashing 2019 ,		1
50	Secrecy rate and residual energy trade-off in energy harvesting cognitive radio networks. <i>Physical Communication</i> , 2019 , 37, 100857	2.2	2
49	Flexible Affinity Matrix Learning for Unsupervised and Semisupervised Classification. <i>IEEE Transactions on Neural Networks and Learning Systems</i> , 2019 , 30, 1133-1149	10.3	23
48	Efficient three-stage auction schemes for cloudlets deployment in wireless access network. <i>Wireless Networks</i> , 2019 , 25, 3335-3349	2.5	5
47	Passivity and synchronization of switched coupled reaction diffusion neural networks with non-delayed and delayed couplings. <i>International Journal of Computer Mathematics</i> , 2019 , 96, 1702-17	22 ^{1.2}	8
46	2019,		9
45	Latent Elastic-Net Transfer Learning. IEEE Transactions on Image Processing, 2019,	8.7	9
44	Task Merging and Scheduling for Parallel Deep Learning Applications in Mobile Edge Computing 2019 ,		1
43	Low-Latency Cooperative Computation Offloading for Mobile Edge Computing 2019,		1
42	Utility-Aware Batch-Processing Algorithms for Dynamic Carpooling Based on Double Auction 2019,		2
41	2019,		2
40	2019,		3
39	Passivity-Based Leader-Following Consensus Control for Nonlinear Multi-Agent Systems with Fixed and Switching Topologies. <i>IEEE Transactions on Network Science and Engineering</i> , 2019 , 6, 844-856	4.9	11
38	Unsupervised feature extraction by low-rank and sparsity preserving embedding. <i>Neural Networks</i> , 2019 , 109, 56-66	9.1	19
37	Precision direction and compact surface type representation for 3D palmprint identification. Pattern Recognition, 2019, 87, 237-247	7.7	11

(2017-2019)

36	TARCO: Two-Stage Auction for D2D Relay Aided Computation Resource Allocation in HetNet. <i>IEEE Transactions on Services Computing</i> , 2019 , 1-1	4.8	14
35	Approximate Low-Rank Projection Learning for Feature Extraction. <i>IEEE Transactions on Neural Networks and Learning Systems</i> , 2018 , 29, 5228-5241	10.3	56
34	BRAINS: Joint Bandwidth-Relay Allocation in Multihoming Cooperative D2D Networks. <i>IEEE Transactions on Vehicular Technology</i> , 2018 , 67, 5387-5398	6.8	15
33	Passivity and pinning control of coupled neural networks with and without time-varying delay. <i>Transactions of the Institute of Measurement and Control</i> , 2018 , 40, 2708-2717	1.8	19
32	Block-secure: Blockchain based scheme for secure P2P cloud storage. <i>Information Sciences</i> , 2018 , 465, 219-231	7.7	105
31	Low-rank and sparse embedding for dimensionality reduction. <i>Neural Networks</i> , 2018 , 108, 202-216	9.1	15
30	Algorithms for Replica Placement and Update in Tree Network. Computer Journal, 2018, 61, 273-287	1.3	1
29	Defect Analysis and Parallel March Test Algorithm for 3D Hybrid CMOS-Memristor Memory 2018 ,		3
28	ETRA: Efficient Three-Stage Resource Allocation Auction for Mobile Blockchain in Edge Computing 2018 ,		7
27	A Direction-Based Vehicular Network Model in Vehicular Fog Computing 2018,		2
26	TAMSA: Two-Stage Auction Mechanism for Spectrum Allocation in Cooperative Cognitive Radio Networks. <i>Lecture Notes in Computer Science</i> , 2018 , 3-16	0.9	
25	Blockchain-Based Secure and Reliable Distributed Deduplication Scheme. <i>Lecture Notes in Computer Science</i> , 2018 , 393-405	0.9	5
24	NESTLE: Incentive Mechanism Specialized for Computation Offloading in Local Edge Community. <i>Lecture Notes in Computer Science</i> , 2018 , 90-104	0.9	2
23	POEM: Pricing Longer for Edge Computing in the Device Cloud. <i>Lecture Notes in Computer Science</i> , 2018 , 355-369	0.9	3
22	Energy-Efficient Offloading in Mobile Edge Computing with Edge-Cloud Collaboration. <i>Lecture Notes in Computer Science</i> , 2018 , 460-475	0.9	8
21	Deduplication with Blockchain for Secure Cloud Storage. <i>Communications in Computer and Information Science</i> , 2018 , 558-570	0.3	10
20	QUICK: QoS-guaranteed efficient cloudlet placement in wireless metropolitan area networks. <i>Journal of Supercomputing</i> , 2018 , 74, 4037-4059	2.5	19
19	Passivity of Directed and Undirected Complex Dynamical Networks With Adaptive Coupling Weights. <i>IEEE Transactions on Neural Networks and Learning Systems</i> , 2017 , 28, 1827-1839	10.3	75

18	TACD: A Three-Stage Auction Scheme for Cloudlet Deployment in Wireless Access Network. <i>Lecture Notes in Computer Science</i> , 2017 , 877-882	0.9	5
17	Joint Charging Tour Planning and Depot Positioning for Wireless Sensor Networks Using Mobile Chargers. <i>IEEE/ACM Transactions on Networking</i> , 2017 , 25, 2250-2266	3.8	49
16	Fast algorithms for capacitated cloudlet placements 2017,		12
15	Blockchain-Based Security Architecture for Distributed Cloud Storage 2017 ,		20
14	Combining Enhanced Competitive Code with Compacted ST for 3D Palmprint Recognition 2017,		3
13	DOTA: Delay Bounded Optimal Cloudlet Deployment and User Association in WMANs 2017,		22
12	Passivity and Pinning Passivity of Coupled Delayed Reaction Diffusion Neural Networks with Dirichlet Boundary Conditions. <i>Neural Processing Letters</i> , 2017 , 45, 869-885	2.4	24
11	Passivity Analysis of Coupled Reaction-Diffusion Neural Networks With Dirichlet Boundary Conditions. <i>IEEE Transactions on Systems, Man, and Cybernetics: Systems,</i> 2017 , 47, 2148-2159	7.3	66
10	QoE-Aware Task Offloading for Time Constraint Mobile Applications 2017,		7
9	Note on Edge-Colored Graphs for Networks with Homogeneous Faults. <i>Computer Journal</i> , 2016 , 59, 14	17 0. 347	7 81
9	Note on Edge-Colored Graphs for Networks with Homogeneous Faults. <i>Computer Journal</i> , 2016 , 59, 14 Pinning Control for Synchronization of Coupled Reaction-Diffusion Neural Networks With Directed Topologies. <i>IEEE Transactions on Systems</i> , <i>Man</i> , <i>and Cybernetics: Systems</i> , 2016 , 46, 1109-1120	17 0. 347	142
	Pinning Control for Synchronization of Coupled Reaction-Diffusion Neural Networks With Directed		
8	Pinning Control for Synchronization of Coupled Reaction-Diffusion Neural Networks With Directed Topologies. <i>IEEE Transactions on Systems, Man, and Cybernetics: Systems</i> , 2016 , 46, 1109-1120 Tight Upper Bound for Accelerating Reconfiguration of VLSI Arrays. <i>Journal of Circuits, Systems and</i>	7.3	142
8	Pinning Control for Synchronization of Coupled Reaction-Diffusion Neural Networks With Directed Topologies. <i>IEEE Transactions on Systems, Man, and Cybernetics: Systems,</i> 2016 , 46, 1109-1120 Tight Upper Bound for Accelerating Reconfiguration of VLSI Arrays. <i>Journal of Circuits, Systems and Computers</i> , 2015 , 24, 1550099 Reconfiguring Three-Dimensional Processor Arrays for Fault-Tolerance: Hardness and Heuristic	7-3	142
8 7 6	Pinning Control for Synchronization of Coupled Reaction-Diffusion Neural Networks With Directed Topologies. <i>IEEE Transactions on Systems, Man, and Cybernetics: Systems,</i> 2016 , 46, 1109-1120 Tight Upper Bound for Accelerating Reconfiguration of VLSI Arrays. <i>Journal of Circuits, Systems and Computers</i> , 2015 , 24, 1550099 Reconfiguring Three-Dimensional Processor Arrays for Fault-Tolerance: Hardness and Heuristic Algorithms. <i>IEEE Transactions on Computers</i> , 2015 , 64, 2926-2939	7-3	142 1
8 7 6 5	Pinning Control for Synchronization of Coupled Reaction-Diffusion Neural Networks With Directed Topologies. <i>IEEE Transactions on Systems, Man, and Cybernetics: Systems</i> , 2016 , 46, 1109-1120 Tight Upper Bound for Accelerating Reconfiguration of VLSI Arrays. <i>Journal of Circuits, Systems and Computers</i> , 2015 , 24, 1550099 Reconfiguring Three-Dimensional Processor Arrays for Fault-Tolerance: Hardness and Heuristic Algorithms. <i>IEEE Transactions on Computers</i> , 2015 , 64, 2926-2939 Reconfigurations for Processor Arrays with Faulty Switches and Links 2015 ,	7-3	142 1 9
8 7 6 5	Pinning Control for Synchronization of Coupled Reaction-Diffusion Neural Networks With Directed Topologies. <i>IEEE Transactions on Systems, Man, and Cybernetics: Systems,</i> 2016 , 46, 1109-1120 Tight Upper Bound for Accelerating Reconfiguration of VLSI Arrays. <i>Journal of Circuits, Systems and Computers,</i> 2015 , 24, 1550099 Reconfiguring Three-Dimensional Processor Arrays for Fault-Tolerance: Hardness and Heuristic Algorithms. <i>IEEE Transactions on Computers,</i> 2015 , 64, 2926-2939 Reconfigurations for Processor Arrays with Faulty Switches and Links 2015 , Efficient Heuristic for Placing Monitors on Flow Networks 2015 , Algorithmic aspects of graph reduction for hardware/software partitioning. <i>Journal of</i>	7.3	142 1 9 3