

# Rahul Pandey

## List of Publications by Year in descending order

Source: <https://exaly.com/author-pdf/5492402/publications.pdf>

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5  
papers

119  
citations

1937685

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2272923

4  
g-index

5  
all docs

5  
docs citations

5  
times ranked

115  
citing authors

#	ARTICLE	IF	CITATIONS
1	Performance Analysis of Drain Pocket Hetero Gate Dielectric DG-TFET: Solution for Ambipolar Conduction and Enhanced Drive Current. Silicon, 2022, 14, 8097-8107.	3.3	9
2	Source Material-Engineered Charge Plasma based Double Gate TFET for Analog/RF Applications. , 2021, , .		3
3	Opportunities and Challenges of Tunnel FETs. IEEE Transactions on Circuits and Systems I: Regular Papers, 2016, 63, 2128-2138.	5.4	40
4	Impact of Single Trap Random Telegraph Noise on Heterojunction TFET SRAM Stability. IEEE Electron Device Letters, 2014, 35, 393-395.	3.9	13
5	Electrical Noise in Heterojunction Interband Tunnel FETs. IEEE Transactions on Electron Devices, 2014, 61, 552-560.	3.0	54