

Brajesh K Kaushik

List of Publications by Year in descending order

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360
papers

5,072
citations

94433

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144013

57
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366
all docs

366
docs citations

366
times ranked

2801
citing authors

#	ARTICLE	IF	CITATIONS
1	Organic Thin Film Transistors: Structures, Models, Materials, Fabrication, and Applications: A Review. Polymer Reviews, 2014, 54, 33-111.	10.9	272
2	Recent advancements in optical biosensors for cancer detection. Biosensors and Bioelectronics, 2022, 197, 113805.	10.1	173
3	MoS ₂ Functionalized Multicore Fiber Probes for Selective Detection of <i>Shigella</i> Bacteria Based on Localized Plasmon. Journal of Lightwave Technology, 2021, 39, 4069-4081.	4.6	144
4	Perspectives and challenges for organic thin film transistors: materials, devices, processes and applications. Journal of Materials Science: Materials in Electronics, 2014, 25, 1-30.	2.2	126
5	Water Pollutants p-Cresol Detection Based on Au-ZnO Nanoparticles Modified Tapered Optical Fiber. IEEE Transactions on Nanobioscience, 2021, 20, 377-384.	3.3	109
6	Etched multicore fiber sensor using copper oxide and gold nanoparticles decorated graphene oxide structure for cancer cells detection. Biosensors and Bioelectronics, 2020, 168, 112557.	10.1	108
7	Asymmetric Dual-Spacer Trigate FinFET Device-Circuit Codesign and Its Variability Analysis. IEEE Transactions on Electron Devices, 2015, 62, 1105-1112.	3.0	102
8	Channel length variation effect on performance parameters of organic field effect transistors. Microelectronics Journal, 2012, 43, 985-994.	2.0	90
9	T-Shaped III-V Heterojunction Tunneling Field-Effect Transistor. IEEE Transactions on Electron Devices, 2017, 64, 3120-3125.	3.0	83
10	Analysis of Delay and Dynamic Crosstalk in Bundled Carbon Nanotube Interconnects. IEEE Transactions on Electromagnetic Compatibility, 2014, 56, 1666-1673.	2.2	80
11	LSPR-based cholesterol biosensor using a tapered optical fiber structure. Biomedical Optics Express, 2019, 10, 2150.	2.9	72
12	LSPR-Based Cholesterol Biosensor Using Hollow Core Fiber Structure. IEEE Sensors Journal, 2019, 19, 7399-7406.	4.7	71
13	Investigation of Symmetric Dual- $\langle k \rangle$ Spacer Trigate FinFETs From Delay Perspective. IEEE Transactions on Electron Devices, 2014, 61, 3579-3585.	3.0	68
14	Time and Frequency Domain Analysis of MLG NR Interconnects. IEEE Nanotechnology Magazine, 2015, 14, 484-492.	2.0	65
15	LSPR based uric acid sensor using graphene oxide and gold nanoparticles functionalized tapered fiber. Optical Fiber Technology, 2019, 53, 102043.	2.7	65
16	Review of Silicene: From Material to Device Applications. ECS Journal of Solid State Science and Technology, 2020, 9, 115031.	1.8	65
17	High-Performance and Robust SRAM Cell Based on Asymmetric Dual- $\langle k \rangle$ Spacer FinFETs. IEEE Transactions on Electron Devices, 2013, 60, 3371-3377.	3.0	61
18	Crosstalk Analysis for a CMOS-Gate-Driven Coupled Interconnects. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 2008, 27, 1150-1154.	2.7	59

#	ARTICLE	IF	CITATIONS
19	Analysis of MWCNT and Bundled SWCNT Interconnects: Impact on Crosstalk and Area. IEEE Electron Device Letters, 2012, 33, 1180-1182.	3.9	59
20	Development of Uric Acid Biosensor Using Gold Nanoparticles and Graphene Oxide Functionalized Micro-Ball Fiber Sensor Probe. IEEE Transactions on Nanobioscience, 2020, 19, 173-182.	3.3	58
21	Development of Dopamine Sensor Using Silver Nanoparticles and PEG-Functionalized Tapered Optical Fiber Structure. IEEE Transactions on Biomedical Engineering, 2020, 67, 1542-1547.	4.2	57
22	Gold Nanoparticles and Uricase Functionalized Tapered Fiber Sensor for Uric Acid Detection. IEEE Sensors Journal, 2020, 20, 219-226.	4.7	56
23	2D Materials-Based Fiber Optic SPR Biosensor for Cancer Detection at 1550 nm. IEEE Sensors Journal, 2021, 21, 23957-23964.	4.7	55
24	Localized Surface Plasmon Resonance Based Hetero-Core Optical Fiber Sensor Structure for the Detection of L-Cysteine. IEEE Nanotechnology Magazine, 2020, 19, 201-208.	2.0	53
25	Frequency response and bandwidth analysis of multi-layer graphene nanoribbon and multi-walled carbon nanotube interconnects. Micro and Nano Letters, 2014, 9, 557-560.	1.3	51
26	Carbon Nanotube: Properties and Applications. SpringerBriefs in Applied Sciences and Technology, 2015, , 17-37.	0.4	50
27	Single and dual gate OTFT based robust organic digital design. Microelectronics Reliability, 2014, 54, 100-109.	1.7	49
28	An Accurate FDTD Model for Crosstalk Analysis of CMOS-Gate-Driven Coupled RLC Interconnects. IEEE Transactions on Electromagnetic Compatibility, 2014, 56, 1185-1193.	2.2	48
29	Detection of Collagen-IV Using Highly Reflective Metal Nanoparticles-Immobilized Photosensitive Optical Fiber-Based MZI Structure. IEEE Transactions on Nanobioscience, 2020, 19, 477-484.	3.3	45
30	Stability and delay analysis of multi-layered GNR and multi-walled CNT interconnects. Journal of Computational Electronics, 2015, 14, 611-618.	2.5	43
31	Design and analysis of noise margin, write ability and read stability of organic and hybrid 6-T SRAM cell. Microelectronics Reliability, 2014, 54, 2801-2812.	1.7	42
32	An accurate model for dynamic crosstalk analysis of CMOS gate driven on-chip interconnects using FDTD method. Microelectronics Journal, 2014, 45, 441-448.	2.0	42
33	Localized Plasmon-Based Multicore Fiber Biosensor for Acetylcholine Detection. IEEE Transactions on Instrumentation and Measurement, 2022, 71, 1-9.	4.7	41
34	Crosstalk analysis for a CMOS gate driven inductively and capacitively coupled interconnects. Microelectronics Journal, 2008, 39, 1834-1842.	2.0	40
35	Delay and crosstalk reliability issues in mixed MWCNT bundle interconnects. Microelectronics Reliability, 2014, 54, 2570-2577.	1.7	40
36	From MTJ Device to Hybrid CMOS/MTJ Circuits: A Review. IEEE Access, 2020, 8, 194105-194146.	4.2	40

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37	Dynamic crosstalk effect in mixed CNT bundle interconnects. Electronics Letters, 2012, 48, 384.	1.0	39
38	Review of Recent Progress on Silicon Nitride-Based Photonic Integrated Circuits. IEEE Access, 2020, 8, 195436-195446.	4.2	39
39	Crosstalk analysis and repeater insertion in crosstalk aware coupled VLSI interconnects. Microelectronics International, 2006, 23, 55-63.	0.6	37
40	Future VLSI interconnects: optical fiber or carbon nanotube – a review. Microelectronics International, 2007, 24, 53-63.	0.6	37
41	Static and dynamic characteristics of dual gate organic TFT based NAND and NOR circuits. Journal of Computational Electronics, 2014, 13, 627-638.	2.5	37
42	An Unconditionally Stable FDTD Model for Crosstalk Analysis of VLSI Interconnects. IEEE Transactions on Components, Packaging and Manufacturing Technology, 2015, 5, 1810-1817.	2.5	37
43	Graphene Based On-Chip Interconnects and TSVs : Prospects and Challenges. IEEE Nanotechnology Magazine, 2014, 8, 14-20.	1.3	36
44	Effect of line resistance and driver width on crosstalk in coupled VLSI interconnects. Microelectronics International, 2007, 24, 42-45.	0.6	34
45	Waveform analysis and delay prediction for a CMOS gate driving RLC interconnect load. The Integration VLSI Journal, 2007, 40, 394-405.	2.1	34
46	Crosstalk analysis of simultaneously switching interconnects. International Journal of Electronics, 2009, 96, 1095-1114.	1.4	34
47	An analytical approach to dynamic crosstalk in coupled interconnects. Microelectronics Journal, 2010, 41, 85-92.	2.0	34
48	Analytical modeling and parameter extraction of top and bottom contact structures of organic thin film transistors. Microelectronics Journal, 2013, 44, 736-743.	2.0	34
49	Carbon Nanotube Based 3-D Interconnects - A Reality or a Distant Dream. IEEE Circuits and Systems Magazine, 2014, 14, 16-35.	2.3	34
50	MXenes-Based Fiber-Optic SPR Sensor for Colorectal Cancer Diagnosis. IEEE Sensors Journal, 2022, 22, 6661-6668.	4.7	34
51	Highly Sensitive, Selective and Portable Sensor Probe using Germanium-Doped Photosensitive Optical Fiber for Ascorbic Acid Detection. IEEE Sensors Journal, 2020, , 1-1.	4.7	33
52	Crosstalk noise modeling of multiwall carbon nanotube (MWCNT) interconnects using finite-difference time-domain (FDTD) technique. Microelectronics Reliability, 2015, 55, 155-163.	1.7	32
53	Effects of process variation in VLSI interconnects – a technical review. Microelectronics International, 2009, 26, 49-55.	0.6	30
54	Analysis of electrical parameters of organic thin film transistors based on thickness variation in semi-conducting and dielectric layers. IET Circuits, Devices and Systems, 2014, 8, 131-140.	1.4	30

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55	Carbon Nanotube Based VLSI Interconnects. SpringerBriefs in Applied Sciences and Technology, 2015, , .	0.4	30
56	Development of Collagen-IV Sensor Using Optical Fiber-Based Mach-Zehnder Interferometer Structure. IEEE Journal of Quantum Electronics, 2020, 56, 1-8.	1.9	30
57	A survey of SRAM-based in-memory computing techniques and applications. Journal of Systems Architecture, 2021, 119, 102276.	4.3	30
58	Performance enhancement of graphene plasmonic nanoantennas for THz communication. IET Microwaves, Antennas and Propagation, 2019, 13, 71-75.	1.4	29
59	Tapered Optical Fiber-Based LSPR Biosensor for Ascorbic Acid Detection. Photonic Sensors, 2021, 11, 418-434.	5.0	29
60	Temperature-Dependent Modeling and Crosstalk Analysis in Mixed Carbon Nanotube Bundle Interconnects. Journal of Electronic Materials, 2017, 46, 5324-5337.	2.2	28
61	Computing-in-Memory Architecture Using Energy-Efficient Multilevel Voltage-Controlled Spin-Orbit Torque Device. IEEE Transactions on Electron Devices, 2020, 67, 1972-1979.	3.0	28
62	Process-Induced Delay Variation in SWCNT, MWCNT, and Mixed CNT Interconnects. IETE Journal of Research, 2015, 61, 533-540.	2.6	27
63	Transient Analysis of Crosstalk Induced Effects in Mixed CNT Bundle Interconnects Using FDTD Technique. IEEE Transactions on Electromagnetic Compatibility, 2019, 61, 1621-1629.	2.2	27
64	(INVITED) Advances in photonic crystal fiber: sensing and supercontinuum generation applications. Optical Fiber Technology, 2022, 72, 102982.	2.7	27
65	Organic Thin Film Transistor Architecture, Parameters and their Applications. , 2011, , .		26
66	Performance analysis of multilayer graphene nanoribbon (MLGNR) interconnects. Journal of Computational Electronics, 2016, 15, 358-366.	2.5	26
67	Temperature-dependent modeling and performance analysis of coupled MLGNR interconnects. International Journal of Circuit Theory and Applications, 2018, 46, 299-312.	2.0	25
68	Modeling of Voltage-Controlled Spin-Orbit Torque MRAM for Multilevel Switching Application. IEEE Transactions on Electron Devices, 2020, 67, 90-98.	3.0	25
69	Modeling of top and bottom contact structure organic field effect transistors. Journal of Vacuum Science and Technology B: Nanotechnology and Microelectronics, 2013, 31, .	1.2	24
70	Delay uncertainty in MLGNR interconnects under process induced variations of width, doping, dielectric thickness and mean free path. Journal of Computational Electronics, 2014, 13, 639-646.	2.5	24
71	Monolayer MoSe ₂ -Based Tunneling Field Effect Transistor for Ultrasensitive Strain Sensing. IEEE Transactions on Electron Devices, 2020, 67, 2140-2146.	3.0	24
72	Thermally aware performance analysis of single-walled carbon nanotube bundle as VLSI interconnects. Journal of Computational Electronics, 2016, 15, 407-419.	2.5	23

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73	Static and dynamic analysis of organic and hybrid inverter circuits. Journal of Computational Electronics, 2013, 12, 765-774.	2.5	22
74	Improved crosstalk noise modeling of MWCNT interconnects using FDTD technique. Microelectronics Journal, 2015, 46, 1263-1268.	2.0	22
75	Crosstalk Induced Delay Analysis of Randomly Distributed Mixed CNT Bundle Interconnect. Journal of Circuits, Systems and Computers, 2015, 24, 1550145.	1.5	22
76	Quantum Computing: Fundamentals, Implementations and Applications. IEEE Open Journal of Nanotechnology, 2022, 3, 61-77.	2.0	22
77	Photosensor Based on Split Gate TMD TFET Using Photogating Effect for Visible Light Detection. IEEE Sensors Journal, 2020, 20, 6346-6353.	4.7	21
78	Knowledge-Based Neural Networks for Fast Design Space Exploration of Hybrid Copper-Graphene On-Chip Interconnect Networks. IEEE Transactions on Electromagnetic Compatibility, 2022, 64, 182-195.	2.2	21
79	Efficient Method and Architecture for Real-Time Video Defogging. IEEE Transactions on Intelligent Transportation Systems, 2021, 22, 6536-6546.	8.0	20
80	A Charge Plasma-Based Monolayer Transition Metal Dichalcogenide Tunnel FET. IEEE Transactions on Electron Devices, 2019, 66, 2837-2843.	3.0	19
81	Design Metrics Improvement for SRAMs Using Symmetric Dual-\$k\$ Spacer (SymD-\$k\$) FinFETs. IEEE Transactions on Electron Devices, 2014, 61, 1123-1130.	3.0	18
82	Energy-Efficient All-Spin BNN Using Voltage-Controlled Spin-Orbit Torque Device for Digit Recognition. IEEE Transactions on Electron Devices, 2021, 68, 385-392.	3.0	18
83	Spintronics-Based Devices to Circuits: Perspectives and challenges. IEEE Nanotechnology Magazine, 2016, 10, 13-28.	1.3	17
84	VLSI interconnects and their testing: prospects and challenges ahead. Journal of Engineering, Design and Technology, 2011, 9, 63-84.	1.7	16
85	Area and Energy Efficient Series Multilevel Cell STT-MRAMs for Optimized Readâ€“Write Operations. IEEE Transactions on Magnetics, 2019, 55, 1-10.	2.1	16
86	First-Principle Analysis of Transition Metal Edge-Passivated Armchair Graphene Nanoribbons for Nanoscale Interconnects. IEEE Nanotechnology Magazine, 2021, 20, 92-98.	2.0	16
87	Antiferromagnetic skyrmion repulsion based artificial neuron device. Nanotechnology, 2021, 32, 215204.	2.6	16
88	Prospects and Limitations of Organic Thin Film Transistors (OTFTs). Advances in Intelligent Systems and Computing, 2012, , 125-139.	0.6	16
89	Repeater insertion in crosstalkâ€“aware inductively and capacitively coupled interconnects. International Journal of Circuit Theory and Applications, 2011, 39, 629-647.	2.0	15
90	Characteristics and applications of Polymeric Thin Film Transistor: Prospects and challenges. , 2011, , .		14

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91	Novel $4F^2$ Buried-Source-Line STT MRAM Cell With Vertical GAA Transistor as Select Device. IEEE Nanotechnology Magazine, 2014, 13, 1163-1171.	2.0	14
92	Efficient Hardware Implementation of DNN-Based Speech Enhancement Algorithm With Precise Sigmoid Activation Function. IEEE Transactions on Circuits and Systems II: Express Briefs, 2021, 68, 3461-3465.	3.0	14
93	SOT and STT-Based 4-Bit MRAM Cell for High-Density Memory Applications. IEEE Transactions on Electron Devices, 2021, 68, 4384-4390.	3.0	14
94	Analysis of static and dynamic performance of organic inverter circuits based on dual and single gate organic thin film transistors. IET Circuits, Devices and Systems, 2013, 7, 345-351.	1.4	13
95	FDTD technique based crosstalk analysis of bundled SWCNT interconnects. Journal of Semiconductors, 2015, 36, 055002.	3.7	13
96	Electrical Tuning of Optical Delay in Graphene-Based Photonic Crystal Waveguide. IEEE Journal of Quantum Electronics, 2015, 51, 1-5.	1.9	13
97	Multispectral Transmission Map Fusion Method and Architecture for Image Dehazing. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2019, 27, 2693-2697.	3.1	13
98	Differential Spin Hall Effect-Based Nonvolatile Static Random Access Memory for Energy-Efficient and Fast Data Restoration Application. IEEE Transactions on Magnetics, 2019, 55, 1-11.	2.1	13
99	Modeling of a Magnetic Tunnel Junction for a Multilevel STT-MRAM Cell. IEEE Nanotechnology Magazine, 2019, 18, 1005-1014.	2.0	13
100	Energy-Efficient Differential Spin Hall MRAM-Based 4-2 Magnetic Compressor. IEEE Transactions on Magnetics, 2020, 56, 1-11.	2.1	13
101	Computing-in-memory using voltage-controlled spin-orbit torque based MRAM array. Microelectronics Journal, 2021, 109, 104943.	2.0	13
102	Performance analysis of single- and multi-walled carbon nanotube based through silicon vias. , 2015, , .		12
103	Variation-aware widely tunable nanoscale design of CMOS active inductor-based RF bandpass filter. International Journal of Circuit Theory and Applications, 2017, 45, 2181-2200.	2.0	12
104	Next Generation Spin Torque Memories. SpringerBriefs in Applied Sciences and Technology, 2017, , .	0.4	12
105	Implementation of reversible Peres gate using electro-optic effect inside lithium-niobate based Mach-Zehnder interferometers. Optics and Laser Technology, 2019, 117, 28-37.	4.6	12
106	Width optimization of global inductive VLSI interconnects. Microelectronics International, 2006, 23, 26-30.	0.6	11
107	Performance analysis for randomly distributed mixed carbon nanotube bundle interconnects. Micro and Nano Letters, 2014, 9, 792-796.	1.3	11
108	High-Performance Computing-in-Memory Architecture Using STT-/SOT-Based Series Triple-Level Cell MRAM. IEEE Transactions on Magnetics, 2021, 57, 1-12.	2.1	11

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109	Implementation of an efficient magnetic tunnel junction-based stochastic neural network with application to iris data classification. Nanotechnology, 2020, 31, 504001.	2.6	11
110	Low-Power High-Density STT MRAMs on a 3-D Vertical Silicon Nanowire Platform. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2016, 24, 1371-1376.	3.1	10
111	Parallel Multilevel Cell STT-MRAMs for Optimized Area Energy and Readâ€“Write Operations. IEEE Transactions on Magnetics, 2018, 54, 1-9.	2.1	10
112	DFT Analysis of Hydrogenated Zigzag Aluminum Nitride Nanoribbons for Spintronic Devices. IEEE Transactions on Electron Devices, 2022, 69, 4494-4500.	3.0	10
113	Signal integrity and propagation delay analysis using FDTD technique for VLSI interconnects. Journal of Computational Electronics, 2014, 13, 300-306.	2.5	9
114	A Transition Metal Dichalcogenide Tunnel FET-Based Waveguide-Integrated Photodetector Using Ge for Near-Infrared Detection. IEEE Sensors Journal, 2019, 19, 9187-9193.	4.7	9
115	Design of grating based narrow band reflector on SOI waveguide. Optik, 2021, 227, 165995.	2.9	9
116	Image Processing Framework for Performance Enhancement of Low-Light Image Sensors. IEEE Sensors Journal, 2021, 21, 8530-8542.	4.7	9
117	Hardware Security Exploiting Post-CMOS Devices: Fundamental Device Characteristics, State-of-the-Art Countermeasures, Challenges and Roadmap. IEEE Circuits and Systems Magazine, 2021, 21, 4-30.	2.3	9
118	Antimonene, CNT and MoS ₂ Based SPR-Fiber-Optic Probe for Tuberculosis Detection. IEEE Sensors Journal, 2022, 22, 14903-14910.	4.7	9
119	VLSI Architecture Design and Implementation for Application Specific CORDIC Processor. , 2010, , .		8
120	Organic thin film transistors characteristics parameters, structures and their applications. , 2011, , .		8
121	Hybrid plasmonic waveguide with centimeter-scale propagation length for nanoscale optical confinement. Applied Optics, 2016, 55, 10341.	2.1	8
122	Transition metal dichalcogenides integrated waveguide modulator and attenuator in silicon nitride platform. Nanotechnology, 2020, 31, 435202.	2.6	8
123	Enhancing the Surveillance Detection Range of Image Sensors Using HDR Techniques. IEEE Sensors Journal, 2021, 21, 19516-19528.	4.7	8
124	Vertical traversal approach towards TSVs optimisation over multilayer network on chip (NoC). Microelectronics Journal, 2021, 116, 105231.	2.0	8
125	Multi-Sensor Surveillance System Based on Integrated Video Analytics. IEEE Sensors Journal, 2022, 22, 10207-10222.	4.7	8
126	Transient Analysis of Hybrid Cu-CNT On-Chip Interconnects Using MRA Technique. IEEE Open Journal of Nanotechnology, 2022, 3, 24-35.	2.0	8

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127	Antiferromagnetic skyrmion based shape-configured leaky-integrate-fire neuron device. Journal Physics D: Applied Physics, 2022, 55, 345007.	2.8	8
128	High-Speed Interconnects: History, Evolution, and the Road Ahead. IEEE Microwave Magazine, 2022, 23, 66-82.	0.8	8
129	Effect of polymer liners in CNT based through silicon vias. , 2014, , .		7
130	Analysis of crosstalk delay using mixed CNT bundle based through silicon vias. , 2014, , .		7
131	Impact of driver size and interwire parasitics on crosstalk noise and delay. Journal of Engineering, Design and Technology, 2014, 12, 475-490.	1.7	7
132	Inverted δ -T δ TM Junctionless FinFET (ITJL FinFET): Performance Estimation through Device Geometry Variation. ECS Journal of Solid State Science and Technology, 2018, 7, Q52-Q59.	1.8	7
133	Analytical modelling and device design optimisation of epitaxial layer δ -based III δ TM tunnel FET. IET Circuits, Devices and Systems, 2019, 13, 763-770.	1.4	7
134	A Temperature and Dielectric Roughness-Aware Matrix Rational Approximation Model for the Reliability Assessment of Copper δ Graphene Hybrid On-Chip Interconnects. IEEE Transactions on Components, Packaging and Manufacturing Technology, 2020, 10, 1454-1465.	2.5	7
135	Performance analysis of DSHE based memories. , 2018, , .		7
136	Comparative Analysis of Spintronic Memories for Low Power on-chip Caches. Spin, 2020, 10, .	1.3	7
137	Energy-Efficient Advanced Data Encryption System Using Spin-Based Computing-in-Memory Architecture. IEEE Transactions on Electron Devices, 2022, 69, 1736-1742.	3.0	7
138	Leakage current reduction using modified gate replacement technique for CMOS VLSI circuit. , 2012, , .		6
139	Performance analysis of dual-k spacer at source side for underlap FinFETs. , 2012, , .		6
140	Optimized delay and power performances in multilayer graphene nanoribbon interconnects. , 2012, , .		6
141	Performance Comparision of Mixed CNT Bundle in Global VLSI Interconnect. , 2012, , .		6
142	A novel approach to reduce leakage current in ULP SRAM. IETE Journal of Research, 2013, 59, 704.	2.6	6
143	Signal Integrity Analysis in Carbon Nanotube Based Through-Silicon Via. Active and Passive Electronic Components, 2014, 2014, 1-7.	0.3	6
144	Modeling of crosstalk effects in coupled MLG NR interconnects based on FDTD method. , 2014, , .		6

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145	Modeling of In-Plane Magnetic Tunnel Junction for Mixed Mode Simulations. IEEE Transactions on Magnetism, 2014, 50, 1-7.	2.1	6
146	FPGA based implementation of real-time image enhancement algorithms for Electro-Optical surveillance systems. , 2015, , .		6
147	Transmission Coefficient Matrix Modeling of Spin-Torque-Based π -Qubit Architecture. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2018, 26, 1461-1470.	3.1	6
148	Temperature-dependent crosstalk between adjacent MLGMR interconnects of different dimensions and its impact on gate oxide reliability. Journal of Computational Electronics, 2020, 19, 191-205.	2.5	6
149	SOT and STT Based Four-Bit Parallel MRAM Cell for High-Density Applications. IEEE Nanotechnology Magazine, 2021, 20, 653-661.	2.0	6
150	Broadband THz Absorber for Large Inclination Angle TE and TM Waves. IEEE Photonics Journal, 2021, 13, 1-7.	2.0	6
151	T-Count Optimized Wallace Tree Integer Multiplier for Quantum Computing. International Journal of Theoretical Physics, 2021, 60, 2823-2835.	1.2	6
152	Propagation Delay Variation due to Process Induced Threshold Voltage Variation. Communications in Computer and Information Science, 2010, , 520-524.	0.5	5
153	Effect of Mutual Inductance and Coupling Capacitance on Propagation Delay and Peak Overshoot in Dynamically Switching Inputs. , 2010, , .		5
154	Crosstalk and Power Reduction Using Bus Encoding in RC Coupled VLSI Interconnects. , 2010, , .		5
155	Design of Ternary Content Addressable Memory (TCAM) with 180 nm. , 2011, , .		5
156	Propagation delay deviations due to process induced line parasitic variations in global VLSI interconnects. , 2011, , .		5
157	Boundary scan based testing algorithm to detect interconnect faults in printed circuit boards. Circuit World, 2011, 37, 27-34.	0.9	5
158	Dynamic crosstalk effect in multi-layer graphene nanoribbon interconnects. , 2012, , .		5
159	Analysis of crosstalk delay and area for MWNT and bundled SWNT in global VLSI interconnects. , 2012, , .		5
160	Bus encoder design for reduced crosstalk, power and area in coupled VLSI interconnects. Microelectronics Journal, 2013, 44, 827-833.	2.0	5
161	Effect of coupling parasitics and CMOS driver width on transition time for dynamic inputs. International Journal of Electronics, 2014, 101, 654-666.	1.4	5
162	High permittivity spacer effects on junctionless FinFET based circuit/SRAM applications. , 2014, , .		5

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163	Dynamic crosstalk analysis of mixed multi-walled carbon nanotube bundle interconnects. Journal of Engineering, 2014, 2014, 227-233.	1.1	5
164	Modeling of Carbon Nanotube Interconnects. SpringerBriefs in Applied Sciences and Technology, 2015, , 39-56.	0.4	5
165	All Spin Logic: A Micromagnetic Perspective. IEEE Transactions on Magnetics, 2015, 51, 1-10.	2.1	5
166	Design of LMS Adaptive Radar Detector for Non-homogeneous Interferences. IETE Technical Review (Institution of Electronics and Telecommunication Engineers, India), 2016, 33, 269-279.	3.2	5
167	Area and Energy Efficient Magnetic Full Adder based on Differential Spin Hall MRAM. , 2018, , .		5
168	Crosstalk Analysis in MWCNTs using a Closed-Form Matrix Rational Approximation Technique. , 2019, , .		5
169	Modeling and Fabrication Aspects of Cu- and Carbon Nanotube-Based Through-Silicon Vias. IETE Journal of Research, 2021, 67, 377-393.	2.6	5
170	Memoryless nonlinearity in IT JL FinFET with spacer technology: Investigation towards reliability. Microelectronics Reliability, 2021, 119, 114072.	1.7	5
171	High-Performance Computing-in-Memory Architecture Based on Single-Level and Multilevel Cell Differential Spin Hall MRAM. IEEE Transactions on Magnetics, 2021, 57, 1-15.	2.1	5
172	Dilute magnetic semiconductor electrode based all semiconductor magnetic tunnel junction for high-temperature applications. Physica B: Condensed Matter, 2022, 627, 413525.	2.7	5
173	Novel Radiation Hardened SOT-MRAM Read Circuit for Multi-Node Upset Tolerance. IEEE Open Journal of Nanotechnology, 2022, 3, 78-84.	2.0	5
174	Propagation Delay Variations under Process Deviation in Driver Interconnect Load System. , 2010, , .		4
175	Comparison of propagation delay in single- and multi-layer graphene nanoribbon interconnects. , 2012, , .		4
176	Dynamic Crosstalk Analysis in RLC Modeled Interconnects Using FDTD Method. , 2012, , .		4
177	Analysis of propagation delay in mixed carbon nanotube bundle as global VLSI interconnects. , 2012, , .		4
178	Crosstalk effect in coupled interconnect lines using FDTD method. , 2012, , .		4
179	Analysis of crosstalk delay and power dissipation in mixed CNT bundle interconnects. , 2012, , .		4
180	Analysis of propagation delay and power with variation in driver size and number of shells in multi walled carbon nanotube interconnects. Journal of Engineering, Design and Technology, 2013, 11, 19-33.	1.7	4

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181	Low-power and robust 6T SRAM cell using symmetric dual-k spacer FinFETs. , 2014, , .		4
182	Delay model for dynamically switching coupled RLC interconnects. EPJ Applied Physics, 2014, 66, 10903.	0.7	4
183	Interconnects. SpringerBriefs in Applied Sciences and Technology, 2015, , 1-15.	0.4	4
184	Signal integrity improvement with peripherally placed MWCNTs in mixed CNT bundle based TSVs. , 2015, , .		4
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186	Performance Enhancement of STT MRAM Using Asymmetric- Sidewall-Spacer NMOS. IEEE Transactions on Electron Devices, 2016, 63, 2771-2776.	3.0	4
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