

Miao Hu

List of Publications by Year in Descending Order

Source: <https://exaly.com/author-pdf/54444465/miao-hu-publications-by-year.pdf>

Version: 2024-04-10

This document has been generated based on the publications and citations recorded by exaly.com. For the latest version of this publication list, visit the link given above.

The third column is the impact factor (IF) of the journal, and the fourth column is the number of citations of the article.

19 papers	4,182 citations	13 h-index	22 g-index
22 ext. papers	5,229 ext. citations	18.5 avg, IF	5.05 L-index

#	Paper	IF	Citations
19	In situ training of feed-forward and recurrent convolutional memristor networks. <i>Nature Machine Intelligence</i> , 2019 , 1, 434-442	22.5	93
18	Reinforcement learning with analogue memristor arrays. <i>Nature Electronics</i> , 2019 , 2, 115-124	28.4	166
17	Long short-term memory networks in memristor crossbar arrays. <i>Nature Machine Intelligence</i> , 2019 , 1, 49-57	22.5	176
16	Fully memristive neural networks for pattern classification with unsupervised learning. <i>Nature Electronics</i> , 2018 , 1, 137-145	28.4	511
15	Memristor-Based Analog Computation and Neural Network Classification with a Dot Product Engine. <i>Advanced Materials</i> , 2018 , 30, 1705914	24	339
14	Capacitive neural network with neuro-transistors. <i>Nature Communications</i> , 2018 , 9, 3208	17.4	132
13	Efficient and self-adaptive in-situ learning in multilayer memristor neural networks. <i>Nature Communications</i> , 2018 , 9, 2385	17.4	371
12	Analogue signal and image processing with large memristor crossbars. <i>Nature Electronics</i> , 2018 , 1, 52-59	28.4	550
11	A Compact Memristor-Based Dynamic Synapse for Spiking Neural Networks. <i>IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems</i> , 2017 , 36, 1353-1366	2.5	59
10	Memristors with diffusive dynamics as synaptic emulators for neuromorphic computing. <i>Nature Materials</i> , 2017 , 16, 101-108	27	1201
9	Dot-product engine for neuromorphic computing 2016 ,		303
8	Dot-product engine as computing memory to accelerate machine learning algorithms 2016 ,		18
7	A Neuromorphic Architecture for Context Aware Text Image Recognition. <i>Journal of Signal Processing Systems</i> , 2016 , 84, 355-369	1.4	7
6	Memristor crossbar-based neuromorphic computing system: a case study. <i>IEEE Transactions on Neural Networks and Learning Systems</i> , 2014 , 25, 1864-78	10.3	235
5	Memristor Modeling -- Static, Statistical, and Stochastic Methodologies 2014 ,		1
4	Emerging memristor technology enabled next generation cortical processor 2014 ,		2
3	Neuromorphic acceleration for context aware text image recognition 2014 ,		3

- 2 The stochastic modeling of TiO₂ memristor and its usage in neuromorphic system design **2014**, 12
- 1 BSB training scheme implementation on memristor-based circuit **2013**, 3