Sameh El-Ashry

List of Publications by Year in descending order

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3311381 3475538 14 85 1 1 citations g-index h-index papers 14 14 14 38 docs citations times ranked citing authors all docs

#	Article	IF	CITATIONS
1	On Error Injection for NoC Platforms: A UVM-Based Generic Verification Environment. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 2020, 39, 1137-1150.	2.7	14
2	Different Reference Models for UVM Environment to Speed Up the Verification Time. , $2018, \ldots$		3
3	Fast Reliable Verification Methodology for RISC-V Without a Reference Model. , 2018, , .		3
4	Efficient Methodology of Sampling UVM RAL During Simulation for SoC Functional Coverage., 2018,,.		3
5	A Configurable RISC-V for NoC-Based MPSoCs: A Framework for Hardware Emulation. , 2018, , .		7
6	RVNoC: A Framework for Generating RISC-V NoC-Based MPSoC. , 2018, , .		12
7	On Error Injection for NoC Platforms. , 2017, , .		7
8	A reusable verification environment for NoC platforms using UVM., 2017,,.		9
9	A novel assertion-based CAD tool for automatic extraction of functional coverage. , 2016, , .		2
10	A narrative of UVM testbench environment for interconnection routers: A practical approach. , 2016, , .		5
11	Coverage Closure Efficient UVM Based Generic Verification Architecture for Flash Memory Controllers. , 2016, , .		4
12	A functional coverage approach for direct testing: An industrial IP as a case study. , 2015, , .		6
13	A novel memory controller architecture. , 2014, , .		3
14	Memory controller architectures: A comparative study. , 2013, , .		7