Jose Lopez

List of Publications by Year in descending order

54 papers	562 citations	687363 13 h-index	677142 22 g-index
55 all docs	55 docs citations	55 times ranked	486 citing authors

#	Article	IF	Citations
1	Laboratory Hyperspectral Image Acquisition System Setup and Validation. Sensors, 2022, 22, 2159.	3.8	4
2	A Novel Data Reutilization Strategy for Real-Time Hyperspectral Image Compression. IEEE Geoscience and Remote Sensing Letters, 2022, 19, 1-5.	3.1	2
3	Real-Time Hyperspectral Data Transmission for UAV-Based Acquisition Platforms. Remote Sensing, 2021, 13, 850.	4.0	11
4	A Multispectral Camera Development: From the Prototype Assembly until Its Use in a UAV System. Sensors, 2020, 20, 6129.	3.8	19
5	Towards the Concurrent Execution of Multiple Hyperspectral Imaging Applications by Means of Computationally Simple Operations. Remote Sensing, 2020, 12, 1343.	4.0	3
6	A Novel Hyperspectral Anomaly Detection Algorithm for Real-Time Applications With Push-Broom Sensors. IEEE Journal of Selected Topics in Applied Earth Observations and Remote Sensing, 2019, 12, 4787-4797.	4.9	18
7	A Simulation Environment for Validation and Verification of Real Time Hyperspectral Processing Algorithms on-Board a UAV. Remote Sensing, 2019, 11, 1852.	4.0	1
8	Real-Time Hyperspectral Image Compression Onto Embedded GPUs. IEEE Journal of Selected Topics in Applied Earth Observations and Remote Sensing, 2019, 12, 2792-2809.	4.9	27
9	A UAV Platform Based on a Hyperspectral Sensor for Image Capturing and On-Board Processing. IEEE Access, 2019, 7, 66919-66938.	4.2	54
10	Multispectral and Hyperspectral Lossless Compressor for Space Applications (HyLoC): A Low-Complexity FPGA Implementation of the CCSDS 123 Standard. IEEE Journal of Selected Topics in Applied Earth Observations and Remote Sensing, 2016, 9, 757-770.	4.9	47
11	Efficient lossy compression implementations of hyperspectral images: tools, hardware platforms, and comparisons. Proceedings of SPIE, 2014, , .	0.8	0
12	A New Preprocessing Technique for Fast Hyperspectral Endmember Extraction. IEEE Geoscience and Remote Sensing Letters, 2013, 10, 1070-1074.	3.1	21
13	Highly-Parallel GPU Architecture for Lossy Hyperspectral Image Compression. IEEE Journal of Selected Topics in Applied Earth Observations and Remote Sensing, 2013, 6, 670-681.	4.9	37
14	FPGA implementation of a lossy compression algorithm for hyperspectral images with a high-level synthesis tool. , 2013, , .		9
15	A hierarchical scheduling and management solution for dynamic reconfiguration in FPGA-based embedded systems. , 2013, , .		1
16	High level modular implementation of a lossy hyperspectral image compression algorithm on a FPGA. , 2013, , .		6
17	Lossy hyperspectral image compression on a graphics processing unit: parallelization strategy and performance evaluation. Journal of Applied Remote Sensing, 2013, 7, 074599.	1.3	8
18	Performance Evaluation of the H.264/AVC Video Coding Standard for Lossy Hyperspectral Image Compression. IEEE Journal of Selected Topics in Applied Earth Observations and Remote Sensing, 2012, 5, 451-461.	4.9	48

#	Article	IF	Citations
19	A Novel Architecture for Hyperspectral Endmember Extraction by Means of the Modified Vertex Component Analysis (MVCA) Algorithm. IEEE Journal of Selected Topics in Applied Earth Observations and Remote Sensing, 2012, 5, 1837-1848.	4.9	27
20	Special Issue on Design of Circuits and Integrated Systems. Microprocessors and Microsystems, 2012, 36, 333.	2.8	0
21	A Low-Computational-Complexity Algorithm for Hyperspectral Endmember Extraction: Modified Vertex Component Analysis. IEEE Geoscience and Remote Sensing Letters, 2012, 9, 502-506.	3.1	30
22	Lossy hyperspectral image compression with state-of-the-art video encoder. Proceedings of SPIE, 2011, , .	0.8	0
23	Performance analysis of the scalable video coding (SVC) extension of H.264/AVC for constrained scenarios. Proceedings of SPIE, 2011, , .	0.8	2
24	A novel real-time DSP-based video super-resolution system. IEEE Transactions on Consumer Electronics, 2009, 55, 2264-2270.	3.6	13
25	Survey of reconfigurable architectures for multimedia applications. Proceedings of SPIE, 2009, , .	0.8	9
26	Anisotropic quality measurement applied to H.264 video compression. , 2009, , .		3
27	A flexible template for H.264/AVC block matching motion estimation architectures. IEEE Transactions on Consumer Electronics, 2008, 54, 845-851.	3.6	4
28	Analysis of fast block matching motion estimation algorithms for video super-resolution systems. IEEE Transactions on Consumer Electronics, 2008, 54, 1430-1438.	3.6	42
29	Impact of Fast Motion Estimation Algorithms on Super-Resolved Video Sequences. , 2008, , .		0
30	Grouped Approach for the Design of H.264/AVC Motion Estimation Architectures. ETRI Journal, 2008, 30, 862-864.	2.0	1
31	Toward the implementation of a baseline H.264/AVC decoder onto a reconfigurable architecture. , 2007, , .		0
32	A Novel High Performance Architecture for H.264/AVC Deblocking Filtering. ETRI Journal, 2007, 29, 396-398.	2.0	3
33	Low-Cost Super-Resolution Algorithms Implementation Over a HW/SW Video Compression Platform. Eurasip Journal on Advances in Signal Processing, 2006, 2006, 1.	1.7	8
34	<title>A quarter pixel precision motion estimation architecture for H.264/AVC video coding</title>. , 2005, , .		1
35	<title>A low-cost bidimensional smart pixel network for video coding operations</title>. , 2005, , .		0
36	<title>Practical considerations for real-time super-resolution implementation techniques over video coding platforms (Keynote Address)</title>. , 2005, 5837, 613.		1

#	Article	IF	Citations
37	Cost-adaptive motion estimation strategy for high-performance video encoders. Electronics Letters, 2005, 41, 182.	1.0	2
38	Adaptive motion vector post-processing for low cost rate-distortion optimisation. Electronics Letters, 2003, 39, 1720.	1.0	1
39	Cell scheduling for VOQ switches with different strict priority levels. Electronics Letters, 2003, 39, 580.	1.0	1
40	0.25-Î¼m technology arithmetic codec for mobile multimedia communicators. , 2003, , .		0
41	Design of a 270MHz/340mW processing element for high performance motion estimation systems application. Microelectronics Journal, 2002, 33, 1123-1134.	2.0	6
42	Low power, high speed, charge recycling CMOS threshold logic gate. Electronics Letters, 2001, 37, 1067.	1.0	38
43	Gallium arsenide processing elements for motion estimation full-search algorithm. , 2001, , .		0
44	Gallium arsenide multiplierless filter bank for two-dimensional discrete wavelet transform (2D-DWT) computation. , 2001, , .		0
45	<title>Novel extension of neu-MOS techniques to neu-GaAs</title>. , 1999, , .		0
46	A CORDIC processor for FFT computation and its implementation using gallium arsenide technology. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 1998, 6, 18-30.	3.1	17
47	Efficient design of gallium arsenide Muller-C element. Electronics Letters, 1997, 33, 757.	1.0	4
48	Noise margin enhancement in GaAs ROM's using current mode logic. IEEE Journal of Solid-State Circuits, 1997, 32, 592-597.	5.4	3
49	GaAs pseudodynamic latched logic for high performance processor cores. IEEE Journal of Solid-State Circuits, 1997, 32, 1297-1303.	5.4	12
50	GaAs ICs for 10 Gb/s ATM switching. , 0, , .		0
51	Low power techniques for digital GaAs VLSI. , 0, , .		0
52	Low-Cost Implementation of a Super-Resolution Algorithm for Real-Time Video Applications. , 0, , .		5
53	A High Quality/Low Computational Cost Technique for Block Matching Motion Estimation. , 0, , .		2
54	Low Cost Efficient Architecture for H.264 Motion Estimation. , 0, , .		11