Sadok Aouini

List of Publications by Year in descending order

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2258059 1720034 10 48 3 7 citations h-index g-index papers 10 10 10 62 docs citations times ranked citing authors all docs

#	Article	IF	CITATIONS
1	A Memory-Based Direct-Digital Frequency Synthesizer for Fractional Synchronization. IEEE Transactions on Circuits and Systems II: Express Briefs, 2022, 69, 899-903.	3.0	3
2	An Area-Efficient High-Resolution Segmented $\hat{l}\hat{E}\hat{l}$ "-DAC for Built-In Self-Test Applications. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2021, 29, 1861-1874.	3.1	8
3	Using Optimized Butterworth-Based \$SigmaDelta\$ Bitstreams for the Testing of High-Resolution Data Converters. , 2020, , .		1
4	Optimized Periodic \hat{l} £ \hat{l} " Bitstreams for DC Signal Generation Used in Dynamic Calibration Applications. IEEE Open Journal of Circuits and Systems, 2020, 1, 3-12.	1.9	4
5	Low-Power Built-in Jitter Injection Using Linearized Phase Interpolator. IEEE Transactions on Instrumentation and Measurement, 2020, 69, 6112-6120.	4.7	2
6	Adaptive Coherent Receiver Settings for Optimum Channel Spacing in Gridless Optical Networks. Future Internet, 2019, 11, 206.	3.8	3
7	An Under Sampling Scope for Characterization of 42-Gs/s DAC in 28-nm FD-SOI. IEEE Microwave and Wireless Components Letters, 2018, 28, 621-623.	3.2	2
8	Temporal epilepsy seizures monitoring and prediction using crossâ€correlation and chaos theory. Healthcare Technology Letters, 2014, 1, 45-50.	3.3	15
9	High Speed On-Chip Signal Generation for Debug and Diagnosis. Journal of Electronic Testing: Theory and Applications (JETTA), 2012, 28, 625-640.	1.2	1
10	Mixed-Signal Production Test: A Measurement Principle Perspective. IEEE Design and Test of Computers, 2009, 26, 48-62.	1.0	9