

Jian Pang

List of Publications by Year in descending order

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687363

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times ranked

743
citing authors

#	ARTICLE	IF	CITATIONS
1	A 28-GHz CMOS Phased-Array Transceiver Based on LO Phase-Shifting Architecture With Gain Invariant Phase Tuning for 5G New Radio. IEEE Journal of Solid-State Circuits, 2019, 54, 1228-1242.	5.4	133
2	A 39-GHz 64-Element Phased-Array Transceiver With Built-In Phase and Amplitude Calibrations for Large-Array 5G NR in 65-nm CMOS. IEEE Journal of Solid-State Circuits, 2020, 55, 1249-1269.	5.4	127
3	64-QAM 60-GHz CMOS Transceivers for IEEE 802.11ad/ay. IEEE Journal of Solid-State Circuits, 2017, 52, 2871-2891.	5.4	95
4	A 28-GHz CMOS Phased-Array Beamformer Utilizing Neutralized Bi-Directional Technique Supporting Dual-Polarized MIMO for 5G NR. IEEE Journal of Solid-State Circuits, 2020, 55, 2371-2386.	5.4	91
5	A 120Gb/s 16QAM CMOS millimeter-wave wireless transceiver. , 2018, , .		73
6	A 50.1-Gb/s 60-GHz CMOS Transceiver for IEEE 802.11ay With Calibration of LO Feedthrough and I/Q Imbalance. IEEE Journal of Solid-State Circuits, 2019, 54, 1375-1390.	5.4	61
7	21.1 A 28GHz CMOS Phased-Array Beamformer Utilizing Neutralized Bi-Directional Technique Supporting Dual-Polarized MIMO for 5G NR. , 2019, , .		39
8	A CMOS Dual-Polarized Phased-Array Beamformer Utilizing Cross-Polarization Leakage Cancellation for 5G MIMO Systems. IEEE Journal of Solid-State Circuits, 2021, 56, 1310-1326.	5.4	39
9	13.3 A 56Gb/s W-band CMOS wireless transceiver. , 2016, , .		36
10	24.9 A 128-QAM 60GHz CMOS transceiver for IEEE802.11ay with calibration of LO feedthrough and I/Q imbalance. , 2017, , .		31
11	A 265- μ W Fractional- N Digital PLL With Seamless Automatic Switching Sub-Sampling/Sampling Feedback Path and Duty-Cycled Frequency-Locked Loop in 65-nm CMOS. IEEE Journal of Solid-State Circuits, 2019, 54, 3478-3492.	5.4	29
12	A 60-GHz 3.0-Gb/s Spectrum Efficient BPOOK Transceiver for Low-Power Short-Range Wireless in 65-nm CMOS. IEEE Journal of Solid-State Circuits, 2019, 54, 1363-1374.	5.4	29
13	A Fully Synthesizable Fractional- N MDLL With Zero-Order Interpolation-Based DTC Nonlinearity Calibration and Two-Step Hybrid Phase Offset Calibration. IEEE Transactions on Circuits and Systems I: Regular Papers, 2021, 68, 603-616.	5.4	27
14	A 28GHz CMOS Phased-Array Transceiver Featuring Gain Invariance Based on LO Phase Shifting Architecture with 0.1-Degree Beam-Steering Resolution for 5G New Radio. , 2018, , .		25
15	A 273-301-GHz Amplifier With 21-dB Peak Gain in 65-nm Standard Bulk CMOS. IEEE Microwave and Wireless Components Letters, 2019, 29, 342-344.	3.2	23
16	A 1.2ps-jitter fully-synthesizable fully-calibrated fractional-N injection-locked PLL using true arbitrary nonlinearity calibration technique. , 2018, , .		16
17	A 39GHz 64-Element Phased-Array CMOS Transceiver with Built-in Calibration for Large-Array 5G NR. , 2019, , .		16
18	A 28.16-Gb/s Area-Efficient 60-GHz CMOS Bidirectional Transceiver for IEEE 802.11ay. IEEE Transactions on Microwave Theory and Techniques, 2020, 68, 252-263.	4.6	16

#	ARTICLE	IF	CITATIONS
19	A Fully-Synthesizable Fractional-N Injection-Locked PLL for Digital Clocking with Triangle/Sawtooth Spread-Spectrum Modulation Capability in 5-nm CMOS. IEEE Solid-State Circuits Letters, 2020, 3, 34-37.	2.0	16
20	A Bi-Directional 300-GHz-Band Phased-Array Transceiver in 65-nm CMOS With Outphasing Transmitting Mode and LO Emission Cancellation. IEEE Journal of Solid-State Circuits, 2022, 57, 2292-2308.	5.4	16
21	An HDL-described Fully-synthesizable Sub-GHz IoT Transceiver with Ring Oscillator based Frequency Synthesizer and Digital Background EVM Calibration. , 2019, , .		13
22	A 28GHz CMOS Phase Shifter Supporting 11.2Gb/s in 256QAM with an RMS Gain Error of 0.13dB for 5G Mobile Network. , 2018, , .		12
23	A 0.4-ps-Jitter $\hat{\sim}$ 52-dBc-Spur Synthesizable Injection-Locked PLL With Self-Clocked Nonoverlap Update and Slope-Balanced Subsampling BBPD. IEEE Solid-State Circuits Letters, 2019, 2, 5-8.	2.0	12
24	A 32-kHz-Reference 2.4-GHz Fractional- N Oversampling PLL With 200-kHz Loop Bandwidth. IEEE Journal of Solid-State Circuits, 2021, 56, 3741-3755.	5.4	11
25	A 28-GHz CMOS Phased-Array Beamformer Supporting Dual-Polarized MIMO with Cross-Polarization Leakage Cancellation. , 2020, , .		10
26	A Ka -Band SATCOM Transceiver in 65-nm CMOS With High-Linearity TX and Dual-Channel Wide-Dynamic-Range RX for Terrestrial Terminal. IEEE Journal of Solid-State Circuits, 2022, 57, 356-370.	5.4	9
27	A Ka -Band 16-Element Deployable Active Phased Array Transmitter for Satellite Communication. , 2021, , .		9
28	A 0.85mm ² BLE Transceiver Using an On-Chip Harmonic-Suppressed RFIO Circuitry With T/R Switch. IEEE Transactions on Circuits and Systems I: Regular Papers, 2021, 68, 196-209.	5.4	8
29	A CMOS Ka -Band SATCOM Transceiver with ACI-Cancellation Enhanced Dual-Channel Low-NF Wide-Dynamic-Range RX and High-Linearity TX. , 2020, , .		7
30	A 28-GHz Phased-Array Relay Transceiver for 5G Network Using Vector-Summing Backscatter With 24-GHz Wireless Power and LO Transfer. IEEE Journal of Solid-State Circuits, 2022, 57, 1211-1223.	5.4	7
31	A 28GHz CMOS Differential Bi-Directional Amplifier for 5G NR. , 2020, , .		6
32	A 3.4mW/element Radiation-Hardened Ka -Band CMOS Phased-Array Receiver Utilizing Magnetic-Tuning Phase Shifter for Small Satellite Constellation. , 2022, , .		6
33	Ultra-high-data-rate 60-GHz CMOS transceiver for future radio access network. , 2017, , .		5
34	An LO-buffer-less 60-GHz CMOS transmitter with oscillator pulling mitigation. , 2016, , .		4
35	28GHz Phase Shifter with Temperature Compensation for 5G NR Phased-array Transceiver. , 2021, , .		3
36	A CMOS 24-30-GHz Low-Phase-Variation Variable Gain Amplifier Design for 5G New Radio. IEEE Solid-State Circuits Letters, 2022, 5, 146-149.	2.0	3

#	ARTICLE	IF	CITATIONS
37	A 100mW 3.0 Gb/s spectrum efficient 60 GHz Bi-Phase OOK CMOS transceiver. , 2017, , .		2
38	A 78 fs RMS Jitter Injection-Locked Clock Multiplier Using Transformer-Based Ultra-Low-Power VCO. , 2019, , .		2
39	A Compact 37-40GHz CMOS Switch-Type Phase Shifter with Fine-Tuning Stage Achieving 0.4-dB RMS Gain Error. , 2020, , .		2
40	A Low-Jitter Injection-Locked Clock Multiplier Using 97- $\frac{1}{4}$ W Transformer-Based VCO with 18-kHz Flicker Noise Corner. IEICE Transactions on Electronics, 2021, E104.C, .	0.6	1
41	A High Accuracy Phase and Amplitude Detection Circuit for Calibration of 28GHz Phased Array Beamformer System. , 2021, , .		1
42	A 28-GHz CMOS Vector-Summing Phase Shifter Featuring I/Q Imbalance Calibration Supporting 11.2Gb/s in 256QAM for 5G New Radio. IEICE Transactions on Electronics, 2020, E103.C, 39-47.	0.6	1
43	A 39GHz Divide-by-8 LC-Ring ILFD Designed for 5G New Radio n260 Band. , 2021, , .		1
44	An 8.5-dB Insertion Loss and 0.8 $\hat{\text{A}}$ $^{\circ}$ RMS Phase Error Ka-Band CMOS Hybrid Phase Shifter Featuring Nonuniform Matching for Satellite Communication. IEICE Transactions on Electronics, 2022, E105.C, 552-560.	0.6	1
45	Substrate noise isolation improvement by helium-3 ion irradiation technique in a triple-well CMOS process. , 2015, , .		0
46	A 28.16-Gb/s Area-Efficient 60GHz CMOS Bi-Directional Transceiver for IEEE 802.11ay. , 2018, , .		0
47	A Compact and High-Resolution CMOS Switch-Type Phase Shifter Achieving 0.4-dB RMS Gain Error for 5G n260 Band. IEICE Transactions on Electronics, 2022, E105.C, 102-109.	0.6	0
48	A Low-Power Pulse-Shaped Duobinary ASK Modulator for IEEE 802.11ad Compliant 60GHz Transmitter in 65nm CMOS. IEICE Transactions on Electronics, 2018, E101.C, 126-134.	0.6	0
49	A Compact TF-Based LC-VCO with Ultra-Low-Power Operation and Supply Pushing Reduction for IoT Applications. IEICE Transactions on Electronics, 2020, E103.C, 505-513.	0.6	0