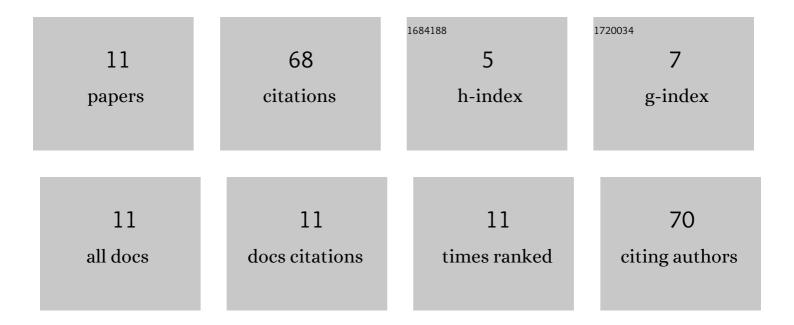
Marvin Damschen

List of Publications by Year in descending order

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#	Article	IF	CITATIONS
1	i-Core: A Runtime-Reconfigurable Processor Platform for Cyber-Physical Systems. , 2020, , 1-36.		2
2	Analyses and architectures for mixed-critical systems. , 2019, , .		0
3	Preemption of the Partial Reconfiguration Process to Enable Real-Time Computing With FPGAs. ACM Transactions on Reconfigurable Technology and Systems, 2018, 11, 1-24.	2.5	16
4	Co-Scheduling on Fused CPU-GPU Architectures With Shared Last Level Caches. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 2018, 37, 2337-2347.	2.7	6
5	Shallow Water Waves on a Deep Technology Stack: Accelerating a Finite Volume Tsunami Model Using Reconfigurable Hardware in Invasive Computing. Lecture Notes in Computer Science, 2018, , 676-687.	1.3	2
6	Timing Analysis of Tasks on Runtime Reconfigurable Processors. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2017, 25, 294-307.	3.1	10
7	CoRQ: Enabling Runtime Reconfiguration Under WCET Guarantees for Real-Time Systems. IEEE Embedded Systems Letters, 2017, 9, 77-80.	1.9	12
8	Auto-SI: An adaptive reconfigurable processor with run-time loop detection and acceleration. , 2017, , .		5
9	Invasive computing for timing-predictable stream processing on MPSoCs. IT - Information Technology, 2016, 58, 267-280.	0.9	5
10	Extending the WCET Problem to Optimize for Runtime-Reconfigurable Processors. Transactions on Architecture and Code Optimization, 2016, 13, 1-24.	2.0	7
11	Floating point acceleration for stream processing applications in dynamically reconfigurable processors. , 2015, , .		3