

# Lucas Cordeiro

## List of Publications by Year in descending order

Source: <https://exaly.com/author-pdf/5379980/publications.pdf>

Version: 2024-02-01

92  
papers

1,119  
citations

567144

15  
h-index

580701

25  
g-index

98  
all docs

98  
docs citations

98  
times ranked

498  
citing authors

#	ARTICLE	IF	CITATIONS
1	Model checking C++ programs. Software Testing Verification and Reliability, 2022, 32, e1793.	1.7	11
2	FuSeBMC v4: Smart Seed Generation for Hybrid Fuzzing. Lecture Notes in Computer Science, 2022, , 336-340.	1.0	6
3	Wit4Java: A Violation-Witness Validator for Java Verifiers (Competition Contribution). Lecture Notes in Computer Science, 2022, , 484-489.	1.0	4
4	Summary of Model Checking C++ Programs. , 2022, , .		0
5	ESBMC-Solidity: An SMT-Based Model Checker for Solidity Smart Contracts. , 2022, , .		2
6	ESBMC-Jimple: verifying Kotlin programs via jimple intermediate representation. , 2022, , .		0
7	ESBMC-CHERI: towards verification of C programs for CHERI platforms with ESBMC. , 2022, , .		2
8	ESBMC 6.1: automated test case generation using bounded model checking. International Journal on Software Tools for Technology Transfer, 2021, 23, 857-861.	1.7	8
9	Verification and refutation of C programs based on k-induction and invariant inference. International Journal on Software Tools for Technology Transfer, 2021, 23, 115-135.	1.7	9
10	FuSeBMC: A White-Box Fuzzer for Finding Security Vulnerabilities in C Programs (Competition) Tj ETQq0 0 0 rgBT /Overlock 10 Tf 50 38	1.0	7
11	FuSeBMC: An Energy-Efficient Test Generator for Finding Security Vulnerabilities in C Programs. Lecture Notes in Computer Science, 2021, , 85-105.	1.0	10
12	Hardware Update through Digital TV Signals. Electronics (Switzerland), 2021, 10, 3072.	1.8	1
13	Assisted Counterexample-Guided Inductive Optimization for Robot Path Planning. , 2021, , .		0
14	Automated formal synthesis of provably safe digital controllers for continuous plants. Acta Informatica, 2020, 57, 223-244.	0.5	4
15	Formal Non-fragile Verification of Step Response Requirements for Digital State-Feedback Control Systems. Journal of Control, Automation and Electrical Systems, 2020, 31, 557-573.	1.2	1
16	Map2Check: Using Symbolic Execution and Fuzzing. Lecture Notes in Computer Science, 2020, , 403-407.	1.0	6
17	An Efficient Floating-Point Bit-Blasting API for Verifying C Programs. Lecture Notes in Computer Science, 2020, , 178-195.	1.0	2
18	Survey on automated symbolic verification and its application for synthesising cyber-physical systems. IET Cyber-Physical Systems: Theory and Applications, 2020, 5, 1-24.	1.9	14

#	ARTICLE	IF	CITATIONS
19	Synthesis of Solar Photovoltaic Systems: Optimal Sizing Comparison. Lecture Notes in Computer Science, 2020, , 87-105.	1.0	0
20	Automated formal verification of stand-alone solar photovoltaic systems. Solar Energy, 2019, 193, 684-691.	2.9	17
21	Verifying fragility in digital systems with uncertainties using DSVerifier v2.0. Journal of Systems and Software, 2019, 153, 22-43.	3.3	6
22	JBMC: Bounded Model Checking for Java Bytecode. Lecture Notes in Computer Science, 2019, , 219-223.	1.0	14
23	Incremental Bounded Model Checking of Artificial Neural Networks in CUDA. , 2019, , .		3
24	ESBMC v6.0: Verifying C Programs Using k-Induction and Invariant Inference. Lecture Notes in Computer Science, 2019, , 209-213.	1.0	31
25	A randomized, multicenter clinical trial to determine the efficacy and safety of pegfilgrastim (GEMA) Tj ETQq1 1 0.784314 rgBT /Overl... patients with breast cancer.. Journal of Clinical Oncology, 2019, 37, 3113-3113.	0.8	0
26	Map2Check Using LLVM and KLEE. Lecture Notes in Computer Science, 2018, , 437-441.	1.0	4
27	Counterexample guided inductive optimization based on satisfiability modulo theories. Science of Computer Programming, 2018, 165, 3-23.	1.5	3
28	ESBMC-GPU A context-bounded model checking tool to verify CUDA programs. Science of Computer Programming, 2018, 152, 63-69.	1.5	12
29	DSVerifier-Aided Verification Applied to Attitude Control Software in Unmanned Aerial Vehicles. IEEE Transactions on Reliability, 2018, 67, 1420-1441.	3.5	11
30	Sim3Tanks: A Benchmark Model Simulator for Process Control and Monitoring. IEEE Access, 2018, 6, 62234-62254.	2.6	12
31	DSValidator. , 2018, , .		1
32	Bounded model checking of C++ programs based on the Qt cross-platform framework (journal-first) Tj ETQq0 0 0 rgBT /Overl... 10 Tf 5		
33	Towards counterexample-guided k-induction for fast bug detection. , 2018, , .		1
34	ESBMC 5.0: an industrial-strength C model checker. , 2018, , .		46
35	JBMC: A Bounded Model Checking Tool for Verifying Java Bytecode. Lecture Notes in Computer Science, 2018, , 183-190.	1.0	42
36	Handling loops in bounded model checking of C programs via k-induction. International Journal on Software Tools for Technology Transfer, 2017, 19, 97-114.	1.7	51

#	ARTICLE	IF	CITATIONS
37	Bounded model checking of C++ programs based on the Qt cross-platform framework. Software Testing Verification and Reliability, 2017, 27, e1632.	1.7	13
38	A method to localize faults in concurrent C programs. Journal of Systems and Software, 2017, 132, 336-352.	3.3	6
39	Automated Formal Synthesis of Digital Controllers for State-Space Physical Plants. Lecture Notes in Computer Science, 2017, , 462-482.	1.0	19
40	BMCLua. Software Engineering Notes: an Informal Newsletter of the Special Interest Committee on Software Engineering / ACM, 2017, 42, 1-10.	0.5	1
41	Verifying digital systems with MATLAB. , 2017, , .		6
42	Formal Non-Fragile Stability Verification of Digital Control Systems with Uncertainty. IEEE Transactions on Computers, 2017, 66, 545-552.	2.4	19
43	SMT-based context-bounded model checking for CUDA programs. Concurrency Computation Practice and Experience, 2017, 29, e3934.	1.4	13
44	A Power Control and Anticollision Mechanism for RFID Systems. IEEE Latin America Transactions, 2017, 15, 1933-1940.	1.2	2
45	Counterexample guided inductive optimization applied to mobile robots path planning. , 2017, , .		0
46	Planning and Evaluation of UAV Mission Planner for Intralogistics Problems. , 2017, , .		5
47	DSSynth: An automated digital controller synthesis tool for physical plants. , 2017, , .		4
48	Multi-core model checking and maximum satisfiability applied to hardware-software partitioning. International Journal of Embedded Systems, 2017, 9, 570.	0.2	1
49	Encoding Floating-Point Numbers Using the SMT Theory in ESBMC: An Empirical Evaluation over the SV-COMP Benchmarks. Lecture Notes in Computer Science, 2017, , 91-106.	1.0	3
50	OptCE: A Counterexample-Guided Inductive Optimization Solver. Lecture Notes in Computer Science, 2017, , 125-141.	1.0	2
51	DepthK: A k-Induction Verifier Based on Invariant Inference for C Programs. Lecture Notes in Computer Science, 2017, , 360-364.	1.0	19
52	Sound and Automated Synthesis of Digital Stabilizing Controllers for Continuous Plants. , 2017, , .		10
53	Model Checking Embedded C Software Using k-Induction and Invariants. Embedded Systems, 2017, , 159-182.	0.6	1
54	Hunting Memory Bugs in C Programs with Map2Check. Lecture Notes in Computer Science, 2016, , 934-937.	1.0	7

#	ARTICLE	IF	CITATIONS
55	Bounded model checking for fixed-point digital filters. Journal of the Brazilian Computer Society, 2016, 22, .	0.8	12
56	Complementary training programme for electrical and computer engineering students through an industrial-academic collaboration. , 2016, , .		2
57	ESBMC $Q^tOM$ : A Bounded Model Checking Tool to Verify Qt Applications. Lecture Notes in Computer Science, 2016, , 97-103.	1.0	4
58	SMT-Based Context-Bounded Model Checking for Embedded Systems. Software Engineering Notes: an Informal Newsletter of the Special Interest Committee on Software Engineering / ACM, 2016, 41, 1-6.	0.5	2
59	Verification of fixed-point digital controllers using direct and delta forms realizations. Design Automation for Embedded Systems, 2016, 20, 95-126.	0.7	19
60	Applying SMT-based verification to hardware/software partitioning in embedded systems. Design Automation for Embedded Systems, 2016, 20, 1-19.	0.7	18
61	Verifying CUDA programs using SMT-based context-bounded model checking. , 2016, , .		12
62	Fault Localization in Multi-threaded C Programs Using Bounded Model Checking. , 2015, , .		6
63	Model Checking Embedded C Software Using k-Induction and Invariants. , 2015, , .		18
64	Hardware reconfiguration based on broadcasted digital TV signal. , 2015, , .		0
65	Applying Multi-core Model Checking to Hardware-Software Partitioning in Embedded Systems. , 2015, , .		2
66	Model checking LTL properties over ANSI-C programs with bounded traces. Software and Systems Modeling, 2015, 14, 65-81.	2.2	18
67	A secondary screen architecture to accurately capture viewers' interactions in an iTV environment. , 2014, , .		0
68	Verification of Delta Form Realization in Fixed-Point Digital Controllers Using Bounded Model Checking. , 2014, , .		7
69	SMT-based bounded model checking of fixed-point digital controllers. , 2014, , .		7
70	Applying symbolic bounded model checking to the 2012 RERS greybox challenge. International Journal on Software Tools for Technology Transfer, 2014, 16, 519-529.	1.7	7
71	BMCLua: Verification of Lua programs in digital TV interactive applications. , 2014, , .		2
72	ESBMC 1.22. Lecture Notes in Computer Science, 2014, , 405-407.	1.0	26

#	ARTICLE	IF	CITATIONS
73	Dynamic and automated product derivation for consumer electronics software applications. IEEE Transactions on Consumer Electronics, 2013, 59, 883-891.	3.0	0
74	SMT-Based Bounded Model Checking of C++ Programs. , 2013, , .		19
75	Handling Unbounded Loops with ESBMC 1.20. Lecture Notes in Computer Science, 2013, , 619-622.	1.0	18
76	A car racing based strategy for the Dynamic Voltage and Frequency Scaling technique. , 2012, , .		1
77	SMT-Based Bounded Model Checking for Embedded ANSI-C Software. IEEE Transactions on Software Engineering, 2012, 38, 957-974.	4.3	120
78	Context-Bounded Model Checking with ESBMC 1.17. Lecture Notes in Computer Science, 2012, , 534-537.	1.0	28
79	Gift young engineers: An extra-curricular initiative for updating computer and electrical engineering courses. , 2011, , .		3
80	Verifying Embedded C Software with Timing Constraints Using an Untimed Bounded Model Checker. , 2011, , .		2
81	Verifying multi-threaded software using smt-based context-bounded model checking. , 2011, , .		93
82	Context-Bounded Model Checking of LTL Properties for ANSI-C Software. Lecture Notes in Computer Science, 2011, , 302-317.	1.0	10
83	SMT-based bounded model checking for multi-threaded software in embedded systems. , 2010, , .		17
84	Continuous Verification of Large Embedded Software Using SMT-Based Bounded Model Checking. , 2010, , .		7
85	Semiformal Verification of Embedded Software in Medical Devices Considering Stringent Hardware Constraints. , 2009, , .		29
86	SMT-Based Bounded Model Checking for Embedded ANSI-C Software. , 2009, , .		69
87	ezRealtime: A Domain-Specific Modeling Tool for Embedded Hard Real-Time Software Synthesis. , 2008, , .		0
88	Towards a model-driven engineering approach for developing embedded hard real-time software. , 2008, , .		1
89	A Platform-Based Software Design Methodology for Embedded Control Systems: An Agile Toolkit. , 2008, , .		4
90	Mandos: A User Interaction Method in Embedded Applications for Mobile Telephony. , 2008, , .		4

#	ARTICLE	IF	CITATIONS
91	Agile Development Methodology for Embedded Systems: A Platform-Based Design Approach. , 2007, , .		13
92	TXM. Software Engineering Notes: an Informal Newsletter of the Special Interest Committee on Software Engineering / ACM, 2007, 32, 4.	0.5	6