

# Qinghui Hong

## List of Publications by Year in descending order

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Version: 2024-02-01

29  
papers

844  
citations

516561

16  
h-index

477173

29  
g-index

29  
all docs

29  
docs citations

29  
times ranked

329  
citing authors

#	ARTICLE	IF	CITATIONS
1	Memristive Circuit Implementation of a Self-Repairing Network Based on Biological Astrocytes in Robot Application. IEEE Transactions on Neural Networks and Learning Systems, 2022, 33, 2106-2120.	7.2	36
2	Memristor-based circuit implementation of Competitive Neural Network based on online unsupervised Hebbian learning rule for pattern recognition. Neural Computing and Applications, 2022, 34, 319-331.	3.2	14
3	Multilayer Memristive Neural Network Circuit Based on Online Learning for License Plate Detection. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 2022, 41, 3000-3011.	1.9	18
4	A surrogate-based parallel optimization of analog circuits using multi-acquisition functions. AEU - International Journal of Electronics and Communications, 2022, 146, 154105.	1.7	4
5	One-Step Calculation Circuit of FFT and Its Application. IEEE Transactions on Circuits and Systems I: Regular Papers, 2022, 69, 2781-2793.	3.5	6
6	A multi-value 3D crossbar array nonvolatile memory based on pure memristors. European Physical Journal: Special Topics, 2022, 231, 3119-3130.	1.2	11
7	A memristor-based circuit design and implementation for blocking on Pavlov associative memory. Neural Computing and Applications, 2022, 34, 14745-14761.	3.2	7
8	A Memristive Circuit Implementation of Eyes State Detection in Fatigue Driving Based on Biological Long Short-Term Memory Rule. IEEE/ACM Transactions on Computational Biology and Bioinformatics, 2021, 18, 2218-2229.	1.9	5
9	Research on cascading high-dimensional isomorphic chaotic maps. Cognitive Neurodynamics, 2021, 15, 157-167.	2.3	10
10	A Novel Memristive Chaotic Neuron Circuit and Its Application in Chaotic Neural Networks for Associative Memory. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 2021, 40, 521-532.	1.9	36
11	Memristive self-learning logic circuit with application to encoder and decoder. Neural Computing and Applications, 2021, 33, 4901-4913.	3.2	34
12	Solving Non-Homogeneous Linear Ordinary Differential Equations Using Memristor-Capacitor Circuit. IEEE Transactions on Circuits and Systems I: Regular Papers, 2021, 68, 4495-4507.	3.5	10
13	Competitive Neural Network Circuit Based on Winner-Take-All Mechanism and Online Hebbian Learning Rule. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2021, 29, 1095-1107.	2.1	5
14	Neural Bursting and Synchronization Emulated by Neural Networks and Circuits. IEEE Transactions on Circuits and Systems I: Regular Papers, 2021, 68, 3397-3410.	3.5	71
15	Memristor-based neural network circuit with weighted sum simultaneous perturbation training and its applications. Neurocomputing, 2021, 462, 581-590.	3.5	38
16	A memristor-based circuit design of pavlov associative memory with secondary conditional reflex and its application. Neurocomputing, 2021, 463, 341-354.	3.5	14
17	An Extremely Simple Multiwing Chaotic System: Dynamics Analysis, Encryption Application, and Hardware Implementation. IEEE Transactions on Industrial Electronics, 2021, 68, 12708-12719.	5.2	79
18	Memristive continuous Hopfield neural network circuit for image restoration. Neural Computing and Applications, 2020, 32, 8175-8185.	3.2	32

#	ARTICLE	IF	CITATIONS
19	Memristive Circuit Implementation of Biological Nonassociative Learning Mechanism and Its Applications. IEEE Transactions on Biomedical Circuits and Systems, 2020, 14, 1036-1050.	2.7	66
20	Composite One- to Six-Scroll Hidden Attractors in a Memristor-Based Chaotic System and Their Circuit Implementation. Complexity, 2020, 2020, 1-13.	0.9	1
21	Locally Active Memristor with Three Coexisting Pinched Hysteresis Loops and Its Emulator Circuit. International Journal of Bifurcation and Chaos in Applied Sciences and Engineering, 2020, 30, 2050184.	0.7	39
22	A Multi-Stable Memristor and its Application in a Neural Network. IEEE Transactions on Circuits and Systems II: Express Briefs, 2020, 67, 3472-3476.	2.2	105
23	A Serial Access Scheme Design on Memristor-CMOS Hybrid Memory. IEEE Access, 2020, 8, 35031-35037.	2.6	3
24	A Versatile Pulse Control Method to Generate Arbitrary Multidirection Multibutterfly Chaotic Attractors. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 2019, 38, 1480-1492.	1.9	27
25	Memristive Circuit Design of Emotional Generation and Evolution Based on Skin-Like Sensory Processor. IEEE Transactions on Biomedical Circuits and Systems, 2019, 13, 631-644.	2.7	52
26	Constructing multi-butterfly attractors based on Sprott C system via non-autonomous approaches. Chaos, 2019, 29, 043112.	1.0	22
27	Novel Nonlinear Function Shift Method for Generating Multiscroll Attractors Using Memristor-Based Control Circuit. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2019, 27, 1174-1185.	2.1	17
28	Novel circuit designs of memristor synapse and neuron. Neurocomputing, 2019, 330, 11-16.	3.5	46
29	Novel designs of spiking neuron circuit and STDP learning circuit based on memristor. Neurocomputing, 2018, 314, 207-214.	3.5	36