

# John Hu

## List of Publications by Year in descending order

Source: <https://exaly.com/author-pdf/5350970/publications.pdf>

Version: 2024-02-01

13  
papers

43  
citations

2682572

2  
h-index

2917675

2  
g-index

13  
all docs

13  
docs citations

13  
times ranked

32  
citing authors

| #  | ARTICLE   | IF  | CITATIONS |
|----|---|-----|-----------|
| 1  | Industry-Oriented Laboratory Development for Mixed-Signal IC Test Education. IEEE Transactions on Education, 2010, 53, 662-671.   | 2.4 | 9         |
| 2  | A 500 nA quiescent, 100 mA maximum load CMOS low-dropout regulator. , 2011, , .   |     | 9         |
| 3  | Sleep-mode ready, area efficient capacitor-free low-dropout regulator with input current-differencing. Analog Integrated Circuits and Signal Processing, 2010, 63, 107-112. | 1.4 | 6         |
| 4  | 50 nA, 1 V nanowatt resistor-free compact CMOS current references. , 2010, , .  |     | 5         |
| 5  | Increasing sleep-mode efficiency by reducing battery current using a DC-DC converter. , 2010, , .   |     | 5         |
| 6  | A true zero-load stable CMOS capacitor-free low-dropout regulator with excessive gain reduction. , 2010, , .  |     | 3         |
| 7  | A Switched-Capacitor Power Side-Channel Attack Detection Circuit in 65-nm CMOS. , 2021, , .   |     | 2         |
| 8  | Power Side-Channel Attack Detection through Battery Impedance Monitoring. , 2021, , .   |     | 2         |
| 9  | Master-slave battery charging system using parallel DC-DC converters for thermal safety. , 2017, , .  |     | 1         |
| 10 | Performance and Noise Trade-off for SC-based Power Side-Channel Attack Detection Circuits. , 2021, , .  |     | 1         |
| 11 | An industry-driven laboratory development for mixed-signal IC test education. , 2010, , .   |     | 0         |
| 12 | Linear Regulators. , 2012, , 35-87.   |     | 0         |
| 13 | System Power Management. , 2012, , 19-32.   |     | 0         |