

Adib Abrishamifar

List of Publications by Year in descending order

Source: <https://exaly.com/author-pdf/534928/publications.pdf>

Version: 2024-02-01

74
papers

639
citations

840776

11
h-index

677142

22
g-index

74
all docs

74
docs citations

74
times ranked

707
citing authors

#	ARTICLE	IF	CITATIONS
1	Fixed Switching Frequency Sliding Mode Control for Single-Phase Unipolar Inverters. IEEE Transactions on Power Electronics, 2012, 27, 2507-2514.	7.9	189
2	Low-frequency current ripple reduction in front-end boost converter with single-phase inverter load. IET Power Electronics, 2012, 5, 1676-1683.	2.1	36
3	Comparison of Electroanesthesia with Chemical Anesthesia (MS222 and Clove Oil) in Rainbow Trout (<i>Oncorhynchus mykiss</i>) using Plasma Cortisol and Glucose Responses as Physiological Stress Indicators. Asian Journal of Animal and Veterinary Advances, 2009, 4, 306-313.	0.0	28
4	Extrinsic Rashba spin-orbit coupling effect on silicene spin polarized field effect transistors. Journal of Physics Condensed Matter, 2017, 29, 145501.	1.8	26
5	A low-power and high-gain fully integrated CMOS LNA. Microelectronics Journal, 2007, 38, 1150-1155.	2.0	23
6	Improved equations of switching loss and conduction loss in SPWM multilevel inverters. , 2016, , .		23
7	A switched-capacitor multilevel inverter for high AC power systems with reduced ripple loss using SPWM technique. , 2015, , .		21
8	A 5-level modified full-bridge stand-alone inverter with reduced number of switches. International Transactions on Electrical Energy Systems, 2018, 28, e2638.	1.9	21
9	Low power and high gain current reuse LNA with modified input matching and inter-stage inductors. Microelectronics Journal, 2008, 39, 1534-1537.	2.0	20
10	Low-Power High-Speed Analog Multiplier/Divider Based on a New Current Squarer Circuit. Arabian Journal for Science and Engineering, 2018, 43, 2909-2918.	3.0	20
11	A New Method for Design of CNFET-Based Quaternary Circuits. Circuits, Systems, and Signal Processing, 2019, 38, 2588-2606.	2.0	20
12	A new simple structure PLL for both single and three phase applications. International Journal of Electrical Power and Energy Systems, 2016, 74, 118-125.	5.5	14
13	Analysis and design of the true piecewise approximation logarithmic amplifiers. Analog Integrated Circuits and Signal Processing, 2012, 72, 193-203.	1.4	13
14	A cross coupled low phase noise oscillator using an output swing enhancement technique. Microelectronics Journal, 2014, 45, 1008-1013.	2.0	11
15	On the design of robust, low power with high noise immunity quaternary circuits. Microelectronics Journal, 2020, 102, 104774.	2.0	11
16	A low voltage highly linear CMOS up conversion mixer based on current conveyor. IEICE Electronics Express, 2009, 6, 930-935.	0.8	10
17	A 5.5-GHz 3mW LNA and inductive degenerative CMOS LNA noise figure calculation. , 2008, , .		9
18	A New Topology of Switched-Capacitor Multilevel Inverter for Single-Phase Grid-Connected with Eliminating Leakage Current. , 2018, , .		9

#	ARTICLE	IF	CITATIONS
19	Evaluating a Methodology for Designing CNFET-Based Ternary Circuits. <i>Circuits, Systems, and Signal Processing</i> , 2020, 39, 5039-5058.	2.0	9
20	A Systematic Methodology for Optimal Design of Wireless Power Transfer System Using Genetic Algorithm. <i>Energies</i> , 2020, 13, 383.	3.1	8
21	A new CMOS all-pass phase shifter for phased array systems. <i>IEICE Electronics Express</i> , 2009, 6, 504-510.	0.8	6
22	A novel analog switch for high-precision switched-capacitor applications. <i>International Journal of Circuit Theory and Applications</i> , 2018, 46, 764-778.	2.0	6
23	A programmable true piecewise approximation logarithmic amplifier. , 2009, , .		5
24	A novel current conveyor with high functionality and optimized ports. <i>IEICE Electronics Express</i> , 2010, 7, 1480-1485.	0.8	5
25	A FAST AND LOW SETTling ERROR CONTINUOUS-TIME COMMON-MODE FEEDBACK CIRCUIT BASED ON DIFFERENTIAL DIFFERENCE AMPLIFIER. <i>Journal of Circuits, Systems and Computers</i> , 2014, 23, 1450065.	1.5	5
26	An improved integrated control modeling of a high-power density interleaved non-inverting buck-boost DC-DC converter. <i>World Journal of Engineering</i> , 2018, 15, 688-699.	1.6	5
27	A Simple and Efficient Charge Injection Error Compensation Structure for MOS Sampling Switches. <i>Journal of Circuits, Systems and Computers</i> , 2018, 27, 1850130.	1.5	5
28	An DSTATCOM for Compensating Different Abnormal Line Voltage and Nonlinear Load. , 2006, , .		4
29	Current controlled current differential current conveyor: a novel building block for analog signal processing. <i>IEICE Electronics Express</i> , 2012, 9, 104-110.	0.8	4
30	A novel method for real-time selective harmonic elimination in five-level converters. , 2016, , .		4
31	Simple and efficient design and control of the single phase PWM rectifier for UPS applications. , 2017, , .		4
32	A New Carrier-Based Pulse Width Modulation Scheme with Reduced Output Voltage Total Harmonic Distortion for Multi-Level Inverters. <i>IETE Journal of Research</i> , 2020, , 1-12.	2.6	4
33	A Simple and Adjustable Technique for Effective Linearization of Power Amplifiers Using Harmonic Injection. <i>IEEE Access</i> , 2021, 9, 37287-37296.	4.2	4
34	Flying Capacitor DTC Drive with Reductions in Common Mode Voltage and Stator Overvoltage. <i>Journal of Power Electronics</i> , 2011, 11, 512-519.	1.5	4
35	Jitter peaking investigation in charge pump based clock and data recovery circuits. , 2007, , .		3
36	An FPGA-based irrational decimator for digital receivers. , 2007, , .		3

#	ARTICLE	IF	CITATIONS
37	A novel peak and deep current mode control for two switches buck-boost converter. IEEE Applied Power Electronics Conference and Exposition, 2008, , .	0.0	3
38	A novel ultra low-leakage switch for switched capacitor circuits. , 2009, , .		3
39	System level design and analysis of a fourth-order continuous-time delta-sigma modulator using relaxed gain-bandwidth amplifiers. International Journal of Circuit Theory and Applications, 2017, 45, 1576-1599.	2.0	3
40	An inductorless wideband LNA with a new noise canceling technique. Turkish Journal of Electrical Engineering and Computer Sciences, 2017, 25, 1147-1153.	1.4	3
41	A Novel Wide-Range Delay Cell for DLLs. , 2006, , .		2
42	Generation and detection of nano ultrasound waves with a multiple strained layer structure. Optical and Quantum Electronics, 2008, 40, 577-586.	3.3	2
43	A new high speed, low power adder; using hybrid analog-digital circuits. , 2009, , .		2
44	A low voltage CMOS analog multiplier with high linearity. , 2009, , .		2
45	A low voltage low power CMOS analog multiplier. , 2011, , .		2
46	Fixed frequency sliding mode controller for the buck converter. , 2011, , .		2
47	NONLINEAR OBSERVER FOR INDUCTION MOTOR TO IMPROVE EFFICIENCY AND DYNAMIC STABILITY ANALYSIS IN FOC METHOD. Journal of Circuits, Systems and Computers, 2012, 21, 1250011.	1.5	2
48	A High-Linear CMOS Down Conversion Mixer Using Adjusting the Second and Third-Order Harmonic in Transconductance Stage. Journal of Circuits, Systems and Computers, 2015, 24, 1550002.	1.5	2
49	Implementation of the first commercial medium power active front end transformerless uninterruptible power supply made in Iran. , 2016, , .		2
50	A 500MHz low offset fully differential latched comparator. Analog Integrated Circuits and Signal Processing, 2017, 92, 233-245.	1.4	2
51	Experimentally evaluation and comparison of control methods applied to three phase four-leg inverter for stand-alone applications. , 2018, , .		2
52	Automatic tuning of the Class E power amplifier applied in inductive links during coil separation variations. AEU - International Journal of Electronics and Communications, 2020, 124, 153337.	2.9	2
53	An Optimized Zero-Current, Zero-Voltage, and Three-Level DC-DC Converter. Canadian Journal of Electrical and Computer Engineering, 2021, 44, 216-222.	2.0	2
54	A current source power supply for driving of series connected power switch. , 2008, , .		1

#	ARTICLE	IF	CITATIONS
55	Investigating the linearity of MOSFET-only switched-capacitor ΔΣ modulators under low-voltage condition. , 2009, , .		1
56	Fast SVM for a five level flying capacitor drive with overvoltage reduction. , 2013, , .		1
57	Evaluation of different positive sequence detection structures applied to grid-connected systems. , 2014, , .		1
58	Design procedure and experimental validation of a 30kVA DSP-based PWM rectifier. , 2016, , .		1
59	An efficient interleaved high step-up converter with winding-cross-coupled inductor and common active clamp for photovoltaic applications. , 2017, , .		1
60	A novel OTA compensation approach suitable for CTΔΣ modulators. International Journal of Circuit Theory and Applications, 2018, 46, 2248-2265.	2.0	1
61	An Improved Threefold Interleaved PFC Circuit With Better Voltage Profile and Optimizing Current-Sharing Method. Canadian Journal of Electrical and Computer Engineering, 2021, 44, 253-260.	2.0	1
62	A modified SOGI-FLL algorithm with DC-offset rejection improvement for single-phase inverter applications. International Journal of Dynamics and Control, 2022, 10, 2020-2033.	2.5	1
63	A novel CMOS all-pass tunable phase shifter for phased array systems. , 2009, , .		0
64	A novel CMOS all-pass tunable phase shifter. , 2009, , .		0
65	A new scheme of multilevel inverter with charge balancing control and minimum number of switches. , 2014, , .		0
66	A modified synchronous detection method with power factor control and suitable performance under non-ideal grid voltage applicable for three phase active power filters. , 2014, , .		0
67	A 200 MHz-to-1.4 GHz fast-locking pulse width control loop. International Journal of Electronics, 2014, 101, 341-353.	1.4	0
68	Designing 3-phase inverters with Δ/Y output isolation transformer. , 2014, , .		0
69	A search algorithm to design optimum CTΔΣ modulator with finite GBW amplifiers. , 2016, , .		0
70	Elimination of low order harmonics in nine-level cascaded H-bridge converter. , 2016, , .		0
71	A high precision analog multiplexer for multi-channel neural recording micro-systems. Microelectronics Journal, 2018, 71, 8-18.	2.0	0
72	A Five-Switch Active NPC With Low Output Voltage THD For Photovoltaic Applications. , 2019, , .		0

#	ARTICLE	IF	CITATIONS
73	A wideband quasi-asymmetric Doherty power amplifier with a two-section matching-phase difference compensator network design using GaAs technology. Analog Integrated Circuits and Signal Processing, 2020, 105, 359-370.	1.4	0
74	A Hybrid SMC Strategy for Sequential Switching Shunt Regulator. , 2021, , .		0