

Eric M Vogel

List of Publications by Year in descending order

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153
papers

8,839
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57719

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153
all docs

153
docs citations

153
times ranked

11196
citing authors

#	ARTICLE	IF	CITATIONS
1	Towards a better understanding of the forming and resistive switching behavior of Ti-doped HfO _x RRAM. Journal of Materials Chemistry C, 2022, 10, 5896-5904.	2.7	16
2	Impact of oxygen concentration at the HfO _x /Ti interface on the behavior of HfO _x filamentary memristors. Journal of Materials Science, 2022, 57, 9299-9311.	1.7	8
3	Impact of titanium doping and pulsing conditions on the analog temporal response of hafnium oxide based memristor synapses. Journal of Applied Physics, 2022, 131, .	1.1	11
4	In-Cu alloy substrates for low-temperature chemical vapor deposition of Mo ₂ C. Journal of Vacuum Science and Technology A: Vacuum, Surfaces and Films, 2021, 39, 012201.	0.9	6
5	Protein interactions with chemical vapor deposited graphene modified by substrate. 2D Materials, 2021, 8, 025015.	2.0	3
6	Quantum Statistical Transport Phenomena in Memristive Computing Architectures. Physical Review Applied, 2021, 15, .	1.5	2
7	Graphene synthesized by chemical vapor deposition as a hydrogen isotope permeation barrier. Carbon, 2021, 176, 106-117.	5.4	14
8	A SPICE compact model for forming-free, low-power graphene-insulator-graphene ReRAM technology. Emergent Materials, 2021, 4, 1055-1065.	3.2	5
9	Protein blocking inhibits ambient degradation of self-assembled monolayers for affinity biosensing. Applied Surface Science, 2021, 557, 149843.	3.1	2
10	Fabrication and characterization of a self-aligned gate stack for electronics applications. Applied Physics Letters, 2021, 119, .	1.5	4
11	Bottom-up nanoscale patterning and selective deposition on silicon nanowires. Nanotechnology, 2021, , .	1.3	2
12	Bottom-Up Masking of Si/Ge Surfaces and Nanowire Heterostructures <i>via</i> Surface-Initiated Polymerization and Selective Etching. ACS Nano, 2020, 14, 282-288.	7.3	9
13	Measurement of gas-concentration-driven permeation for the examination of permeability, solubility, and diffusivity in varying materials. Review of Scientific Instruments, 2020, 91, 105105.	0.6	4
14	Synthetic Engineering of Morphology and Electronic Band Gap in Lateral Heterostructures of Monolayer Transition Metal Dichalcogenides. ACS Nano, 2020, 14, 6323-6330.	7.3	24
15	The synthesis mechanism of Mo ₂ C on Ag-Cu alloy substrates by chemical vapor deposition and the impact of substrate choice. 2D Materials, 2020, 7, 035022.	2.0	10
16	Substrate dependent resistive switching in amorphous-HfO _x memristors: an experimental and computational investigation. Journal of Materials Chemistry C, 2020, 8, 5092-5101.	2.7	25
17	Impact of the thermal environment on the analog temporal response of HfO _x -based neuromorphic devices. Applied Physics Letters, 2020, 116, .	1.5	13
18	Experimental and computational analysis of thermal environment in the operation of HfO ₂ memristors. AIP Advances, 2020, 10, .	0.6	13

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19	Preblocking Procedure to Mitigate Nonselective Protein Adsorption for Carboxyl-SAMs Used in Biosensing. <i>Journal of Physical Chemistry C</i> , 2019, 123, 16778-16786.	1.5	7
20	General model for mass transport to planar and nanowire biosensor surfaces. <i>Journal of Applied Physics</i> , 2019, 125, 114502.	1.1	3
21	Disorder and Weak Localization near Charge Neutral Point in Cleaned Single-Layer Graphene. <i>Physica Status Solidi (B): Basic Research</i> , 2019, 256, 1800541.	0.7	1
22	The impact of defect density, grain size, and Cu orientation on thermal oxidation of graphene-coated Cu. <i>Applied Surface Science</i> , 2019, 478, 959-968.	3.1	12
23	Total Ionizing Dose Effects and Proton-Induced Displacement Damage on MoS ₂ -Interlayer-MoS ₂ Tunneling Junctions. <i>IEEE Transactions on Nuclear Science</i> , 2019, 66, 420-427.	1.2	6
24	Epitaxial and atomically thin graphene-metal hybrid catalyst films: the dual role of graphene as the support and the chemically-transparent protective cap. <i>Energy and Environmental Science</i> , 2018, 11, 1610-1616.	15.6	34
25	Strain relaxation via formation of cracks in compositionally modulated two-dimensional semiconductor alloys. <i>Npj 2D Materials and Applications</i> , 2018, 2, .	3.9	23
26	Low-temperature, plasma assisted, cyclic synthesis of MoS ₂ . <i>Journal of Vacuum Science and Technology B: Nanotechnology and Microelectronics</i> , 2018, 36, .	0.6	6
27	Plasma-assisted synthesis of MoS ₂ . <i>2D Materials</i> , 2018, 5, 015005.	2.0	19
28	Impact of Synthesized MoS ₂ Wafer-Scale Quality on Fermi Level Pinning in Vertical Schottky-Barrier Heterostructures. <i>ACS Applied Materials & Interfaces</i> , 2018, 10, 39860-39871.	4.0	5
29	Material Constraints and Scaling of 2-D Vertical Heterostructure Interlayer Tunnel Field-Effect Transistors. <i>IEEE Transactions on Electron Devices</i> , 2017, 64, 2714-2720.	1.6	7
30	<i>In situ</i> thermal oxidation kinetics in few layer MoS ₂ . <i>2D Materials</i> , 2017, 4, 025058.	2.0	49
31	Direct correlation between potentiometric and impedance biosensing of antibody-antigen interactions using an integrated system. <i>Applied Physics Letters</i> , 2017, 111, .	1.5	14
32	Comprehensive Capacitance-Voltage Simulation and Extraction Tool Including Quantum Effects for High- κ on SiGe and InGaAs: Part I Model Description and Validation. <i>IEEE Transactions on Electron Devices</i> , 2017, 64, 3786-3793.	1.6	3
33	Comprehensive Capacitance-Voltage Simulation and Extraction Tool Including Quantum Effects for High- κ on SiGe and InGaAs: Part II Fits and Extraction From Experimental Data. <i>IEEE Transactions on Electron Devices</i> , 2017, 64, 3794-3801.	1.6	4
34	Solution-Processed Doping of Trilayer WSe ₂ with Redox-Active Molecules. <i>Chemistry of Materials</i> , 2017, 29, 7296-7304.	3.2	25
35	Interfacial Li-Ion Storage between Graphene Layers. <i>ECS Transactions</i> , 2017, 77, 19-25.	0.3	2
36	ALD TiO ₂ as a top-gate dielectric and passivation layer for InGaZnO115ISFETs. <i>Semiconductor Science and Technology</i> , 2017, 32, 114004.	1.0	4

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37	Spatio-temporal pattern recognition in neural circuits with memory-transistor-driven memristive synapses. , 2017, , .		5
38	Band structure effects on resonant tunneling in III-V quantum wells versus two-dimensional vertical heterostructures. Journal of Applied Physics, 2016, 119, .	1.1	22
39	Resonant Light-Induced Heating in Hybrid Cavity-Coupled 2D Transition-Metal Dichalcogenides. ACS Photonics, 2016, 3, 700-707.	3.2	27
40	Graphene-Molybdenum Disulfide-Graphene Tunneling Junctions with Large-Area Synthesized Materials. ACS Applied Materials & Interfaces, 2016, 8, 8702-8709.	4.0	16
41	Field-effect transistors based on wafer-scale, highly uniform few-layer p-type WSe ₂ . Nanoscale, 2016, 8, 2268-2276.	2.8	58
42	A potentiometric biosensor for rapid on-site disease diagnostics. Biosensors and Bioelectronics, 2016, 79, 669-678.	5.3	81
43	Two-dimensional layered transition-metal dichalcogenides for versatile properties and applications. MRS Bulletin, 2015, 40, 558-563.	1.7	39
44	Flexible MoS ₂ Field-Effect Transistors for Gate-Tunable Piezoresistive Strain Sensors. ACS Applied Materials & Interfaces, 2015, 7, 12850-12855.	4.0	127
45	Enhanced Resonant Tunneling in Symmetric 2D Semiconductor Vertical Heterostructure Transistors. ACS Nano, 2015, 9, 5000-5008.	7.3	50
46	Gold-coated graphene field-effect transistors for quantitative analysis of protein-antibody interactions. 2D Materials, 2015, 2, 044008.	2.0	32
47	Controlled Doping of Large-Area Trilayer MoS ₂ with Molecular Reductants and Oxidants. Advanced Materials, 2015, 27, 1175-1181.	11.1	183
48	Layer-by-Layer Evolution of Structure, Strain, and Activity for the Oxygen Evolution Reaction in Graphene-Templated Pt Monolayers. ACS Applied Materials & Interfaces, 2015, 7, 6180-6188.	4.0	40
49	Highly Uniform Trilayer Molybdenum Disulfide for Wafer-Scale Device Fabrication. Advanced Functional Materials, 2014, 24, 6389-6400.	7.8	99
50	Cleaning graphene with a titanium sacrificial layer. Applied Physics Letters, 2014, 104, .	1.5	30
51	Tunneling characteristics in chemical vapor deposited graphene-hexagonal boron nitride-graphene junctions. Applied Physics Letters, 2014, 104, .	1.5	49
52	Investigation of switching mechanism in forming-free multi-level resistive memories with atomic layer deposited HfTiO _x nanolaminate. , 2014, , .		1
53	Nonlinear Switching With Ultralow Reset Power in Graphene-Insulator-Graphene Forming-Free Resistive Memories. IEEE Electron Device Letters, 2014, 35, 750-752.	2.2	16
54	Electrical and chemical characteristics of Al ₂ O ₃ /InP metal-oxide-semiconductor capacitors. Applied Physics Letters, 2013, 102, 132903.	1.5	37

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55	Spike-Timing-Dependent Plasticity Using Biologically Realistic Action Potentials and Low-Temperature Materials. IEEE Nanotechnology Magazine, 2013, 12, 450-459.	1.1	28
56	Logic Gates and Ring Oscillators Based on Ambipolar Nanocrystalline-Silicon TFTs. Active and Passive Electronic Components, 2013, 2013, 1-7.	0.3	11
57	Multilevel Switching in Forming-Free Resistive Memory Devices With Atomic Layer Deposited HfTiO_x Nanolaminate. IEEE Electron Device Letters, 2013, 34, 867-869.	2.2	37
58	Low-Temperature Fabrication of Spiking Soma Circuits Using Nanocrystalline-Silicon TFTs. IEEE Transactions on Neural Networks and Learning Systems, 2013, 24, 1466-1472.	7.2	2
59	Chemical and electrical characterization of the $\text{HfO}_2/\text{InAlAs}$ interface. Journal of Applied Physics, 2013, 114, .	1.1	22
60	Forming-free resistive switching with low current operation in graphene-insulator-graphene structures. , 2013, , .		2
61	Reducing Extrinsic Performance-Limiting Factors in Graphene Grown by Chemical Vapor Deposition. ACS Nano, 2012, 6, 3224-3229.	7.3	216
62	Issues with characterizing transport properties of graphene field effect transistors. Solid State Communications, 2012, 152, 1311-1316.	0.9	19
63	Noise effects in field-effect transistor biological sensor detection circuits. , 2012, , .		2
64	SPICE macromodel of silicon-on-insulator-field-effect-transistor-based biological sensors. Sensors and Actuators B: Chemical, 2012, 161, 163-170.	4.0	27
65	Submicron Ambipolar Nanocrystalline Silicon Thin-Film Transistors and Inverters. IEEE Transactions on Electron Devices, 2012, 59, 359-366.	1.6	18
66	Neural Learning Circuits Utilizing Nano-Crystalline Silicon Transistors and Memristors. IEEE Transactions on Neural Networks and Learning Systems, 2012, 23, 565-573.	7.2	110
67	Ambipolar nano-crystalline-silicon TFTs with submicron dimensions and reduced threshold voltage shift. , 2011, , .		7
68	Spike timing-dependent synaptic plasticity using memristors and nano-crystalline silicon TFT memories. , 2011, , .		8
69	One-Step Selective Chemistry for Silicon-on-Insulator Sensor Geometries. Langmuir, 2011, 27, 7337-7340.	1.6	24
70	The effect of chemical residues on the physical and electrical properties of chemical vapor deposited graphene transferred to SiO_2 . Applied Physics Letters, 2011, 99, .	1.5	829
71	Effective mobility of single-layer graphene transistors as a function of channel dimensions. Journal of Applied Physics, 2011, 109, .	1.1	114
72	Large-Area Graphene Single Crystals Grown by Low-Pressure Chemical Vapor Deposition of Methane on Copper. Journal of the American Chemical Society, 2011, 133, 2816-2819.	6.6	1,161

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73	Control and stability of self-assembled monolayers under biosensing conditions. Journal of Materials Chemistry, 2011, 21, 4384.	6.7	55
74	Hebbian Learning in Spiking Neural Networks With Nanocrystalline Silicon TFTs and Memristive Synapses. IEEE Nanotechnology Magazine, 2011, 10, 1066-1073.	1.1	142
75	Interfacial chemistry of oxides on $\text{In}_x\text{Ga}(1-x)\text{As}$ and implications for MOSFET applications. Current Opinion in Solid State and Materials Science, 2011, 15, 188-207.	5.6	119
76	Comparison of Methods to Bias Fully Depleted SOI-Based MOSFET Nanoribbon pH Sensors. IEEE Transactions on Electron Devices, 2011, 58, 1752-1760.	1.6	21
77	Investigation of Tunneling Current in $\text{SiO}_2/\text{HfO}_2$ Gate Stacks for Flash Memory Applications. IEEE Transactions on Electron Devices, 2011, 58, 4189-4195.	1.6	5
78	Is interfacial chemistry correlated to gap states for high-k/III-V interfaces?. Microelectronic Engineering, 2011, 88, 1061-1065.	1.1	62
79	Remote phonon and surface roughness limited universal electron mobility of $\text{In}_{0.53}\text{Ga}_{0.47}\text{As}$ surface channel MOSFETs. Microelectronic Engineering, 2011, 88, 1083-1086.	1.1	27
80	Submicron ambipolar nanocrystalline-silicon TFTs with high-K gate dielectrics. , 2011, , .		2
81	Effect of post deposition anneal on the characteristics of HfO_2/InP metal-oxide-semiconductor capacitors. Applied Physics Letters, 2011, 99, .	1.5	51
82	(Invited) Band-Edge Effective Work Functions by Controlling HfO_2/TiN Interfacial Composition for Gate-Last CMOS. ECS Transactions, 2011, 35, 285-295.	0.3	3
83	High-k Oxide Growth on III-V Surfaces: Chemical Bonding and MOSFET Performance. ECS Transactions, 2011, 35, 403-413.	0.3	6
84	Transport Properties of Graphene Transistors. ECS Transactions, 2011, 35, 229-237.	0.3	1
85	Impact of Semiconductor and Interface-State Capacitance on Metal/High-k/GaAs Capacitance-Voltage Characteristics. IEEE Transactions on Electron Devices, 2010, 57, 2599-2606.	1.6	38
86	(Invited) Electrical and Physical Properties of High-k Gate Dielectrics on $\text{In}_x\text{Ga}_{1-x}\text{As}$. ECS Transactions, 2010, 28, 209-219.	0.3	3
87	Hydrogenated amorphous silicon nanowire transistors with Schottky barrier source/drain junctions. Applied Physics Letters, 2010, 97, .	1.5	8
88	SPICE simulation of nanoscale non-crystalline silicon TFTs in spiking neuron circuits. , 2010, , .		11
89	First-principles study of metal-graphene interfaces. Journal of Applied Physics, 2010, 108, .	1.1	358
90	First-Principles and Quantum Transport Studies of Metal-Graphene End Contacts. Materials Research Society Symposia Proceedings, 2010, 1259, 1.	0.1	2

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91	Graphene Films with Large Domain Size by a Two-Step Chemical Vapor Deposition Process. Nano Letters, 2010, 10, 4328-4334.	4.5	896
92	Interfacial oxygen and nitrogen induced dipole formation and vacancy passivation for increased effective work functions in TiN/HfO ₂ gate stacks. Applied Physics Letters, 2010, 96, .	1.5	29
93	Interfacial Chemistry of Oxides on III-V Compound Semiconductors. , 2010, , 131-172.		2
94	Materials Science of Graphene for Novel Device Applications. ECS Transactions, 2009, 19, 185-199.	0.3	2
95	The significance of core-level electron binding energies on the proper analysis of InGaAs interfacial bonding. Applied Physics Letters, 2009, 95, 151905.	1.5	50
96	In-situ Studies of Atomic Layer Deposition Studies on High-Mobility Channel Materials. ECS Transactions, 2009, 25, 115-122.	0.3	0
97	Contact Resistance Studies of Metal on HOPG and Graphene Stacks. , 2009, , .		6
98	Atomic-Layer-Deposited Al ₂ O ₃ as Gate Dielectrics for Graphene-Based Devices. ECS Transactions, 2009, 19, 225-230.	0.3	14
99	Surface passivation and implications on high mobility channel performance (Invited Paper). Microelectronic Engineering, 2009, 86, 1544-1549.	1.1	41
100	Deposition of HfO ₂ on InAs by atomic-layer deposition. Microelectronic Engineering, 2009, 86, 1561-1563.	1.1	39
101	Detection of Ga suboxides and their impact on III-V passivation and Fermi-level pinning. Applied Physics Letters, 2009, 94, .	1.5	250
102	Surface Studies of III-V Materials: Oxidation Control and Device Implications. ECS Transactions, 2009, 19, 387-403.	0.3	24
103	Extraction of the Effective Mobility of $\text{In}_{0.53}\text{Ga}_{0.47}\text{As}$ MOSFETs. IEEE Electron Device Letters, 2009, 30, 316-318.	2.2	39
104	Structural and Electrical Properties of HfO ₂ /n-In _x Ga _{1-x} As structures (x: 0,) Tj ETQq0 0 0 rgBT /Overlock 10 Tf 5	0.3	20
105	Technology and metrology of new electronic materials and devices. , 2009, , 166-173.		1
106	Half-cycle atomic layer deposition reaction studies of Al ₂ O ₃ on (NH ₄) ₂ S passivated GaAs(100) surfaces. Applied Physics Letters, 2008, 93, .	1.5	94
107	Conformal Al ₂ O ₃ dielectric layer deposited by atomic layer deposition for graphene-based nanoelectronics. Applied Physics Letters, 2008, 92, .	1.5	245
108	A Gate Dielectric Last Approach to Integrate Organic Based Devices on Plastic Substrates. , 2008, , .		8

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109	Lithographically Defined Si Nanowire Field Effect Transistors for Biochemical Sensing. , 2008, , .		0
110	Performance enhancement of n-channel inversion type $\text{In}_x\text{Ga}_{1-x}\text{As}$ metal-oxide-semiconductor field effect transistor using <i>ex situ</i> deposited thin amorphous silicon layer. Applied Physics Letters, 2008, 93, .	1.5	54
111	Indium stability on InGaAs during atomic H surface cleaning. Applied Physics Letters, 2008, 92, .	1.5	62
112	Comparison of n-type and p-type GaAs oxide growth and its effects on frequency dispersion characteristics. Applied Physics Letters, 2008, 93, 113506.	1.5	55
113	Half-cycle atomic layer deposition reaction studies of Al_2O_3 on $\text{In}_{0.2}\text{Ga}_{0.8}\text{As}$ (100) surfaces. Applied Physics Letters, 2008, 93, .	1.5	138
114	Three-Dimensional Simulation Study of the Improved On/Off Current Ratio in Silicon Nanowire Field-Effect Transistors. Journal of the Korean Physical Society, 2008, 53, 1680-1684.	0.3	0
115	14MHz organic diodes fabricated using photolithographic processes. Applied Physics Letters, 2007, 90, 262105.	1.5	34
116	Frequency dispersion reduction and bond conversion on n-type GaAs by in situ surface oxide removal and passivation. Applied Physics Letters, 2007, 91, 163512.	1.5	88
117	Precise Alignment of Single Nanowires and Fabrication of Nanoelectromechanical Switch and Other Test Structures. IEEE Nanotechnology Magazine, 2007, 6, 256-262.	1.1	52
118	Technology and metrology of new electronic materials and devices. Nature Nanotechnology, 2007, 2, 25-32.	15.6	173
119	Spatial Distributions of Trapping Centers in $\text{HfO}_2/\text{SiO}_2$ Gate Stack. IEEE Transactions on Electron Devices, 2007, 54, 1338-1345.	1.6	56
120	Metrology Challenges for Emerging Research Devices and Materials. IEEE Transactions on Semiconductor Manufacturing, 2006, 19, 397-403.	1.4	7
121	Interface characterization of molecular-monolayer/ SiO_2 based molecular junctions. Solid-State Electronics, 2006, 50, 1088-1096.	0.8	17
122	A Comparison of Thickness Values for Very Thin SiO_2 Films by Using Ellipsometric, Capacitance-Voltage, and HRTEM Measurements. Journal of the Electrochemical Society, 2006, 153, F12.	1.3	6
123	Combinatorial study of $\text{Ni}/\text{Ti}/\text{Pt}$ ternary metal gate electrodes on HfO_2 for the advanced gate stack. Applied Physics Letters, 2006, 89, 142108.	1.5	21
124	Spatial distributions of trapping centers in $\text{HfO}_2/\text{SiO}_2$ gate stacks. Applied Physics Letters, 2006, 88, 152907.	1.5	67
125	CHARACTERIZATION OF ELECTRICALLY ACTIVE DEFECTS IN HIGH-K GATE DIELECTRICS USING CHARGE PUMPING. , 2006, , 85-96.		1
126	Metrology for Emerging Devices and Materials. AIP Conference Proceedings, 2005, , .	0.3	0

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127	Nanometre gaps in gold wires are formed by thermal migration. <i>Nanotechnology</i> , 2005, 16, 1294-1299.	1.3	16
128	Enhanced Channel Modulation in Dual-Gated Silicon Nanowire Transistors. <i>Nano Letters</i> , 2005, 5, 2519-2523.	4.5	129
129	Silicon nanowires as enhancement-mode Schottky barrier field-effect transistors. <i>Nanotechnology</i> , 2005, 16, 1482-1485.	1.3	126
130	Influence of buffer layer thickness on memory effects of SrBi ₂ Ta ₂ O ₉ /SiN/Si structures. <i>Applied Physics Letters</i> , 2004, 85, 1439-1441.	1.5	25
131	Molecular devices formed by direct monolayer attachment to silicon. <i>Solid-State Electronics</i> , 2004, 48, 1747-1752.	0.8	24
132	Asymmetric Energy Distribution of Interface Traps in n- and p-MOSFETs With HfO ₂ /Gate Dielectric on Ultrathin SiON Buffer Layer. <i>IEEE Electron Device Letters</i> , 2004, 25, 126-128.	2.2	34
133	High Inversion Current in Silicon Nanowire Field Effect Transistors. <i>Nano Letters</i> , 2004, 4, 2197-2201.	4.5	87
134	A capacitance-voltage model for polysilicon-gated MOS devices including substrate quantization effects based on modification of the total semiconductor charge. <i>Solid-State Electronics</i> , 2003, 47, 1589-1596.	0.8	21
135	Impact of substrate hot hole injection on ultrathin silicon dioxide breakdown. <i>Applied Physics Letters</i> , 2003, 82, 3242-3244.	1.5	12
136	Thickness Evaluation for 2nm SiO ₂ Films, a Comparison of Ellipsometric, Capacitance-Voltage and HRTEM Measurements. <i>AIP Conference Proceedings</i> , 2003, , .	0.3	5
137	Challenges of Electrical Measurements of Advanced Gate Dielectrics in Metal-Oxide-Semiconductor Devices. <i>AIP Conference Proceedings</i> , 2003, , .	0.3	3
138	Interaction between low-energy electrons and defects created by hot holes in ultrathin silicon dioxide. <i>Applied Physics Letters</i> , 2002, 80, 3343-3345.	1.5	3
139	Observation of latent reliability degradation in ultrathin oxides after heavy-ion irradiation. <i>Applied Physics Letters</i> , 2002, 80, 1282-1284.	1.5	42
140	A comparison of quantum-mechanical capacitance-voltage simulators. <i>IEEE Electron Device Letters</i> , 2001, 22, 35-37.	2.2	72
141	Heavy-ion-induced soft breakdown of thin gate oxides. <i>IEEE Transactions on Nuclear Science</i> , 2001, 48, 1913-1916.	1.2	46
142	Time-dependent breakdown of ultra-thin SiO ₂ gate dielectrics under pulsed biased stress. <i>IEEE Electron Device Letters</i> , 2001, 22, 224-226.	2.2	19
143	Reliability of ultra-thin silicon dioxide under substrate hot-electron, substrate hot-hole and tunneling stress. <i>Microelectronic Engineering</i> , 2001, 59, 73-83.	1.1	6
144	Study of low-frequency charge pumping on thin stacked dielectrics. <i>IEEE Transactions on Electron Devices</i> , 2001, 48, 2754-2762.	1.6	26

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145	Defect generation and breakdown of ultrathin silicon dioxide induced by substrate hot-hole injection. <i>Journal of Applied Physics</i> , 2001, 90, 2338-2346.	1.1	30
146	Limitations of conductance to the measurement of the interface state density of MOS capacitors with tunneling gate dielectrics. <i>IEEE Transactions on Electron Devices</i> , 2000, 47, 601-608.	1.6	98
147	Reliability of ultrathin silicon dioxide under combined substrate hot-electron and constant voltage tunneling stress. <i>IEEE Transactions on Electron Devices</i> , 2000, 47, 1183-1191.	1.6	54
148	Analysis of leakage currents and impact on off-state power consumption for CMOS technology in the 100-nm regime. <i>IEEE Transactions on Electron Devices</i> , 2000, 47, 1393-1400.	1.6	89
149	Estimating oxide thickness of tunnel oxides down to 1.4 nm using conventional capacitance-voltage measurements on MOS capacitors. <i>IEEE Electron Device Letters</i> , 1999, 20, 179-181.	2.2	147
150	Modeled tunnel currents for high dielectric constant dielectrics. <i>IEEE Transactions on Electron Devices</i> , 1998, 45, 1350-1355.	1.6	152
151	Low-frequency noise characterization of n- and p-MOSFET's with ultrathin oxynitride gate films. <i>IEEE Electron Device Letters</i> , 1996, 17, 395-397.	2.2	49
152	Low-pressure rapid thermal chemical vapor deposition of oxynitride gate dielectrics for n-channel and p-channel MOSFETs. <i>IEEE Transactions on Electron Devices</i> , 1996, 43, 15-22.	1.6	45
153	Mobility behaviour of n-channel and p-channel MOSFETs with oxynitride gate dielectrics formed by low-pressure rapid thermal chemical vapor deposition. <i>IEEE Transactions on Electron Devices</i> , 1996, 43, 753-758.	1.6	30