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List of Publications by Year in descending order

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Version: 2024-02-01

7
papers

177
citations

1684188

5
h-index

1872680

6
g-index

7
all docs

7
docs citations

7
times ranked

181
citing authors

#	ARTICLE	IF	CITATIONS
1	A multiple negative differential resistance heterojunction device and its circuit application to ternary static random access memory. <i>Nanoscale Horizons</i> , 2020, 5, 654-662.	8.0	70
2	Double Negative Differential Transconductance Characteristic: From Device to Circuit Application toward Quaternary Inverter. <i>Advanced Functional Materials</i> , 2019, 29, 1905540.	14.9	39
3	A Charge-Domain Scalable-Weight In-Memory Computing Macro With Dual-SRAM Architecture for Precision-Scalable DNN Accelerators. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , 2021, 68, 3305-3316.	5.4	30
4	A Fully Static True-Single-Phase-Clocked Dual-Edge-Triggered Flip-Flop for Near-Threshold Voltage Operation in IoT Applications. <i>IEEE Access</i> , 2020, 8, 40232-40245.	4.2	17
5	An Ultra-Low-Power Fully-Static Contention-Free Flip-Flop With Complete Redundant Clock Transition and Transistor Elimination. <i>IEEE Journal of Solid-State Circuits</i> , 2021, 56, 3039-3048.	5.4	11
6	A Static Contention-Free Differential Flip-Flop in 28nm for Low-Voltage, Low-Power Applications. , 2020, , .		7
7	A Redundancy Eliminated Flip-Flop in 28 nm for Low-Voltage Low-Power Applications. <i>IEEE Solid-State Circuits Letters</i> , 2020, 3, 446-449.	2.0	3