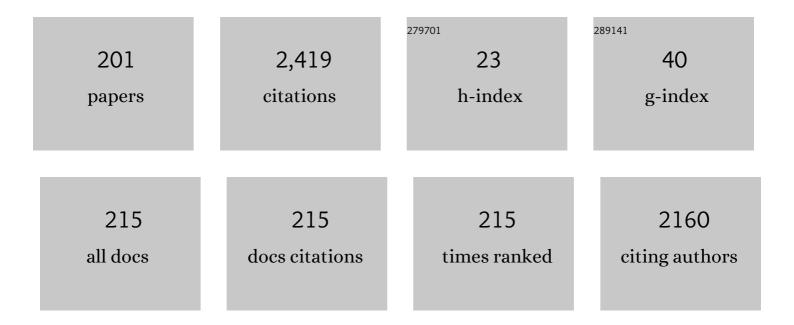
List of Publications by Year in descending order

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#	Article	IF	CITATIONS
1	State of the art of metal oxide memristor devices. Nanotechnology Reviews, 2016, 5, .	2.6	147
2	Characterization of Human Body-Based Thermal and Vibration Energy Harvesting for Wearable Devices. IEEE Journal on Emerging and Selected Topics in Circuits and Systems, 2014, 4, 354-363.	2.7	129
3	Low-Power ECG-Based Processor for Predicting Ventricular Arrhythmia. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2016, 24, 1962-1974.	2.1	104
4	Novel Electronics for Flexible and Neuromorphic Computing. Advanced Functional Materials, 2018, 28, 1801690.	7.8	94
5	Human Vital Signs Detection Methods and Potential Using Radars: A Review. Sensors, 2020, 20, 1454.	2.1	94
6	Ultra-Low Power QRS Detection and ECG Compression Architecture for IoT Healthcare Devices. IEEE Transactions on Circuits and Systems I: Regular Papers, 2019, 66, 669-679.	3.5	73
7	Robust Hybrid Memristor-CMOS Memory: Modeling and Design. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2013, 21, 2069-2079.	2.1	63
8	An Efficient Switched-Capacitor DC-DC Buck Converter for Self-Powered Wearable Electronics. IEEE Transactions on Circuits and Systems I: Regular Papers, 2016, 63, 1557-1566.	3.5	55
9	Ultra-Low Power, Secure IoT Platform for Predicting Cardiovascular Diseases. IEEE Transactions on Circuits and Systems I: Regular Papers, 2017, 64, 2624-2637.	3.5	55
10	Modeling and Optimization of Memristor and STT-RAM-Based Memory for Low-Power Applications. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2016, 24, 1003-1014.	2.1	41
11	MemSens: Memristor-Based Radiation Sensor. IEEE Sensors Journal, 2018, 18, 3198-3205.	2.4	41
12	A fully bypassed six-issue integer datapath and register file on the Itanium-2 microprocessor. IEEE Journal of Solid-State Circuits, 2002, 37, 1433-1440.	3.5	40
13	MOMSense: Metal-Oxide-Metal Elementary Glucose Sensor. Scientific Reports, 2019, 9, 5524.	1.6	39
14	NeuroMem: Analog Graphene-Based Resistive Memory for Artificial Neural Networks. Scientific Reports, 2020, 10, 9473.	1.6	37
15	An Efficient Heterogeneous Memristive xnor for In-Memory Computing. IEEE Transactions on Circuits and Systems I: Regular Papers, 2017, 64, 2427-2437.	3.5	35
16	Bipolar Cu/HfO2/p++ Si Memristors by Sol-Gel Spin Coating Method and Their Application to Environmental Sensing. Scientific Reports, 2019, 9, 9983.	1.6	33
17	ReRAM-Based In-Memory Computing for Search Engine and Neural Network Applications. IEEE Journal on Emerging and Selected Topics in Circuits and Systems, 2019, 9, 388-397.	2.7	33

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#	Article	IF	CITATIONS
19	A Gain-Controlled, Low-Leakage Dickson Charge Pump for Energy-Harvesting Applications. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2019, 27, 1114-1123.	2.1	32
20	Memristor-Based Hardware Accelerator for Image Compression. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2018, 26, 2749-2758.	2.1	31
21	Sol-gel/drop-coated micro-thick TiO2 memristors for γ-ray sensing. Materials Chemistry and Physics, 2016, 184, 72-81.	2.0	30
22	An Efficient Polarity Detection Technique for Thermoelectric Harvester in L-based Converters. IEEE Transactions on Circuits and Systems I: Regular Papers, 2017, 64, 705-716.	3.5	27
23	Modeling Valance Change Memristor Device: Oxide Thickness, Material Type, and Temperature Effects. IEEE Transactions on Circuits and Systems I: Regular Papers, 2016, 63, 2139-2148.	3.5	26
24	Cache Design for Low Power and High Yield. , 2008, , .		25
25	Mathematical modeling of a memristor device. , 2012, , .		24
26	Power management unit for multi-source energy harvesting in wearable electronics. , 2016, , .		23
27	A 93% Peak Efficiency Fully-Integrated Multilevel Multistate Hybrid DC–DC Converter. IEEE Transactions on Circuits and Systems I: Regular Papers, 2018, 65, 2617-2630.	3.5	23
28	A survey of thermal energy harvesting techniques and interface circuitry. , 2013, , .		22
29	An Efficient Zero Current Switching Control for L-Based DC–DC Converters in TEG Applications. IEEE Transactions on Circuits and Systems II: Express Briefs, 2017, 64, 294-298.	2.2	22
30	A Dual-Output Switched Capacitor DC–DC Buck Converter Using Adaptive Time Multiplexing Technique in 65-nm CMOS. IEEE Transactions on Circuits and Systems I: Regular Papers, 2018, 65, 4007-4016.	3.5	22
31	ScanSAT: Unlocking Static and Dynamic Scan Obfuscation. IEEE Transactions on Emerging Topics in Computing, 2021, 9, 1867-1882.	3.2	22
32	Comparative study of current mode and voltage mode sense amplifier used for 28nm SRAM. , 2012, , .		21
33	Adaptive technique for P and T wave delineation in electrocardiogram signals. , 2014, 2014, 90-3.		21
34	A 28 nm DSP Powered by an On-Chip LDO for High-Performance and Energy-Efficient Mobile Applications. IEEE Journal of Solid-State Circuits, 2015, 50, 81-91.	3.5	21
35	Hyper-Dimensional Computing Challenges and Opportunities for AI Applications. IEEE Access, 2022, 10, 97651-97664.	2.6	21
36	<i>UNSAIL</i> : Thwarting Oracle-Less Machine Learning Attacks on Logic Locking. IEEE Transactions on Information Forensics and Security, 2021, 16, 2508-2523.	4.5	21

#	Article	IF	CITATIONS
37	Novel secret key generation techniques using memristor devices. AIP Advances, 2016, 6, .	0.6	20
38	A Nanowatt Real-Time Cardiac Autonomic Neuropathy Detector. IEEE Transactions on Biomedical Circuits and Systems, 2018, 12, 739-750.	2.7	19
39	Enhanced FPGA realization of the fractional-order derivative and application to a variable-order chaotic system. Nonlinear Dynamics, 2020, 99, 3143-3154.	2.7	19
40	Combination of PVA with Graphene to Improve the Seebeck Coefficient for Thermoelectric Generator Applications. Journal of Electronic Materials, 2015, 44, 420-424.	1.0	17
41	A Nano-Watt ECG Feature Extraction Engine in 65-nm Technology. IEEE Transactions on Circuits and Systems II: Express Briefs, 2018, 65, 1099-1103.	2.2	17
42	Memristor-based PUF for lightweight cryptographic randomness. Scientific Reports, 2022, 12, .	1.6	17
43	A maximally stable extremal regions system-on-chip for real-time visual surveillance. , 2015, , .		15
44	GNN-RE: Graph Neural Networks for Reverse Engineering of Gate-Level Netlists. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 2022, 41, 2435-2448.	1.9	15
45	Highly Flexible and Asymmetric Hexagonalâ€Shaped Crystalline Structured Germanium Dioxideâ€Based Multistate Resistive Switching Memory Device for Data Storage and Neuromorphic Computing. Advanced Electronic Materials, 2022, 8, .	2.6	15
46	Characterization of a Graphene-Based Thermoelectric Generator Using a Cost-Effective Fabrication Process. Energy Procedia, 2015, 75, 615-620.	1.8	14
47	Memristor Technology: Synthesis and Modeling for Sensing and Security Applications. Analog Circuits and Signal Processing Series, 2018, , .	0.3	14
48	Effects of top electrode material in hafnium-oxide-based memristive systems on highly-doped Si. Scientific Reports, 2020, 10, 19541.	1.6	14
49	Integrated graphene oxide resistive element in tunable RF filters. Scientific Reports, 2020, 10, 13128.	1.6	14
50	A reduced voltage swing circuit using a single supply to enable lower voltage operation for SRAM-based memory. Microelectronics Journal, 2012, 43, 110-118.	1.1	13
51	Functional Reverse Engineering on SAT-Attack Resilient Logic Locking. , 2019, , .		13
52	RRAM Crossbar-Based In-Memory Computation of Anisotropic Filters for Image Preprocessingloa. IEEE Access, 2020, 8, 127569-127580.	2.6	13
53	Onâ€chip tunable Memristorâ€based flashâ€ADC converter for artificial intelligence applications. IET Circuits, Devices and Systems, 2020, 14, 107-114.	0.9	13
54	Single wall carbon nanotube based optical rectenna. RSC Advances, 2021, 11, 24116-24124.	1.7	13

#	Article	IF	CITATIONS
55	Computational Power Evaluation for Energy-Constrained Wireless Communications Systems. IEEE Open Journal of the Communications Society, 2020, 1, 308-319.	4.4	12
56	Bioinspired Soft Multistate Resistive Memory Device Based on Silk Fibroin Gel for Neuromorphic Computing. Advanced Engineering Materials, 2022, 24, .	1.6	12
57	Automated real-time video surveillance algorithms for SoC implementation: A survey. , 2013, , .		11
58	65-nm ASIC implementation of QRS detector based on Pan and Tompkins algorithm. , 2014, , .		11
59	An AC–DC converter for human body-based vibration energy harvesting. Microelectronics Journal, 2016, 55, 1-7.	1.1	11
60	Subthreshold Continuum Conductance Change in NbO Pt Memristor Interfaces. Journal of Physical Chemistry C, 2016, 120, 18971-18976.	1.5	11
61	Low-power content addressable memory (CAM) array for mobile devices. Microelectronics Journal, 2017, 67, 10-18.	1.1	11
62	Stateful Memristor-Based Search Architecture. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2018, 26, 2773-2780.	2.1	11
63	IoT for Healthcare. Analog Circuits and Signal Processing Series, 2019, , 7-12.	0.3	11
64	Embedded Memory Design for Multi-Core and Systems on Chip. Analog Circuits and Signal Processing Series, 2014, , .	0.3	10
65	An efficient thermal energy harvesting and power management for νWatt wearable BioChips. , 2016, , .		10
66	Switching characteristics of microscale unipolar Pd/Hf/HfO 2 /Pd memristors. Microelectronic Engineering, 2018, 185-186, 35-42.	1.1	10
67	Planar analog memimpedance behavior in reduced GO-Based Metal-Semiconductor-Metal. Materials and Design, 2021, 210, 110077.	3.3	10
68	RRAM-based CAM combined with time-domain circuits for hyperdimensional computing. Scientific Reports, 2021, 11, 19848.	1.6	10
69	Ionic liquid multistate resistive switching characteristics in two terminal soft and flexible discrete channels for neuromorphic computing. Microsystems and Nanoengineering, 2022, 8, .	3.4	10
70	Effect of device, size, activation energy, temperature, and frequency on memristor switching time. , 2014, , .		9
71	Design Methodologies for Yield Enhancement and Power Efficiency in SRAM-Based SoCs. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2015, 23, 2054-2064.	2.1	9

72 Characterization of RF energy harvesting at 2.4 GHz., 2017, , .

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73	A Charge Pump Based Power Management Unit With 66%-Efficiency in 65 nm CMOS. , 2018, , .		9
74	Effect of the Compliance Current on the Retention Time of Cu/HfO2-Based Memristive Devices. Journal of Electronic Materials, 2021, 50, 4397-4406.	1.0	9
75	Effect of vacuum annealing on structural and electrical properties of germanium telluride thin films. Materials Research Bulletin, 2022, 146, 111575.	2.7	9
76	Tunable Switching Behavior of GO-Based Memristors Using Thermal Reduction. Nanomaterials, 2022, 12, 1812.	1.9	9
77	An all-digital, CMOS zero current switching circuit for thermal energy harvesting. , 2015, , .		8
78	Novel microscale memristor with uniqueness property for securing communications. , 2016, , .		8
79	Embedded memory options for ultra-low power IoT devices. Microelectronics Journal, 2019, 93, 104634.	1.1	8
80	Silver/(sub-10 nm)hafnium-oxide-based resistive switching devices on silicon: characteristics and switching mechanism. Nanotechnology, 2020, 31, 165202.	1.3	8
81	An Efficient In-Memory Computing Architecture for Image Enhancement in Al Applications. IEEE Access, 2022, 10, 48229-48241.	2.6	8
82	Cache Organization for Embeded Processors: CAM-vs-SRAM. , 2006, , .		7
83	Memristor: Modeling read and write operations. , 2011, , .		7
84	Energy efficient system-on-chip architecture for non-invasive mobile monitoring of diabetics. , 2013, , .		7
85	Modeling and device parameter design to improve reset time in binary-oxide memristors. Applied Physics A: Materials Science and Processing, 2014, 117, 1019-1023.	1.1	7
86	Adaptive ECG interval extraction. , 2015, , .		7
87	Cascaded power management unit characterization for TEG-based IoT devices in 65â€⁻nm CMOS. Microelectronics Journal, 2019, 90, 285-296.	1.1	7
88	Digital Emulation of a Versatile Memristor With Speech Encryption Application. IEEE Access, 2019, 7, 174280-174297.	2.6	7
89	SecureMem: efficient flexible Pt/GO/Cu memristor for true random number generation. Flexible and Printed Electronics, 2021, 6, 035004.	1.5	7
90	Impact of vacuum on the resistive switching in HfO2-based conductive-bridge RAM with highly-doped silicon bottom electrode. Materials Science and Engineering B: Solid-State Materials for Advanced Technology, 2021, 271, 115267.	1.7	7

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91	Switched capacitor DC-DC converter for ultra-low power applications. , 2014, , .		6
92	LDO regulator versus switched inductor DC-DC converter. , 2014, , .		6
93	Novel fast and scalable parallel union-find ASIC implementation for real-time digital image segmentation. , 2015, , .		6
94	Embedded Memory Interface Logic and Interconnect Testing. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2015, 23, 1946-1950.	2.1	6
95	Resistive switching in sol-gel derived microscale memristors. , 2016, , .		6
96	Editorial TVLSI Positioning—Continuing and Accelerating an Upward Trajectory. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2019, 27, 253-280.	2.1	6
97	High-Density ReRAM Crossbar with Selector Device for Sneak Path Reduction. , 2019, , .		6
98	ASIC Implementation of a Pre-Trained Neural Network for ECG Feature Extraction. , 2020, , .		6
99	A 28-GHz Cascode Inverse Class-D Power Amplifier Utilizing Pulse Injection in 22-nm FDSOI. IEEE Access, 2020, 8, 97353-97360.	2.6	6
100	Semi-custom design flow: Leveraging Place and route tools in Custom Circuit design. , 2009, , .		5
101	Piezo Electric energy harvester and its interface circuit: Opportunities and challenges. , 2013, , .		5
102	Memory impact on the lifetime of a Wireless Sensor Node using a Semi-Markov model. , 2015, , .		5
103	Micro-Pattern of Graphene Oxide Films Using Metal Bonding. Micromachines, 2020, 11, 399.	1.4	5
104	Nanojunction Material Effect on the Photoelectric Response of Single-Wall Carbon Nanotube Rectennas. ACS Omega, 2021, 6, 35692-35698.	1.6	5
105	Adaptive SRAM memory for low power and high yield. , 2008, , .		4
106	Dynamic cache resizing architecture for high yield SOC. , 2009, , .		4
107	Hybrid Memristor-CMOS memory cell: Modeling and design. , 2011, , .		4

108 The optimum Booth radix for low power integer multipliers. , 2013, , .

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#	Article	IF	CITATIONS
109	Memristors for digital, memory and neuromorphic circuits. , 2013, , .		4
110	A 65-nm low power ECG feature extraction system. , 2015, , .		4
111	Novel logarithmic ECG feature extraction algorithm based on pan and tompkins. , 2016, , .		4
112	Novel hafnium oxide memristor device: Switching behaviour and size effect. , 2017, , .		4
113	Memristor Device Overview. Analog Circuits and Signal Processing Series, 2018, , 1-29.	0.3	4
114	An Efficient and Small Area Multioutput Switched Capacitor Buck Converter for IoTs. , 2018, , .		4
115	A novel algorithm for the prediction and detection of ventricular arrhythmia. Analog Integrated Circuits and Signal Processing, 2019, 99, 413-426.	0.9	4
116	Fused RRAM-Based Shift-Add Architecture for Efficient Hyperdimensional Computing Paradigm. , 2021, , .		4
117	MemChar: Portable Low-Power and Low-Cost Characterization Tool for Memristor Devices. IEEE Transactions on Instrumentation and Measurement, 2022, 71, 1-9.	2.4	4
118	Production of Size-Controlled Gold Nanoclusters for Vapor–Liquid–Solid Method. Nanomaterials, 2022, 12, 763.	1.9	4
119	A 65-nm pulsed latch with a single clocked transistor. , 2007, , .		3
120	Portable wireless biomedical temperature monitoring system: Architecture and implementation. , 2013, , .		3
121	Embedded memory design using memristor: Retention time versus write energy. , 2013, , .		3
122	Evolutionary QR-Based Traffic Sign Recognition System for Next-Generation Intelligent Vehicles. , 2015, , .		3
123	A simple hybrid 3-level buck-boost DC-DC converter with efficient PWM regulation scheme. , 2015, , .		3
124	A multi-input, multi-output power management unit using dickson charge pump for energy harvesting applications. , 2016, , .		3
125	Doherty CMOS power amplifiers for 5G technology. , 2017, , .		3

A novel secure conference communication in IoT devices based on memristors. , 2017, , .

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#	Article	IF	CITATIONS
127	Memristor Device Modeling. Analog Circuits and Signal Processing Series, 2018, , 93-104.	0.3	3
128	A novel SIFT architecture and ASIC implementation for real time SOC application. Analog Integrated Circuits and Signal Processing, 2019, 99, 325-338.	0.9	3
129	FPGA-Based Memristor Emulator Circuit for Binary Convolutional Neural Networks. IEEE Access, 2020, 8, 117736-117745.	2.6	3
130	SLID: Exploiting Spatial Locality in Input Data as a Computational Reuse Method for Efficient CNN. IEEE Access, 2021, 9, 57179-57187.	2.6	3
131	Design Exploration of ReRAM-Based Crossbar for AI Inference. IEEE Access, 2021, 9, 70430-70442.	2.6	3
132	C3PU: Cross-Coupling Capacitor Processing Unit Using Analog-Mixed Signal for AI Inference. IEEE Access, 2021, 9, 167353-167363.	2.6	3
133	ASIC Implementation of Associative Memory and Hamming Distance for HDC Application. , 2021, , .		3
134	Switched Inductor DC–DC Boost Regulator Using Voltage-to-Time Controller for TEG Applications. Energies, 2022, 15, 3330.	1.6	3
135	Low leakage power SRAM cell for embedded memory. , 2011, , .		2
136	Write-through method for embedded memory with compression Scan-based testing. , 2012, , .		2
137	A robust histogram-based image segmentation ASIC design for System-on-Chip using 65nm technology. , 2015, , .		2
138	An efficient power management unit for μWatt thermoelectric generators. , 2015, , .		2
139	An 83% efficiency, 0.6V to 1V output switched-capacitor DC-DC converter for micro-watt power applications. , 2016, , .		2
140	A biomedical SoC architecture for predicting ventricular arrhythmia. , 2016, , .		2
141	Computational power analysis of wireless communications systems using operation-level power measurements. , 2017, , .		2
142	Assessment of seven reconstruction methods for contemporary compressive sensing. , 2017, , .		2
143	Synthesis and Characterization of Micro-Thick TiO2Âand HfO2ÂMemristors. Analog Circuits and Signal Processing Series, 2018, , 31-51.	0.3	2
144	Synthesis and Characterization of Nano-Thick HfO2ÂMemristive Crossbar. Analog Circuits and Signal Processing Series, 2018, , 53-64.	0.3	2

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#	Article	IF	CITATIONS
145	Analysis and characterization of leakage reduction methodologies for stacking, body biasing and DLS in 65Anm CMOS technology. Analog Integrated Circuits and Signal Processing, 2020, 102, 1-8.	0.9	2
146	A 1:4 Active Power Divider for 5G Phased-Array Transmitters in 22nm CMOS FDSOI. , 2021, , .		2
147	Memristor Device for Security and Radiation Applications. Analog Circuits and Signal Processing Series, 2018, , 75-92.	0.3	2
148	Temperature dependence of capacitance–voltage characteristics of germanium telluride thin films. Journal of Materials Research and Technology, 2022, 18, 2631-2640.	2.6	2
149	Deep Neural Networks-Based Weight Approximation and Computation Reuse for 2-D Image Classification. IEEE Access, 2022, 10, 41551-41563.	2.6	2
150	Optimum organization of SRAM-based memory for leakage power reduction. , 2008, , .		1
151	Effective screening for NBTI effect on SRAM-based memory. , 2010, , .		1
152	Universal fused floating-point dot-product unit (UFDP). , 2013, , .		1
153	The revolution of glucose monitoring methods and systems: A survey. , 2013, , .		1
154	Memristor for energy efficient wireless sensor node. , 2013, , .		1
155	Energy efficient and hign bandwidth embedded memory implementation. , 2013, , .		1
156	Efficient power management in wireless sensor networks. , 2013, , .		1
157	Modeling of STT-MTJ for low power embedded memory applications: A comparative review. , 2013, , .		1
158	Automated flow for generating CMOS custom memory bit map between logical and physical implementation. , 2013, , .		1
159	Digital pulse frequency modulation for switched capacitor DC-DC converter on 65nm process. , 2014, ,		1
160	Novel MSER-guided street extraction from satellite images. , 2015, , .		1
161	Physics model of memristor devices with varying active materials. , 2016, , .		1

A Re-configurable Memristor Array Structure for In-Memory Computing Applications. , 2018, , .

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163	Nonenzymatic Glucose Sensor Using MIM Pt/CuO/Pt. , 2018, , .		1
164	Time-based Digital LDO Regulator. , 2019, , .		1
165	Introduction to Ultra-Low Power ECG Processor. Analog Circuits and Signal Processing Series, 2019, , 1-6.	0.3	1
166	Background on ECG Processing. Analog Circuits and Signal Processing Series, 2019, , 13-26.	0.3	1
167	Tunable Non-Volatile Analog Resistive Memory and Its Application in Al. , 2020, , .		1
168	Emerging Memory Technology Opportunities and Challenges. Analog Circuits and Signal Processing Series, 2014, , 83-89.	0.3	1
169	Energy Harvesting Sources, Models, and Circuits. Analog Circuits and Signal Processing Series, 2018, , 7-35.	0.3	1
170	Introduction to TEG-Based Power Management Unit. Analog Circuits and Signal Processing Series, 2020, , 15-29.	0.3	1
171	A Low-Cost, Nanowatt, Millimeter-Scale Memristive-Vacuum Sensor. IEEE Sensors Journal, 2022, 22, 6080-6087.	2.4	1
172	A 65nm level-1 cache for mobile applications. , 2008, , .		0
173	Dynamic power analysis for custom designs. , 2009, , .		0
174	Memristors-based NMOS logic circuits. , 2012, , .		0
175	Survey of wireless baseband SoC for biomedical application. , 2013, , .		0
176	A high-throughput, contention-free low-power, Radix-2 1k,2k, 4k and 8k-point fast fourier transform engine using 28nm standard-cell process. , 2013, , .		0
177	Implementation of boost converter zero current detection using digital ASIC design flow. , 2014, , .		0
178	Electret-free electrostatic energy harvesting interface circuit design and analysis. , 2016, , .		0
179	Fused floating point arithmetic for discrete wavelet transform. , 2016, , .		0
180	A sub-μW bio-potential front end in 65nm CMOS. , 2017, , .		0

A sub- $\hat{l}^1\!\!/\!\!4W$ bio-potential front end in 65nm CMOS. , 2017, , . 180

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#	Article	IF	CITATIONS
181	A 230ÂμW built-in on-chip auto-calibrating RF amplitude detector in 65Ânm CMOS. Analog Integrated Circuits and Signal Processing, 2019, 101, 175-185.	0.9	0
182	Reconfigurable, Switched-Capacitor Power Converter for IoT. , 2019, , 277-290.		0
183	ACLT-Based QRS Detection and ECG Compression Architecture. Analog Circuits and Signal Processing Series, 2019, , 39-57.	0.3	0
184	MSER-in-Chip: An Efficient Vision ToolÂfor IoT Devices. , 2019, , 245-259.		0
185	Ultra-Low-Power ECG Processor for IoT SOCs. , 2019, , 141-152.		0
186	Combined CLT and DWT-Based ECG Feature Extractor. Analog Circuits and Signal Processing Series, 2019, , 27-38.	0.3	0
187	DS2B: Dynamic and Secure Substitution Box for Efficient Speech Encryption Engine. IEEE Access, 2021, 9, 93902-93915.	2.6	0
188	L-based DC-DC Boost Regulator Using Voltage-to-Time Converter for Self-powered Devices. , 2021, , .		0
189	Power and Yield for SRAM Memory. Analog Circuits and Signal Processing Series, 2014, , 53-59.	0.3	0
190	Leakage Reduction. Analog Circuits and Signal Processing Series, 2014, , 61-68.	0.3	0
191	SRAM-Based Memory Operation and Yield. Analog Circuits and Signal Processing Series, 2014, , 37-52.	0.3	0
192	Zero Crossing Switching Control for L-Based DC–DC Converters. Analog Circuits and Signal Processing Series, 2018, , 47-60.	0.3	0
193	System Design and Development. Analog Circuits and Signal Processing Series, 2018, , 23-38.	0.3	0
194	Performance and Results. Analog Circuits and Signal Processing Series, 2018, , 51-64.	0.3	0
195	and Implementation. Analog Circuits and Signal Processing Series, 2018, , 39-49.	0.3	0
196	Ultra-Low Power CAN Detection and VA Prediction. Analog Circuits and Signal Processing Series, 2019, , 59-83.	0.3	0
197	Self-Powered SoC Platform for Wearable Health Care. , 2019, , 307-325.		0

A Low-Power, High-Resolution ZCS Control for Inductor-Based Converters. , 2019, , 263-275.

#	ARTICLE	IF	CITATIONS
199	Dual-Outputs Switched Capacitor Voltage Regulator. Analog Circuits and Signal Processing Series, 2020, , 47-71.	0.3	0
200	Introduction to Power Management. Analog Circuits and Signal Processing Series, 2020, , 1-13.	0.3	0
201	TEC-Based Power Management Designs and Characterizations. Analog Circuits and Signal Processing Series, 2020, , 31-46.	0.3	0