

# Yuan Liang

## List of Publications by Year in descending order

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30  
papers

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citations

1162367

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30  
all docs

30  
docs citations

30  
times ranked

209  
citing authors

#	ARTICLE	IF	CITATIONS
1	On-chip sub-terahertz surface plasmon polariton transmission lines in CMOS. Scientific Reports, 2015, 5, 14853.	1.6	60
2	An Energy-Efficient and Low-Crosstalk Sub-THz I/O by Surface Plasmonic Polariton Interconnect in CMOS. IEEE Transactions on Microwave Theory and Techniques, 2017, 65, 2762-2774.	2.9	43
3	On-chip sub-terahertz surface plasmon polariton transmission lines with mode converter in CMOS. Scientific Reports, 2016, 6, 30063.	1.6	33
4	A 0.061-mm <sup>2</sup> 11-GHz Noise-Canceling Low-Noise Amplifier Employing Active Feedforward With Simultaneous Current and Noise Reduction. IEEE Transactions on Microwave Theory and Techniques, 2021, 69, 3093-3106.	2.9	27
5	High-Sensitivity CMOS Super-Regenerative Receiver with Quench-Controlled High-Q Metamaterial Resonator for Millimeter-Wave Imaging at 96 and 135 GHz. IEEE Transactions on Microwave Theory and Techniques, 2014, 62, 3095-3106.	2.9	21
6	Millimeter-Wave Sources at 60 and 140 GHz by Magnetic-Plasmon-Waveguide-Based In-Phase Coupled Oscillator Network in 65-nm CMOS. IEEE Transactions on Microwave Theory and Techniques, 2016, 64, 1560-1571.	2.9	18
7	A 13.5-Gb/s 140-GHz Silicon Redriver Exploiting Metadevices for Short-Range OOK Communications. IEEE Transactions on Microwave Theory and Techniques, 2022, 70, 239-253.	2.9	14
8	A Low-Jitter and Low-Reference-Spur 320 GHz Signal Source With an 80 GHz Integer-N Phase-Locked Loop Using a Quadrature XOR Technique. IEEE Transactions on Microwave Theory and Techniques, 2022, 70, 2642-2657.	2.9	10
9	A 40 GHz CMOS PLL With $-75$ -dBc Reference Spur and 121.9-fs <sub>rms</sub> Jitter Featuring a Quadrature Sampling Phase-Frequency Detector. IEEE Transactions on Microwave Theory and Techniques, 2022, 70, 2299-2314.	2.9	9
10	Complementary metal-oxide-semiconductor 60 GHz power amplifier by in-phase power combining and digitally assisted power back-off efficiency enhancement. IET Microwaves, Antennas and Propagation, 2016, 10, 79-87.	0.7	8
11	A Crosstalk-immune Sub-THz All-surface-wave I/O Transceiver in 65-nm CMOS. , 2018, , .		8
12	Design and Analysis of $S$ -Band On-Chip Modulator and Signal Source Based on Split-Ring Resonator. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2019, 27, 1513-1526.	2.1	8
13	A 239&#x2013;281GHz Sub-THz imager with 100MHz resolution by CMOS direct-conversion receiver with on-chip circular-polarized SIW antenna. , 2014, , .		7
14	CMOS sub-THz on-chip modulator by stacked split ring resonator with high-extinction ratio. , 2015, , .		7
15	A 23.4 mW $-72$ -dBc Reference Spur 40 GHz CMOS PLL Featuring a Spur-Compensation Phase Detector. IEEE Microwave and Wireless Components Letters, 2022, 32, 1091-1094.	2.0	7
16	A 54 to 62.8GHz PA with 95.2mW/mm <sup>2</sup> output power density by 4&#x00D7;4 distributed in-phase power combining in 65nm CMOS. , 2014, , .		5
17	CMOS sub-THz on-chip communication with SRR modulator and SPP interconnect. , 2015, , .		5
18	D-Band Surface-Wave Modulator and Signal Source with 40 dB Extinction Ratio and 3.7mW Output Power in 65 nm CMOS. , 2018, , .		5

#	ARTICLE	IF	CITATIONS
19	A 280GHz CMOS on-chip composite right/left handed transmission line based leaky wave antenna with broadside radiation. , 2014, , .		4
20	An energy efficient and low cross-talk CMOS sub-THz I/O with surface-wave modulator and interconnect. , 2015, , .		4
21	A Low-Power D-type Flip-flop with Active Inductor and Forward Body Biasing Techniques in 40-nm CMOS. , 2019, , .		4
22	Power-efficient CMOS integrated circuit for sub-THz massive MIMO wireless communication. , 2015, , .		3
23	A zero-phase coupler for high output power millimeter-wave signal source in 65 nm CMOS. , 2016, , .		3
24	A 311.6 GHz Phase-locked Loop in 0.13 Åµm SiGe BiCMOS Process with $\hat{\sim}$ 90 dBc/Hz in-band Phase Noise. , 2020, , .		3
25	A Low-Power Quadrature LO Generator With Mutual Power-Supply Rejection Technique. IEEE Access, 2021, 9, 137241-137248.	2.6	3
26	An Energy-efficient Adaptive Sub-THz Wireless Interconnect with MIMO-Beamforming between Cores and DRAMs. , 2016, , .		2
27	An Energy Efficient CMOS Sub-THz Interconnect with Surface Plasmonic Converter and Oscillator. , 2016, , .		2
28	Multi-Channel FSK Inter/Intra-Chip Communication by Exploiting Field-Confined Slow-Wave Transmission Line. , 2020, , .		2
29	A 60GHz digitally-assisted power amplifier with 17.2dBm P<inf>sat</inf>, 11.3% PAE in 65nm CMOS. , 2015, , .		0
30	Millimetre-Wave and Terahertz Antennas and Directional Coupler Enabled by Wafer-Level Packaging Platform with Interposer. , 2021, , .		0