

# Hsin-Chia Lu

## List of Publications by Year in descending order

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82  
papers

1,028  
citations

516215

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h-index

476904

29  
g-index

82  
all docs

82  
docs citations

82  
times ranked

871  
citing authors

#	ARTICLE	IF	CITATIONS
1	60-GHz Four-Element Phased-Array Transmit/Receive System-in-Package Using Phase Compensation Techniques in 65-nm Flip-Chip CMOS Process. IEEE Transactions on Microwave Theory and Techniques, 2012, 60, 743-756.	2.9	177
2	Magnetic Near-Field Probes With High-Pass and Notch Filters for Electric Field Suppression. IEEE Transactions on Microwave Theory and Techniques, 2013, 61, 2460-2470.	2.9	78
3	A 77-GHz 2T6R Transceiver With Injection-Lock Frequency Sextupler Using 65-nm CMOS for Automotive Radar System Application. IEEE Transactions on Microwave Theory and Techniques, 2016, 64, 3031-3048.	2.9	55
4	38-GHz Phased Array Transmitter and Receiver Based on Scalable Phased Array Modules With Endfire Antenna Arrays for 5G MMW Data Links. IEEE Transactions on Microwave Theory and Techniques, 2021, 69, 980-999.	2.9	53
5	Multiport scattering matrix measurement using a reduced-port network analyzer. IEEE Transactions on Microwave Theory and Techniques, 2003, 51, 1525-1533.	2.9	50
6	Space Difference Magnetic Near-Field Probe With Spatial Resolution Improvement. IEEE Transactions on Microwave Theory and Techniques, 2013, 61, 4233-4244.	2.9	50
7	Port reduction methods for scattering matrix measurement of an n-port network. IEEE Transactions on Microwave Theory and Techniques, 2000, 48, 959-968.	2.9	43
8	MMICs in the millimeter-wave regime. IEEE Microwave Magazine, 2009, 10, 99-117.	0.7	41
9	Flip-Chip-Assembled $\text{W}$ -Band CMOS Chip Modules on Ceramic Integrated Passive Device With Transition Compensation for Millimeter-Wave System-in-Package Integration. IEEE Transactions on Microwave Theory and Techniques, 2012, 60, 766-777.	2.9	41
10	Parasitic-Insensitive Linearization Methods for 60-GHz 90-nm CMOS LNAs. IEEE Transactions on Microwave Theory and Techniques, 2012, 60, 2512-2523.	2.9	33
11	A $\text{K}$ -Band High-Efficiency VCO Using Current Reused Technique. IEEE Microwave and Wireless Components Letters, 2017, 27, 1134-1136.	2.0	27
12	A Fully SiP Integrated $\text{V}$ -Band Butler Matrix End-Fire Beam-Switching Transmitter Using Flip-Chip Assembled CMOS Chips on LTCC. IEEE Transactions on Microwave Theory and Techniques, 2012, 60, 1424-1436.	2.9	26
13	A 0.54-0.55 THz $\times 4$ coherent source array with EIRP of 24.4 dBm in 65nm CMOS technology. , 2015, , .		26
14	A 28-GHz Low-Power Vector-Sum Phase Shifter Using Biphasic Modulator and Current Reused Technique. IEEE Microwave and Wireless Components Letters, 2018, 28, 1014-1016.	2.0	25
15	Improvement of the Phase Shifter in 90 $^{\circ}$ Power Splitter for UWB Applications. IEEE Microwave and Wireless Components Letters, 2012, 22, 621-623.	2.0	19
16	LTCC Spiral Inductor Synthesis and Optimization With Measurement Verification. IEEE Transactions on Advanced Packaging, 2010, 33, 160-168.	1.7	17
17	A $\text{V}$ -Band Low-Power Digital Variable-Gain Low-Noise Amplifier Using Current-Reused Technique With Stable Matching and Maintained OP1dB. IEEE Transactions on Microwave Theory and Techniques, 2019, 67, 4404-4417.	2.9	16
18	Electric field coupling suppression using via fences for magnetic near-field shielded-loop coil probes in low temperature co-fired ceramics. , 2011, , .		15

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19	LTCC Layer-to-Layer Misalignment-Tolerant Coupled Inductors and Their Application to Bandpass Filter and Helical Inductors. IEEE Transactions on Components, Packaging and Manufacturing Technology, 2011, 1, 1608-1615.	1.4	14
20	Microwave diversity imaging using six-port reflectometer. IEEE Transactions on Microwave Theory and Techniques, 1999, 47, 84-87.	2.9	13
21	Heat dissipation analysis and design of a board-level phased-array transmitter module for 60-GHz communication. Applied Thermal Engineering, 2013, 53, 78-88.	3.0	13
22	Chip Last Fan-Out Packaging for Millimeter Wave Application. , 2016, , .		13
23	A K-band high-gain down-converter mixer using cross couple pair active load. , 2016, , .		12
24	A D-band wide tuning range VCO using switching transformer. , 2017, , .		12
25	Broadband Low Phase Error Phase Shifter Using High-Pass Network With a Coupled Line Section. IEEE Microwave and Wireless Components Letters, 2015, 25, 775-777.	2.0	11
26	Unequal Line (uL) Calibrator Input Mismatch Correction for TRuL calibration method. , 2007, , .		10
27	Ultra broad band CMOS balanced amplifiers using quadrature power splitters on glass integrated passive device (GIPD) and LTCC with flip chip interconnects for SiP integration. , 2012, , .		10
28	Broadband Differential Phase-Shifter Design Using Bridged T-Type Bandpass Network. IEEE Transactions on Microwave Theory and Techniques, 2014, 62, 1470-1479.	2.9	9
29	A 38 GHz Low Power Variable Gain LNA Using PMOS Current-Steering Device and G&lt;inf&gt;m&lt;/inf&gt;-Boost Technique. , 2018, , .		9
30	A K-band compact fully integrated transformer power amplifier in 0.18- $\mu\text{m}$ CMOS. , 2013, , .		8
31	36-40 GHz Tx/Rx Beamformers for 5G mm-Wave Phased-Array. , 2018, , .		8
32	A 38-GHz Up-conversion sub-harmonic mixer with buffer amplifier in 65-nm CMOS process. , 2017, , .		6
33	A V-Band High Gain Sub-Harmonic Down-Conversion Mixer Using PMOS Cross Couple Pair to Implement Negative Impedance and Current-Bleeding Technique. IEEE Transactions on Circuits and Systems II: Express Briefs, 2021, 68, 2765-2769.	2.2	6
34	Antenna gain and scattering measurement using reflective three-antenna method. , 0, , .		5
35	The thru-reflection-unequal-line (TRuL) calibration method with asymmetric R calibrator for multi-port scattering matrix measurement. , 2006, , .		4
36	Phase and Amplitude Responses of Narrowband Optical Filter Measured by Microwave Network Analyzer. Journal of Lightwave Technology, 2006, 24, 5075-5081.	2.7	4

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37	Design and implementation of A 24-/60-GHz dual-band monopole meander-line planar CMOS antenna. Microwave and Optical Technology Letters, 2012, 54, 1731-1737.	0.9	4
38	A K-band power amplifier with parasitic diode linearizer in 0.18- $\mu$ m CMOS process using 1.8-V supply voltage. , 2016, , .		4
39	A $\sqrt{V}$ -Band Ultra Low Power Sub-Harmonic I/Q Down-Conversion Mixer Using Current Re-Used Technique. IEEE Transactions on Circuits and Systems II: Express Briefs, 2020, 67, 2893-2897.	2.2	4
40	LTCC spiral inductor modeling, synthesis, and optimization. , 2008, , .		3
41	2.4 GHz low-pass filters with harmonic suppression using integrated passive device process. , 2009, , .		3
42	Dual-band CRLH branch-line coupler in LTCC by lump elements with parasite control. , 2010, , .		3
43	W-band flip-chip assembled CMOS amplifier with transition compensation network for SiP integration. , 2010, , .		3
44	Performance Comparison of Flip-Chip-Assembled 5-GHz 0.18- $\mu$ m CMOS Power Amplifiers on Different Packaging Substrates. IEEE Transactions on Components, Packaging and Manufacturing Technology, 2013, 3, 2014-2021.	1.4	3
45	Low loss transmission lines on flexible COP substrate by standard lamination process. , 2014, , .		3
46	Phase and time switching modulations for multi-point wireless power grid to realize stable power reception under rotational misalignment. , 2015, , .		3
47	On-chip Slot Ring Antenna Integrated with Voltage Controlled Oscillator at 140 GHz in 40nm CMOS Technology. , 2019, , .		3
48	LTCC Dielectric Constant and Loss Tangent Extraction by Thru-Line Method in Stripline. , 2019, , .		3
49	A 28 GHz High Slope Automatic Switching Power Detector System Using PMOS Current-Steering Variable Gain Amplifier and Schmitt Trigger. IEEE Transactions on Circuits and Systems II: Express Briefs, 2022, 69, 3134-3138.	2.2	3
50	Capacitor and coupled inductor with high process tolerance in LTCC. , 2006, , .		2
51	40-48 GHz Sub-harmonic Transceiver for High Data-Rate Communication System Applications. , 2008, , .		2
52	LTCC layer-to-layer misalignment resistant coupled inductor and bandpass filter. , 2009, , .		2
53	Phase shifters based on surface mount phase leading bandpass unit cells using low temperature cofired ceramic(LTCC). , 2012, , .		2
54	A V-band high linearity sub-harmonic I/Q demodulator using transformer coupling. , 2017, , .		2

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55	Modularized prototype of 5G mmWave base station system at 38 GHz. , 2018, , .		2
56	Multi-port scattering matrix measurement using a reduced-port network analyzer. , 0, , .		1
57	A quasi-monostatic reflective three-antenna method for antenna gain and scattering measurement. , 0, , .		1
58	The thru-reflection-unequal-line (TRuL) calibration method for scattering matrix measurement of multi-port networks. , 0, , .		1
59	A dual-mode rectangular ring bandpass filter with transmission zeros on LTCC. , 2008, , .		1
60	Coupling coefficient improvement for inductor coupled vertical interconnect in 3D IC die stacking. , 2009, , .		1
61	Transition characterization using TRL calibration method with unequal &#x201C;R&#x201D; calibrators. , 2009, , .		1
62	Design and implementation of a high-performance 60-GHz CMOS slot antenna. Microwave and Optical Technology Letters, 2012, 54, 2061-2065.	0.9	1
63	Embedded end-fire monopole antenna in low temperature cofired ceramic for 60 GHz. , 2013, , .		1
64	Antenna with switchable linear polarization for 60 GHz. , 2014, , .		1
65	Dielectric Constant Measurement Using Metallized Slot Substrate Integrated Waveguide at PCB Process. , 2018, , .		1
66	An E-Band Gate-Pump SSB Mixer for Vital Signs Doppler Radar. , 2018, , .		1
67	A K-band Temperature Compensated Transmitted Power Detector. , 2019, , .		1
68	Smart RF Integrated Circuits: A Millimeter-Wave Gigabit Transceiver with Digitally-Enabled Built-In Self-Healing and Auto-Switching Functions. IEEE Microwave Magazine, 2019, 20, 28-37.	0.7	1
69	A 38-GHz 32-Element Phased-Array Transmitter Based on Scalable 8-Element Phased-Array Modules for 5G MMW Data Links. , 2020, , .		1
70	A Low Power Wideband V-Band LNA Using Double-Transformer-Coupling Technique and T-Type Matching in 90nm CMOS. , 2019, , .		1
71	Microwave diversity imaging using six-port reflectometer. , 0, , .		0
72	Two methods for scattering matrix measurement of an n-port network. , 0, , .		0

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73	Antenna polarimetric calibration using multi-mode TRL calibration method and its extension. , 0, , .		0
74	Antenna polarimetric calibration using multi-mode TRL calibration method and its extension. , 0, , .		0
75	The Analysis of Relation between Q-factor and Phase Noise by Using Substrate-integrated Waveguide Cavity Oscillators. , 0, , .		0
76	Schematic extraction from layout of microwave multi-layer circuits. , 2008, , .		0
77	Variability-aware ITCC spiral inductor synthesis. , 2008, , .		0
78	Lump devices mapping between designer's schematic and layout extracted schematic in microwave frequency. , 2010, , .		0
79	A new doubly balanced sub-harmonically miniature mixer using dual Marchand balun in CMOS 0.18-µm technology. , 2012, , .		0
80	On-chip bi-semicircular slot antenna at 550 GHz for 2×4 coherent source array in 65nm CMOS technology. , 2015, , .		0
81	A Low-Power Digital Variable-Gain V-Band Receiver Using Current-reused Technique with Embedded Power Detectors. , 2021, , .		0
82	Frequency diversity transmitting array for stable power reception under rotation in 2D far-field wireless power transmission. , 2021, , .		0