

Mehdi Dolatshahi

List of Publications by Year in descending order

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Version: 2024-02-01

36
papers

430
citations

759055

12
h-index

794469

19
g-index

36
all docs

36
docs citations

36
times ranked

343
citing authors

#	ARTICLE	IF	CITATIONS
1	Line-of-sight target tracking control of underactuated autonomous underwater vehicles. Ocean Engineering, 2017, 133, 244-252.	1.9	111
2	Gain enhancement and mutual coupling reduction of multiple-input multiple-output antenna for millimeter-wave applications using two types of novel metamaterial structures. International Journal of RF and Microwave Computer-Aided Engineering, 2020, 30, e22006.	0.8	25
3	An Inverter-Based, CMOS, Low-Power Optical Receiver Front-End. Fiber and Integrated Optics, 2019, 38, 1-20.	1.7	22
4	A new ultra low-power, universal OTA-C filter in subthreshold region using bulk-drive technique. AEU - International Journal of Electronics and Communications, 2017, 82, 458-466.	1.7	20
5	A CMOS, low-power current-mirror-based transimpedance amplifier for 10-Gbps optical communications. Microelectronics Journal, 2018, 80, 18-27.	1.1	20
6	Inverter-based, low-power and low-voltage, new mixed-mode Gm-C filter in subthreshold CNTFET technology. IET Circuits, Devices and Systems, 2018, 12, 681-688.	0.9	16
7	A new low-power, universal, multi-mode Gm-C filter in CNTFET technology. Microelectronics Journal, 2019, 90, 342-352.	1.1	16
8	A low-power multi-mode and multi-output high-order CMOS universal Gm-C filter. Analog Integrated Circuits and Signal Processing, 2014, 79, 95-104.	0.9	14
9	High-precision, resistor less gas pressure sensor and instrumentation amplifier in CNT technology. AEU - International Journal of Electronics and Communications, 2018, 93, 325-336.	1.7	14
10	Novel efficient full adder and full subtractor designs in quantum cellular automata. Journal of Supercomputing, 2020, 76, 2191-2205.	2.4	14
11	A new systematic design approach for low-power analog integrated circuits. AEU - International Journal of Electronics and Communications, 2012, 66, 384-389.	1.7	13
12	High-gain, high-isolation, and wideband millimeter-wave closely spaced multiple-input multiple-output antenna with metamaterial wall and metamaterial superstrate for 5G applications. IET Microwaves, Antennas and Propagation, 2021, 15, 379-388.	0.7	13
13	A novel dual-band tunable notch filter with controllable center frequencies and bandwidths. AEU - International Journal of Electronics and Communications, 2018, 88, 70-77.	1.7	12
14	A dual-mode tunable bandpass filter for GSM, UMTS, WiFi, and WiMAX standards applications. International Journal of Circuit Theory and Applications, 2019, 47, 561-571.	1.3	12
15	Design of a new CMOS Low-Power Analogue Neuron. IETE Journal of Research, 2018, 64, 67-75.	1.8	11
16	A CNTFET universal mixed-mode biquad active filter in subthreshold region. International Journal of RF and Microwave Computer-Aided Engineering, 2018, 28, e21574.	0.8	11
17	A CMOS Low-Power Optical Front-End for 5 Gbps Applications. Fiber and Integrated Optics, 2018, 37, 37-56.	1.7	10
18	An energy-efficient, 6 GS/s dynamic comparator in 90-nm CMOS technology. Analog Integrated Circuits and Signal Processing, 2019, 101, 319-330.	0.9	10

#	ARTICLE	IF	CITATIONS
19	A low-power CMOS transimpedance amplifier in 90nm technology for 5Gbps optical communication applications. International Journal of Circuit Theory and Applications, 2018, 46, 2217-2230.	1.3	8
20	Design of a low-voltage and low-power, reconfigurable universal OTA-C filter. Analog Integrated Circuits and Signal Processing, 2022, 111, 169-188.	0.9	8
21	Tunable dual-band bandpass filter for multi-standard applications. AEU - International Journal of Electronics and Communications, 2019, 111, 152885.	1.7	7
22	A novel two stage cross coupled architecture for low voltage low power voltage reference generator. Analog Integrated Circuits and Signal Processing, 2019, 99, 393-402.	0.9	7
23	A new low-power Dynamic-GDI full adder in CNFET technology. The Integration VLSI Journal, 2022, 83, 46-59.	1.3	7
24	An active CMOS optical receiver employing an inductor-less, low-noise and high-gain regulated cascode transimpedance amplifier. Microelectronics Journal, 2021, 110, 105015.	1.1	6
25	A new quantum-dot cellular automata full-adder. , 2016, , .		5
26	Comparing the reliability in systems with triple and five modular redundancy. , 2016, , .		3
27	A Simple Method to Enhance Gain and Isolation of MIMO Antennas Simultaneously Based on Metamaterial Structures for Millimeter-Wave Applications. Journal of Infrared, Millimeter, and Terahertz Waves, 2021, 42, 1078.	1.2	3
28	A New 10 Gb/s Optical Receiver Using Active Inductor in 90 nm CMOS Technology. , 2018, , .		2
29	A 274µW, Inductor-less, Active RGC-Based Transimpedance Amplifier Operating at 5Gbps. , 2019, , .		2
30	An energy-efficient sample-and-hold circuit in CNTFET technology for high-speed applications. Analog Integrated Circuits and Signal Processing, 2020, 103, 209-221.	0.9	2
31	A Fully-integrated, Low-Power Transimpedance Amplifier for 5Gbps Optical Front-End. , 2020, , .		2
32	An energy-efficient dynamic comparator in Carbon Nanotube Field Effect Transistor technology for successive approximation register ADC applications. IET Circuits, Devices and Systems, 0, , .	0.9	2
33	Automated Design and Optimization Of A CMOS OTA Using Geometric Programming. , 2018, , .		1
34	Increasing the efficiency of NOC routing algorithms based on fault tolerance measurement method. , 2018, , .		1
35	A Single Clock Charge Pump with High Conversion Ratio in 0.18 µm Process. IETE Journal of Research, 2018, 64, 11-16.	1.8	0
36	Design of a Bulk-Driven High-Gain OTA Using Positive Feedback. Circuits, Systems, and Signal Processing, 0, , 1.	1.2	0