

Koldo Basterretxea

List of Publications by Year in descending order

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34
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citing authors

#	ARTICLE	IF	CITATIONS
1	Robust embedded MPC with reduced-precision arithmetic for cost-optimized implementations. , 2021, , .		0
2	DBHI: A Tool for Decoupled Functional Hardware-Software Co-Design on SoCs. , 2020, , .		0
3	Towards the Development of a CAD Tool for the Implementation of High-Speed Embedded MPCs on FPGAs. , 2020, , .		3
4	A versatile hardware/software platform for personalized driver assistance based on online sequential extreme learning machines. Neural Computing and Applications, 2019, 31, 8871-8886.	5.6	5
5	Towards the Automatic Implementation of Reduced-size and High Throughput MPC on FPGAs. , 2019, , .		2
6	A fully configurable and scalable neural coprocessor IP for SoC implementations of machine learning applications. , 2017, , .		5
7	Piecewise multi-linear fuzzy extreme learning machine for the implementation of intelligent agents. , 2017, , .		0
8	ELM-based hyperspectral imagery processor for onboard real-time classification. , 2016, , .		6
9	Driving Behavior Signals and Machine Learning: A Personalized Driver Assistance System. , 2015, , .		33
10	Efficient Algorithms for Accelerometer-Based Wearable Hand Gesture Recognition Systems. , 2015, , .		13
11	Easy tuning of fractional-order holds to reduce transient energy consumption of lightly damped resonant systems. IET Control Theory and Applications, 2014, 8, 838-846.	2.1	0
12	A wearable human activity recognition system on a chip. , 2014, , .		20
13	Generalized sampled-data holds to reduce energy consumption in resonant systems. Control Engineering Practice, 2014, 26, 28-40.	5.5	0
14	Scalable parallel architecture for singular value decomposition of large matrices. , 2014, , .		4
15	Adaptive scalable SVD unit for fast processing of large LSE problems. , 2014, , .		3
16	An FPGA-based multiprocessor-architecture for intelligent environments. Microprocessors and Microsystems, 2014, 38, 730-740.	2.8	12
17	A fault tolerant single-chip intelligent agent with feature extraction capability. Applied Soft Computing Journal, 2014, 22, 358-371.	7.2	3
18	Dynamic significant feature extraction for embedded intelligent agent implementations. , 2013, , .		0

#	ARTICLE	IF	CITATIONS
19	Controlled accuracy approximation of sigmoid function for efficient FPGA-based implementation of artificial neurons. Electronics Letters, 2013, 49, 1598-1600.	1.0	33
20	Dynamic Partial Reconfiguration in Embedded Systems for Intelligent Environments. , 2012, , .		4
21	A hardware/software embedded agent for real-time control of ambient-intelligence environments. , 2012, , .		10
22	Recursive sigmoidal neurons for adaptive accuracy neural network implementations. , 2012, , .		10
23	Generalized sampled-data hold functions with asymptotic zero-order hold behavior and polynomial reconstruction. Automatica, 2012, 48, 1171-1176.	5.0	11
24	A System-on-Chip Development of a Neuro-Fuzzy Embedded Agent for Ambient-Intelligence Environments. IEEE Transactions on Systems, Man, and Cybernetics, 2012, 42, 501-512.	5.0	24
25	Embedded high-speed Model Predictive Controller on a FPGA. , 2011, , .		11
26	SCALABLE ARCHITECTURE FOR HIGH-SPEED MULTIDIMENSIONAL FUZZY INFERENCE SYSTEMS. Journal of Circuits, Systems and Computers, 2011, 20, 375-400.	1.5	5
27	A semi-active suspension embedded controller in a FPGA. , 2010, , .		3
28	An Experimental Study on Nonlinear Function Computation for Neural/Fuzzy Hardware Design. IEEE Transactions on Neural Networks, 2007, 18, 266-283.	4.2	41
29	Digital Gaussian membership function circuit for neuro-fuzzy hardware. Electronics Letters, 2006, 42, 44.	1.0	24
30	Gaussian Function Approximation in Neuro-Fuzzy Systems. , 2004, , 163-170.		1
31	Digital design of sigmoid approximator for artificial neural networks. Electronics Letters, 2002, 38, 35.	1.0	36
32	Optimised PWL recursive approximation and its application to neuro-fuzzy systems. Mathematical and Computer Modelling, 2002, 35, 867-883.	2.0	15
33	Quantisation Errors in Digital Implementations of Fuzzy Controllers. Advances in Industrial Control, 2001, , 253-274.	0.5	1
34	Electronic Hardware for Fuzzy Computation. , 0, , 1-30.		8