Chun-Fu Chen

List of Publications by Year
in descending order

13 papers	78 citations	1937685 4 h-index	1588992 8 g-index
13 all docs	13 docs citations	13 times ranked	73 citing authors

#	Article	IF	Citations
1	Reconfigurable Parser Architecture Design with Microprogrammed Controller for Multiple Purposes. Journal of Signal Processing Systems, 2017, 88, 67-81.	2.1	1
2	Complexity-Aware Gabor Filter Bank Architecture Using Principal Component Analysis. Journal of Signal Processing Systems, 2017, 89, 431-444.	2.1	6
3	Reconfigurable Interpolation Architecture for Multistandard Video Decoding. Journal of Signal Processing Systems, 2016, 84, 251-264.	2.1	1
4	High-Throughput Reconfigurable Variable Length Coding Decoder for MPEG-2 and AVC/H.264. Journal of Signal Processing Systems, 2016, 82, 27-40.	2.1	1
5	3-D Video Generation from Monocular Video Based on Hierarchical Video Segmentation. Journal of Signal Processing Systems, 2015, 81, 345-358.	2.1	4
6	Bi-Directional Trajectory Tracking With Variable Block-Size Motion Estimation for Frame Rate Up-Convertor. IEEE Journal on Emerging and Selected Topics in Circuits and Systems, 2014, 4, 29-42.	3.6	16
7	Density analysis of collagen fibers based on enhanced frangi filter in Second Harmonic Generation virtual biopsy images. , 2014, , .		1
8	Automatic Cell Segmentation and Nuclear-to-Cytoplasmic Ratio Analysis for Third Harmonic Generated Microscopy Medical Images. IEEE Transactions on Biomedical Circuits and Systems, 2013, 7, 158-168.	4.0	27
9	Algorithmic complexity analysis on data transfer rate and data storage for multidimensional signal processing. , 2013, , .		2
10	Depth map enhancement based on Z-displacement of objects. , 2013, , .		0
11	Cell segmentation and NC ratio analysis of third harmonic generation virtual biopsy images based on marker-controlled gradient watershed algorithm. , 2012, , .		4
12	Quantifying Intrinsic Parallelism Using Linear Algebra for Algorithm/Architecture Coexploration. IEEE Transactions on Parallel and Distributed Systems, 2012, 23, 944-957.	5.6	13
13	Reconfigurable inverse transform architecture for multiple purpose video coding. , 2011, , .		2