

# Siddhartha Vishwanatha

## List of Publications by Year in descending order

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9  
papers

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1684188

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docs citations

9  
times ranked

75  
citing authors

#	ARTICLE	IF	CITATIONS
1	Systematic circuit design and analysis of a non-ideal DC-DC pulse width modulation boost converter. IET Circuits, Devices and Systems, 2018, 12, 144-156.	1.4	15
2	Non-ideal modelling and IMC based PID Controller Design of PWM DC-DC Buck Converter. IFAC-PapersOnLine, 2018, 51, 639-644.	0.9	14
3	IMC-PID Design of DC-DC Converters Exhibiting Non-minimum phase Characteristics. , 2019, , .		12
4	Non-inverting buck-boost derived hybrid converter. , 2016, , .		9
5	Analysis and Application of Linear ADRC for the Control of DC-DC Converters. , 2019, , .		9
6	Low-power non-ideal pulse-width modulated DC-DC buck-boost converter: design, analysis and experimentation. IET Circuits, Devices and Systems, 2018, 12, 735-745.	1.4	7
7	Polynomial Controller Design and Its Application: Experimental Validation on a Laboratory Setup of Nonideal DC-DC Buck Converter. IEEE Transactions on Industry Applications, 2020, 56, 7020-7031.	4.9	7
8	Design of PI-lead controller for a plant having a right-half-plane zero. , 2017, , .		5
9	Experimental validation of fractional order internal model controller design on buck and boost converter. Measurement and Control, 2021, 54, 748-766.	1.8	5