

# Ebru Aydin Gol

## List of Publications by Year in descending order

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49  
papers

569  
citations

1163117

8  
h-index

752698

20  
g-index

51  
all docs

51  
docs citations

51  
times ranked

340  
citing authors

#	ARTICLE	IF	CITATIONS
1	Formal Methods for Discrete-Time Dynamical Systems. Studies in Systems, Decision and Control, 2017, , .	1.0	204
2	Temporal logic inference for classification and prediction from data. , 2014, , .		73
3	Traffic Network Control From Temporal Logic Specifications. IEEE Transactions on Control of Network Systems, 2016, 3, 162-172.	3.7	46
4	Language-Guided Controller Synthesis for Linear Systems. IEEE Transactions on Automatic Control, 2014, 59, 1163-1176.	5.7	29
5	A Formal Methods Approach to Pattern Recognition and Synthesis in Reaction Diffusion Networks. IEEE Transactions on Control of Network Systems, 2018, 5, 308-320.	3.7	29
6	A formal methods approach to pattern synthesis in reaction diffusion systems. , 2014, , .		26
7	Finite Bisimulations for Switched Linear Systems. IEEE Transactions on Automatic Control, 2014, 59, 3122-3134.	5.7	16
8	Temporal logic model predictive control. Automatica, 2015, 56, 78-85.	5.0	16
9	Language-guided controller synthesis for discrete-time linear systems. , 2012, , .		15
10	A Plume Tracking Algorithm Based on Crosswind Formations. Springer Tracts in Advanced Robotics, 2013, , 91-102.	0.4	13
11	Time-constrained temporal logic control of multi-affine systems. Nonlinear Analysis: Hybrid Systems, 2013, 10, 21-33.	3.5	11
12	Controlling a network of signalized intersections from temporal logical specifications. , 2015, , .		11
13	Finite bisimulations for switched linear systems. , 2012, , .		9
14	Time-Constrained Temporal Logic Control of Multi-Affine Systems. IFAC Postprint Volumes IPPV / International Federation of Automatic Control, 2012, 45, 102-107.	0.4	7
15	Efficient Online Monitoring and Formula Synthesis with Past STL. , 2018, , .		6
16	Temporal logic model predictive control for discrete-time systems. , 2013, , .		5
17	An Efficient Formula Synthesis Method with Past Signal Temporal Logic. IFAC-PapersOnLine, 2019, 52, 43-48.	0.9	5
18	Synthesis of Monitoring Rules via Data Mining. , 2019, , .		5

#	ARTICLE	IF	CITATIONS
19	An automated system repair framework with signal temporal logic. Acta Informatica, 2022, 59, 183-209.	0.5	5
20	Experimentally driven verification of synthetic biological circuits. , 2012, , .		4
21	Locomotion Gait Optimization For Modular Robots; Coevolving Morphology and Control. Procedia Computer Science, 2011, 7, 320-322.	2.0	3
22	On the use of genetic algorithms for synthesis of signal temporal logic formulas. , 2018, , .		3
23	Cause Mining and Controller Synthesis with STL. , 2019, , .		3
24	A Novel MDP Based Decision Support Framework to Restore Earthquake Damaged Distribution Systems. , 2019, , .		3
25	Synthesis of Monitoring Rules with STL. Journal of Circuits, Systems and Computers, 2020, 29, 2050177.	1.5	3
26	Timed Automata Relaxation for Reachability. Lecture Notes in Computer Science, 2021, , 291-310.	1.3	3
27	MDP based Decision Support for Earthquake Damaged Distribution System Restoration. , 2020, , .		3
28	Discrete-Time Dynamical Systems. Studies in Systems, Decision and Control, 2017, , 111-118.	1.0	3
29	Formal Control of Traffic Systems via Network Decomposition. , 2018, , .		2
30	Clock Reduction in Timed Automata While Preserving Design Parameters. , 2019, , .		1
31	Optimal Policy Synthesis from A Sequence of Goal Sets with An Application to Electric Distribution System Restoration. IFAC-PapersOnLine, 2021, 54, 271-276.	0.9	1
32	Finite Temporal Logic Control. Studies in Systems, Decision and Control, 2017, , 81-108.	1.0	1
33	Largest Satisfying Region. Studies in Systems, Decision and Control, 2017, , 119-139.	1.0	1
34	Transition Systems. Studies in Systems, Decision and Control, 2017, , 3-25.	1.0	1
35	Adaptive Cruise Control with Timed Automata. IFAC-PapersOnLine, 2020, 53, 1918-1923.	0.9	1
36	Learning Parameters of ptSTL Formulas with Backpropagation. , 2020, , .		1

#	ARTICLE	IF	CITATIONS
37	Data-driven verification of synthetic gene networks. , 2013, , .		0
38	An additive cost approach to optimal Temporal Logic control. , 2014, , .		0
39	Synthesis of formal control strategies for traffic networks via partitioning. , 2017, , .		0
40	Optimizing Parameters of Signal Temporal Logic Formulas with Local Search. , 2019, , .		0
41	Control synthesis for parametric timed automata under reachability. Turkish Journal of Electrical Engineering and Computer Sciences, 0, , 1751-1764.	1.4	0
42	Ebru Aydin Gol [People in Control]. IEEE Control Systems, 2021, 41, 25-26.	0.8	0
43	Largest Finite Satisfying Region. Studies in Systems, Decision and Control, 2017, , 47-79.	1.0	0
44	Parameter Synthesis. Studies in Systems, Decision and Control, 2017, , 141-161.	1.0	0
45	Language Guided Controller Synthesis. Studies in Systems, Decision and Control, 2017, , 205-230.	1.0	0
46	Model Checking. Studies in Systems, Decision and Control, 2017, , 41-46.	1.0	0
47	Temporal Logic Control. Studies in Systems, Decision and Control, 2017, , 163-183.	1.0	0
48	Optimal Temporal Logic Control. Studies in Systems, Decision and Control, 2017, , 231-256.	1.0	0
49	Synthesis of Controllable ptSTL Formulas. , 2020, , .		0