## Shinsuke Tamura

List of Publications by Year in descending order

Source: https://exaly.com/author-pdf/517726/publications.pdf

Version: 2024-02-01

20 98 4 5 papers citations h-index g-index

20 20 20 24 all docs docs citations times ranked citing authors

#	Article	IF	Citations
1	A comparison between symmetric and asymmetric key encryption algorithm based decryption mixnets. , 2015, , .		17
2	A Mechanism for Anonymous Credit Card Systems. IEEJ Transactions on Electronics, Information and Systems, 2007, 127, 81-87.	0.2	16
3	Information Sharing among Untrustworthy Entities. IEEJ Transactions on Electronics, Information and Systems, 2005, 125, 1767-1772.	0.2	14
4	An Electronic Voting Scheme Based on Revised-SVRM and Confirmation Numbers. IEEE Transactions on Dependable and Secure Computing, 2021, 18, 400-410.	5.4	10
5	An Anonymous Voting Scheme based on Confirmation Numbers. IEEJ Transactions on Electronics, Information and Systems, 2010, 130, 2065-2073.	0.2	10
6	Expenditure Limits in Anonymous Credit Card Systems. , 2006, , .		6
7	Electronic voting using confirmation numbers. , 2009, , .		5
8	A path planning algorithm for multi manipulators. , 2009, , .		4
9	Feasiblity of Hungarian algorithm based Scheduling. , 2010, , .		4
10	Detection of dishonest entities., 2010,,.		4
11	Evaluation of a New Backtrack Free Path Planning Algorithm for Manipulators. IEEJ Transactions on Electronics, Information and Systems, 2008, 128, 1293-1302.	0.2	4
12	A mechanism for enhanced symmetric key encryption based mixnet. , 2009, , .		2
13	Evaluation of a new Backtrack Free path planning Algorithm for two manipulators. , 2011, , .		1
14	Electronic voting - Scopes and limitations. , 2012, , .		1
15	Tracing objects owned by anonymous entities. , 2008, , .		О
16	A scheme for collecting anonymous data. , 2013, , .		0
17	A Safety Ensuring Mechanism for Holonic Manufacturing Systems. IEEJ Transactions on Electronics, Information and Systems, 2005, 125, 106-112.	0.2	0
18	The Channel Acquisition Slot Protocol-A New MAC Protocol for Wireless Networks IEEJ Transactions on Electronics, Information and Systems, 2007, 127, 887-896.	0.2	0

ā	#	Article	IF	CITATIONS
	19	Implementation of BFA (Backtrack Free Path Planning Algorithm) for 3 Dimensional Work Spaces and its Application to Path Planning of Multi Manipulators. IEEJ Transactions on Electronics, Information and Systems, 2011, 131, 1059-1067.	0.2	O
2	20	An Anonymous Off-line Credit Card System based on ITLs. IEEJ Transactions on Electronics, Information and Systems, 2013, 133, 756-768.	0.2	0