Mehedi Hasan

List of Publications by Year
in descending order

40 papers	705 citations	759233 12 h-index	794594 19 g-index
40 all docs	40 docs citations	40 times ranked	113 citing authors

#	Article	IF	Citations
1	A high-performance full swing 1-bit hybrid full adder cell. IET Circuits, Devices and Systems, 2022, 16, 210-217.	1.4	18
2	Quantization, gate dielectric and channel length effect in double-gate tunnel field-effect transistor. Results in Physics, 2022, 34, 105312.	4.1	15
3	Neural networks based multivariate time series forecasting of solar radiation using meteorological data of different cities of Bangladesh. Results in Engineering, 2022, 13, 100365.	5.1	39
4	Identification and Classification of Melanoma Using Deep Learning Algorithm. , 2022, , .		3
5	Feasibility and Cost Analysis of Grid Connected Hybrid Solar Home System: A Case Study of Chattogram District in Bangladesh. , 2022, , .		4
6	Prospect of Mini-Hydel Power Generation in Drainage Systems of Bangladesh. , 2021, , .		8
7	Design and Evaluation of a FIR Filter Using Hybrid Adders and Vedic Multipliers. , 2021, , .		5
8	Smart Electrification of Rural Bangladesh Through Smart Grids. Lecture Notes on Data Engineering and Communications Technologies, 2021, , 41-55.	0.7	12
9	Smart Transformer Theft Protection and Maintenance Monitoring System. , 2021, , .		9
10	Analysis of Lemon Company's Cross-Border E-Commerce Logistics Distribution Mode Selection. , 2021, , 601-615.		3
11	Design of an Area Efficient Quantum Dot Cellular Automata Based Full Adder Cell Having Low Latency. , 2021, , .		8
12	Voice Activated Portable Braille with Audio Feedback. , 2021, , .		10
13	A high-speed 4-bit Carry Look-Ahead architecture as a building block for wide word-length Carry-Select Adder. Microelectronics Journal, 2021, 109, 104992.	2.0	19
14	Smart parking systems: comprehensive review based on various aspects. Heliyon, 2021, 7, e07050.	3.2	81
15	Comprehensive study of 1-Bit full adder cells: review, performance comparison and scalability analysis. SN Applied Sciences, 2021, 3, 1.	2.9	23
16	A high-speed and scalable XOR-XNOR-based hybrid full adder design. Computers and Electrical Engineering, 2021, 93, 107200.	4.8	29
17	A scalable high-speed hybrid 1-bit full adder design using XOR-XNOR module. International Journal of Circuit Theory and Applications, 2021, 49, 3597-3606.	2.0	12
18	Renewable Energy Sector in Bangladesh: The Current Scenario, Challenges and the Role of IoT in Building a Smart Distribution Grid. Energies, 2021, 14, 5083.	3.1	28

#	Article	IF	Citations
19	Effect of doping profile variation on nanoscale cylindrical gate carbon nanotube field-effect transistor: a computational study using nonequilibrium Green's function formalism. Semiconductor Science and Technology, 2021, 36, 015012.	2.0	7
20	Prospective Smart Distribution Substation in Bangladesh: Modeling and Analysis. Sustainability, 2021, 13, 10904.	3.2	13
21	Fault and System Analysis Model of Voltage Source Control Based HVDC Transmission System. , 2021, , .		9
22	Performance Analysis of Machine Learning Techniques for Wind Speed Prediction. , 2021, , .		9
23	Consumer side economic perception of telemedicine during COVID-19 era: A survey on Bangladesh's perspective. Informatics in Medicine Unlocked, 2021, 27, 100797.	3.4	12
24	Healthcare Monitoring System for Dedicated COVID-19 Hospitals or Isolation Centers. , 2021, , .		3
25	Design of a Scalable Low-Power 1-Bit Hybrid Full Adder for Fast Computation. IEEE Transactions on Circuits and Systems II: Express Briefs, 2020, 67, 1464-1468.	3.0	69
26	Design of a Two-Bit Magnitude Comparator Based on Pass Transistor, Transmission Gate and Conventional Static CMOS Logic. , 2020, , .		19
27	Bengali Fake News Detection. , 2020, , .		11
28	Gate Diffusion Input technique based full swing and scalable 1-bit hybrid Full Adder for high performance applications. Engineering Science and Technology, an International Journal, 2020, 23, 1364-1373.	3.2	35
29	Plasmonic corrugated waveguide coupled to a rectangular nano-resonator as an optical filter. OSA Continuum, 2020, 3, 3314.	1.8	8
30	An IoT based Environment Monitoring System. , 2020, , .		19
31	High-Performance Design of a 4-Bit Carry Look-Ahead Adder in Static CMOS Logic. Indonesian Journal of Electrical Engineering and Informatics, 2020, 8, .	0.3	1
32	Low Power Design of a Two Bit Mangitude Comparator for High Speed Operation. , 2019, , .		14
33	A Novel Hybrid Full Adder Based on Gate Diffusion Input Technique, Transmission Gate and Static CMOS Logic. , 2019, , .		20
34	High Speed and Ultra Low Power Design of Carry-Out Bit of 4-Bit Carry Look-Ahead Adder. , 2019, , .		17
35	Low-cost Appliance Switching Circuit for Discarding Technical Issues of Microcontroller Controlled Smart Home. International Journal of Sensors and Sensor Networks, 2019, 7, 16.	1.4	13
36	Performance Improvement of 4-Bit Static CMOS Carry Look-Ahead Adder Using Modified Circuits for Carry Propagate and Generate Terms. Science Journal of Circuits Systems and Signal Processing, 2019, 8, 76.	0.1	8

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37	Microcontroller Based Smart Home System with Enhanced Appliance Switching Capacity. , 2018, , .		35
38	Smart Home Systems: Overview and Comparative Analysis. , 2018, , .		35
39	Overview and Comparative Performance Analysis of Various Full Adder Cells in 90 nm Technology. , 2018, , .		22
40	Designing of a Plasmonic High Pass Wavelength Filter With Apodized Grating Waveguide. , 2017, , .		0