

Gerardo Malavena

List of Publications by Year in descending order

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Version: 2024-02-01

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papers

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1307594

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1372567

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203
citing authors

#	ARTICLE	IF	CITATIONS
1	Random Telegraph Noise Intensification After High-Temperature Phases in 3-D NAND Flash Arrays. IEEE Electron Device Letters, 2022, 43, 557-560.	3.9	9
2	A comparison of modeling approaches for current transport in polysilicon-channel nanowire and macaroni GAA MOSFETs. Journal of Computational Electronics, 2021, 20, 537-544.	2.5	5
3	Random Telegraph Noise in 3D NAND Flash Memories. Micromachines, 2021, 12, 703.	2.9	7
4	A Noise-Resilient Neuromorphic Digit Classifier Based on NOR Flash Memories with Pulse-Width Modulation Scheme. Electronics (Switzerland), 2021, 10, 2784.	3.1	1
5	Memristive and CMOS Devices for Neuromorphic Computing. Materials, 2020, 13, 166.	2.9	83
6	Investigation of the Meyer-Neldel Rule in Si MOSFETs. IEEE Electron Device Letters, 2020, 41, 1821-1824.	3.9	0
7	Unsupervised Learning by Spike-Timing-Dependent Plasticity in a Mainstream NOR Flash Memory Array-Part II: Array Learning. IEEE Transactions on Electron Devices, 2019, 66, 4733-4738.	3.0	9
8	Unsupervised Learning by Spike-Timing-Dependent Plasticity in a Mainstream NOR Flash Memory Array-Part I: Cell Operation. IEEE Transactions on Electron Devices, 2019, 66, 4727-4732.	3.0	13
9	Compact modeling of GIDL-assisted erase in 3-D NAND Flash strings. Journal of Computational Electronics, 2019, 18, 561-568.	2.5	13
10	Investigation and Compact Modeling of the Time Dynamics of the GIDL-Assisted Increase of the String Potential in 3-D NAND Flash Arrays. IEEE Transactions on Electron Devices, 2018, 65, 2804-2811.	3.0	23