Edward Lee

List of Publications by Year
in descending order

15 papers	**72** citations	2258059 **3** h-index	2053705 **5** g-index
15 all docs	**15** docs citations	**15** times ranked	**90** citing authors

#	Article	IF	Citations
1	A Ferroelectric FET-Based Processing-in-Memory Architecture for DNN Acceleration. IEEE Journal on Exploratory Solid-State Computational Devices and Circuits, 2019, 5, 113-122.	1.5	40
2	RTL-to-GDS Tool Flow and Design-for-Test Solutions for Monolithic 3D ICs. , 2019, , .		8
3	Advances in Design and Test of Monolithic 3-D ICs. IEEE Design and Test, 2020, 37, 92-100.	1.2	6
4	Flex-PIM: A Ferroelectric FET based Vector Matrix Multiplication Engine with Dynamical Bitwidth and Floating Point Precision. , 2020, , .		4
5	A System-in-Package Based Energy Harvesting for IoT Devices with Integrated Voltage Regulators and Embedded Inductors. , 2018, , .		3
6	MONETA: A Processing-In-Memory-Based Hardware Platform for the Hybrid Convolutional Spiking Neural Network With Online Learning. Frontiers in Neuroscience, 2022, 16, 775457.	2.8	3
7	Cross-Layer Noise Analysis in Smart Digital Pixel Sensors With Integrated Deep Neural Network. IEEE Journal on Emerging and Selected Topics in Circuits and Systems, 2020, 10, 444-457.	3.6	2
8	A Fully Synthesized Integrated Buck Regulator with Auto-generated GDS-II in 65nm CMOS Process. , 2020, , .		2
9	A Configurable Dual-Mode PRINCE Cipher with Security Aware Pipelining in 65nm for High Throughput Applications. , 2020, , .		1
10	An Authentication IC with Visible Light Based Interrogation in 65nm CMOS. , 2020, , .		1
11	A Low-Power Authentication IC for Visible-Light-Based Interrogation. IEEE Transactions on Industrial Electronics, 2022, 69, 3120-3130.	7.9	1
12	An SRAM Compiler for Monolithic-3D Integrated Circuit with Carbon Nanotube Transistors. IEEE Journal on Exploratory Solid-State Computational Devices and Circuits, 2021, , 1-1.	1.5	1
13	Aging Challenges in On-chip Voltage Regulator Design. , 2020, , .		0
14	Energy Efficient Pixel-Parallel Read-Out Circuits for Digital Image Sensors Using Cross-Layer Pixel Depth Control. IEEE Sensors Journal, 2022, 22, 11317-11327.	4.7	0
15	A PFM Boost Harvester With System-Level Self-Tuned Maximum Power Point Tracking. IEEE Transactions on Power Electronics, 2022, 37, 9428-9442.	7.9	0