Rakesh R

List of Publications by Year
in descending order

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15 papers	113 citations	1478505 6 h-index	1372567 10 g-index
15 all docs	15 docs citations	15 times ranked	68 citing authors

#	Article	IF	Citations
1	A Cascaded Nine-Level Inverter Topology With T-Type and H-Bridge With Increased DC-Bus Utilization. IEEE Transactions on Power Electronics, 2021, 36, 285-294.	7.9	28
2	A Fault-Tolerant Five-Level Inverter Topology With Reduced Component Count for OEIM Drives. IEEE Journal of Emerging and Selected Topics in Power Electronics, 2021, 9, 961-969.	5.4	24
3	A Switched Capacitive Filter-Based Harmonic Elimination Technique by Generating a 30-Sided Voltage Space Vector Structure for IM Drive. IEEE Transactions on Power Electronics, 2020, 35, 2402-2410.	7.9	11
4	Extending the Linear Modulation Range to Full Base Speed Independent of Load Power Factor for a Multilevel Inverter Fed IM Drive. IEEE Transactions on Industrial Electronics, 2020, 67, 9143-9152.	7.9	10
5	A Very High Resolution 30-Sided Space Vector Generation From a Single DC-Link for Induction Motor Drives. IEEE Transactions on Industrial Electronics, 2022, 69, 160-168.	7.9	7
6	A Multilevel Inverter for Instantaneous Voltage Balancing of Single Sourced Stacked DC-Link Capacitors for an Induction Motor Load. IEEE Transactions on Power Electronics, 2022, 37, 10633-10641.	7.9	7
7	Suppression of Lower Order Harmonics for the Full Modulation Range for a Two-Level Inverter-Fed IM Drive With a Switched-Capacitive Filter Technique Forming a 42-Sided Voltage Space Vector Structure. IEEE Transactions on Industrial Electronics, 2021, 68, 6701-6709.	7.9	6
8	A Multilevel 30-Sided Space Vector Structure With Congruent Triangles and Timing Calculation Using Only Sampled Reference Voltages. IEEE Transactions on Industrial Electronics, 2021, 68, 7884-7894.	7.9	4
9	A Dense Multilevel 30-Sided Space Vector Generation Using a Single DC Link for an Induction Motor Drive. IEEE Transactions on Power Electronics, 2021, 36, 11681-11690.	7.9	4
10	A Multilevel Inverter With Inherent Common Coupling Point Voltage Balancing of Stacked Capacitors Across a Single DC-Link for Induction Motor Drives. IEEE Transactions on Industrial Electronics, 2022, 69, 12496-12505.	7.9	4
11	A Nine Level Inverter Topology with Linear Operation at Over-modulation Region. , 2020, , .		3
12	A Novel approach for the analysis of Harmonic Suppression in higher-sided polygonal SV structures. , 2020, , .		3
13	A Ten-Level Inverter Fed Drive Scheme with Extended Linear Modulation Range. IEEE Transactions on Industrial Electronics, 2022, 69, 12261-12269.	7.9	2
14	A Fifteen Concentric 30-sided Polygonal Space Vector Structure Using a Single DC-link for OEIM drive. , 2020, , .		0
15	A Multilevel 30-sided Space Vector Structure Generation for an Induction Motor Drive Using a Single DC-link. , 2020, , .		0