Daniela Kaufmann

List of Publications by Year in descending order

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DANIELA KALIEMANN

#	Article	IF	CITATIONS
1	Adding Dual Variables to Algebraic Reasoning for Gate-Level Multiplier Verification. , 2022, , .		5
2	Formal verification of multiplier circuits using computer algebra. IT - Information Technology, 2022, .	0.9	2
3	AMuletÂ2.0 for Verifying Multiplier Circuits. Lecture Notes in Computer Science, 2021, , 357-364.	1.3	12
4	Formal Verification of Integer Multiplier Circuits Using Algebraic Reasoning: A Survey. , 2021, , 1-27.		1
5	Incremental column-wise verification of arithmetic circuits using computer algebra. Formal Methods in System Design, 2020, 56, 22-54.	0.8	9
6	From DRUP to PAC and Back. , 2020, , .		10
7	Nullstellensatz-Proofs for Multiplier Verification. Lecture Notes in Computer Science, 2020, , 368-389.	1.3	7
8	Verifying Large Multipliers by Combining SAT and Computer Algebra. , 2019, , .		57
9	Practical algebraic calculus and Nullstellensatz with the checkers Pacheck and Pastèque and Nuss-Checker. Formal Methods in System Design, 0, , 1.	0.8	2