

Naser Pourmousavian

List of Publications by Year in descending order

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Version: 2024-02-01

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#	ARTICLE	IF	CITATIONS
1	A 0.5-V 1.6-mW 2.4-GHz Fractional-N All-Digital PLL for Bluetooth LE With PVT-Insensitive TDC Using Switched-Capacitor Doubler in 28-nm CMOS. IEEE Journal of Solid-State Circuits, 2018, 53, 2572-2583.	5.4	34
2	A Mismatch Calibration Technique for SAR ADCs Based on Deterministic Self-Calibration and Stochastic Quantization. IEEE Transactions on Circuits and Systems I: Regular Papers, 2020, 67, 2883-2896.	5.4	16
3	A 180 mV 81.2%-Efficient Switched-Capacitor Voltage Doubler for IoT Using Self-Biasing Deep N-Well in 16-nm CMOS FinFET. IEEE Solid-State Circuits Letters, 2018, 1, 158-161.	2.0	7