

Burcu Erkmen

List of Publications by Year in descending order

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Version: 2024-02-01

26
papers

465
citations

1307594

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1199594

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27
docs citations

27
times ranked

876
citing authors

#	ARTICLE	IF	CITATIONS
1	A Low-Cost Real-Time BCI Integration for Automated Door Opening System. Journal of Circuits, Systems and Computers, 2021, 30, 2150030.	1.5	2
2	FPGA-Based Wigner-Hough Transform System for Detection and Parameter Extraction of LPI Radar LFMCW Signals. IEEE Transactions on Instrumentation and Measurement, 2021, 70, 1-15.	4.7	13
3	Stochastic Gaussian Function For RBF Network. , 2020, , .		0
4	Intelligent Wireless Sensor Networks for Early Fire Warning System. Electrica, 2020, 20, 1-9.	1.2	2
5	FPGA-Based Space Vector PWM and Closed Loop Controllers Design for the Z Source Inverter. IEEE Access, 2019, 7, 130865-130873.	4.2	7
6	Design of gain-scheduling PID controllers for Z-source inverter using iterative reduction-based heuristic algorithms. Simulation Modelling Practice and Theory, 2019, 94, 162-176.	3.8	10
7	KAPALI ALAN YAYA KONUMLANDIRMA SÄ°STEMÄ°. MÄ¼hendislik Bilimleri Ve TasarÄ±m Dergisi, 2019, 7, 337-344.0.3		3
8	Real-Time, Portable EEG Signal Acquisition System. , 2018, , .		1
9	Smart phone application for drowsiness detection during driving. , 2017, , .		4
10	Implementation aspects of Wigner-Hough Transform based detectors for LFMCW signals. , 2016, , .		7
11	Analysis of graphene field effect transistor based current mirrors. , 2016, , .		1
12	Multiscale assembly for tissue engineering and regenerative medicine. Trends in Biotechnology, 2015, 33, 269-279.	9.3	162
13	Improving Wigner-Hough Transform for hardware implementation to intercept LFMCW signals. , 2015, , .		2
14	A Very Low-Profile Dual Output LLC Resonant Converter for LCD/LED TV Applications. IEEE Transactions on Power Electronics, 2014, 29, 3514-3524.	7.9	65
15	Realization of the differential evolution algorithm on FPGA. , 2014, , .		0
16	FPGA implementation of Differential Evaluation Algorithm for MLP training. , 2014, , .		1
17	Training multilayer perceptron using differential evolution algorithm for signature recognition application. , 2013, , .		3
18	The performance of differential evolution algorithm for training CSFNN using a pattern recognition application. , 2013, , .		1

#	ARTICLE	IF	CITATIONS
19	A Mixed Mode Neural Network Circuitry for Object Recognition Application. Circuits, Systems, and Signal Processing, 2013, 32, 29-46.	2.0	8
20	Simple Precision Creation of Digitally Specified, Spatially Heterogeneous, Engineered Tissue Architectures. Advanced Materials, 2013, 25, 1192-1198.	21.0	67
21	Field Programmable Gate Array implementation of Conic Section Function Neural Network: An alternative to analog CSFNN circuitry. , 2012, , .		0
22	Conic Section Function Neural Network Circuitry for Offline Signature Recognition. IEEE Transactions on Neural Networks, 2010, 21, 667-672.	4.2	27
23	Improving classification performance of sonar targets by applying general regression neural network with PCA. Expert Systems With Applications, 2008, 35, 472-475.	7.6	70
24	CSFNN optimization of signature recognition problem for a special VLSI NN chip. , 2008, , .		4
25	Obtaining decision boundaries of CSFNN neurons using current mode analog circuitry. , 2007, , .		3
26	CSFNN Synapse and Neuron Design Using Current Mode Analog Circuitry. , 2007, , 17-25.		0