Scott Koziol

List of Publications by Year
in descending order

24 papers	399 citations	1163117 8 h-index	1281871 11 g-index
24 all docs	24 docs citations	24 times ranked	362 citing authors

#	Article	IF	Citations
1	Implementation and Feedback Control Tuning of an Analog Izhikevich Neuron Circuit. IEEE Access, 2022, 10, 67289-67304.	4.2	0
2	Multi-Objective Path Planning for Autonomous Robots Using Reconfigurable Analog VLSI. IEEE Access, 2020, 8, 80134-80147.	4.2	2
3	Tunable, Hardware-Based Quantum Random Number Generation Using Coupled Quantum Dots. IEEE Nanotechnology Magazine, 2020, 19, 292-296.	2.0	8
4	A quantitative method for evaluation of 6 degree of freedom virtual reality systems. Journal of Biomechanics, 2019, 97, 109379.	2.1	12
5	Robot Path Planning Using Analog Circuit Phase Delay. IEEE Transactions on Systems, Man, and Cybernetics: Systems, 2019, , 1-14.	9.3	2
6	Mobile Robot Rendezvous Using Potential Fields combined With Parallel Navigation. IEEE Access, 2018, 6, 16948-16957.	4.2	10
7	Mobile Robot Path Planning With a Moving Goal. IEEE Access, 2018, 6, 12800-12814.	4.2	20
8	Monitoring Human Head and Neck-Based Motions From Around-Neck Creeping Wave Propagations. IEEE Antennas and Wireless Propagation Letters, 2018, 17, 1199-1203.	4.0	17
9	Single-Objective Path Planning for Autonomous Robots Using Reconfigurable Analog VLSI. IEEE Transactions on Systems, Man, and Cybernetics: Systems, 2017, 47, 1301-1314.	9.3	12
10	Neuromorphic Computation Using Quantum-Dot Cellular Automata. , 2017, , .		3
11	Bio-inspired multi-agent search algorithms for locating and sampling environment threats. , 2017, , .		1
12	Transforming mixed-signal circuits class through SoC FPAA IC, PCB, and toolset. , 2016, , .		10
13	Efficient SIFT processing using sub-sampled convolution and masking techniques. , 2014, , .		0
14	Three dimensional robot path planning using a field programmable analog array. , 2014, , .		4
15	Low Power Dendritic Computation for Wordspotting. Journal of Low Power Electronics and Applications, 2013, 3, 73-98.	2.0	23
16	Robot path planning using Field Programmable Analog Arrays. , 2012, , .		15
17	FPAA chips and tools as the center of an design-based analog systems education. , 2011, , .		8
18	Reconfigurable Analog VLSI circuits for robot path planning. , 2011, , .		11

#	Article	IF	Citations
19	Using floating-gate based programmable analog arrays for real-time control of a game-playing robot. , 2011, , .		5
20	Hardware and software infrastructure for a family of floating-gate based FPAAs. , 2010, , .		29
21	A Floating-Gate-Based Field-Programmable Analog Array. IEEE Journal of Solid-State Circuits, 2010, 45, 1781-1794.	5.4	109
22	Neural Dynamics in Reconfigurable Silicon. IEEE Transactions on Biomedical Circuits and Systems, 2010, 4, 311-319.	4.0	73
23	RASP 2.8: A new generation of floating-gate based field programmable analog array. , 2008, , .		23
24	A biophysically based dendrite model using programmable floating-gate devices. , 2008, , .		2