

# Muh-Tian Shiue

## List of Publications by Year in descending order

Source: <https://exaly.com/author-pdf/5020503/publications.pdf>

Version: 2024-02-01

46  
papers

206  
citations

1684188  
5  
h-index

1588992  
8  
g-index

46  
all docs

46  
docs citations

46  
times ranked

196  
citing authors

#	ARTICLE	IF	CITATIONS
1	A Design of Input-Decimation Technique for Recursive DFT/IDFT Algorithm. IEEE Transactions on Circuits and Systems I: Regular Papers, 2019, 66, 4713-4726.	5.4	1
2	A Low-Computation-Cycle Design of Input-Decimation Technique for RIDFT Algorithm. , 2019, , .		1
3	A Bootstrapped Comparator-Switched Active Rectifying Circuit for Wirelessly Powered Integrated Miniaturized Energy Sensing Systems. Sensors, 2019, 19, 4714.	3.8	3
4	A body sensor node SoC for ECG/EMG applications with compressed sensing and wireless powering. , 2017, , .		1
5	Data-rate-efficient CMOS modulator for wireless biomedical sensor network applications. International Journal of Microwave and Wireless Technologies, 2017, 9, 1-8.	1.9	12
6	Decision-Directed Beamforming and Channel Equalization Algorithm for IEEE 802.11n OFDM Systems. , 2016, , .		3
7	Multiplication of a Constant ( $2k\hat{\pm}1$ ) and Its Fast Hardware Implementation. Journal of Signal Processing Systems, 2016, 82, 41-53.	2.1	0
8	Design of digital baseband inner receiver for PLC system based on IEEE P1901 specification. , 2015, , .		1
9	Design and implementation of the OFDM receiver for visible-light communication. , 2015, , .		2
10	Efficient Algorithm and Fast Hardware Implementation for Multiply-by- $(1+2^k)$ . IEICE Transactions on Fundamentals of Electronics, Communications and Computer Sciences, 2015, E98.A, 966-974.	0.3	0
11	Design and Implementation of Power-Efficient K-Best MIMO Detector for Configurable Antennas. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2014, 22, 2418-2422.	3.1	16
12	FPGA prototyping for CORDIC-based OFDM baseband receiver. , 2014, , .		0
13	CORDIC-based OFDM baseband receiver design. , 2014, , .		0
14	A CMOS multichannel electrical stimulation prototype system. International Journal of Circuit Theory and Applications, 2013, 41, 238-258.	2.0	6
15	An inductive biomedical communication processing chain. , 2013, , .		0
16	Low-cost parallel FFT processors with conflict-free ROM-based twiddle factor generator for DVB-T2 applications. , 2013, , .		4
17	Design of antenna-configurable MIMO detector with high speed sorting architectures. , 2012, , .		1
18	On investigation into A CMOS-process-based high-voltage driver applied to implantable microsystem. , 2012, , .		2

#	ARTICLE	IF	CITATIONS
19	An impedance measurement analog front end for wirelessly bioimplantable applications. , 2012, , .		0
20	I/Q imbalance compensation and channel equalization algorithm for MISO-OFDM systems. , 2012, , .		1
21	Self-powered integrated rectifier for wireless medical energy harvesting applications. , 2011, , .		2
22	Design of a neural recording amplifier with tunable pseudo resistors. , 2011, , .		4
23	Joint Carrier Synchronization and Equalization Algorithm for Packet-Based OFDM Systems Over the Multipath Fading Channel. IEEE Transactions on Vehicular Technology, 2010, 59, 248-260.	6.3	17
24	DHT-Based OFDM System for Passband Transmission Over Frequency-Selective Channel. IEEE Signal Processing Letters, 2010, 17, 699-702.	3.6	14
25	Joint Carrier Synchronization and Equalization Algorithm for OFDM Systems – Closed-Loop Derivation. , 2009, , .		1
26	Cost efficient FEQ implementation for IEEE 802.16a OFDM transceiver. , 2009, , .		1
27	10Gbps decision feedback equalizer with dynamic lookahead decision loop. , 2009, , .		7
28	A low complexity real-valued kernel DHT-based OFDM modulator/demodulator design. , 2009, , .		1
29	Joint Carrier Synchronization and Equalization Algorithm for OFDM Systems - Loop-Delay Analysis and Complexity Consideration. , 2009, , .		1
30	On the DHT-based multicarrier transceiver over multipath fading channel. , 2009, , .		6
31	A low power and variable-length FFT processor design for flexible MIMO OFDM systems. , 2009, , .		10
32	A chopper stabilized front-end for neural recording applications with DC-drift suppressed amplifier. , 2009, , .		2
33	A differential difference amplifier for neural recording system with tunable low-frequency cutoff. , 2008, , .		6
34	A 28mW OFDM baseband receiver chip for DVB-T/H with all digital synchronization. , 2008, , .		2
35	A 256-point dataflow scheduling 2&#x00D7;2 MIMO FFT/IFFT processor for IEEE 802.16 WMAN. , 2008, , .		10
36	A Miniaturized PSK Demodulation Device for Short-Range Wireless Neural Prosthetic Systems. , 2008, , .		4

#	ARTICLE	IF	CITATIONS
37	Efficient CMOS rectifier for inductively power-harvested implants. , 2008, , .		2
38	A Truly Low-Cost High-Efficiency ASK Demodulator Based on Self-Sampling Scheme for Bioimplantable Applications. IEEE Transactions on Circuits and Systems I: Regular Papers, 2008, 55, 1464-1477.	5.4	38
39	A compact and low-power SRAM with improved read static noise margin. , 2008, , .		0
40	An efficiency-enhanced CMOS voltage regulator module for bio-electronic implants. , 2008, , .		1
41	Joint Carrier Synchronization and Equalization for OFDM Systems Over Multipath Fading Channel. , 2008, , .		6
42	A pseudo-flash ADC for large-channel-volume biomedical sensing applications. , 2008, , .		2
43	Design And Implementation Of A Monolithic Programme-Controlled System For Retinal Prosthesis. , 2006, , .		4
44	An Efficient Micro-Stimulator Array Using Unitary-Size DAC With Adiabatic Baseband Scheme. , 2006, , .		4
45	A VLSI design of dual-loop automatic gain control for dual-mode QAM/VSB CATV modem. , 0, , .		5
46	A hardware efficient 64-QAM low-IF transceiver baseband for broadband communications. , 0, , .		2