## José Ricardo Cardenas-Valdez

List of Publications by Year in descending order

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1937685 1872680 6 124 4 6 citations h-index g-index papers 6 6 6 129 docs citations times ranked citing authors all docs

#	Article	IF	CITATIONS
1	A Crest Factor Reduction Technique for LTE Signals with Target Relaxation in Power Amplifier Linearization. Sensors, 2022, 22, 1176.	3.8	1
2	A Weighted Linearization Method for Highly RF-PA Nonlinear Behavior Based on the Compression Region Identification. Applied Sciences (Switzerland), 2021, 11, 2942.	2.5	4
3	Implementing a Chaotic Cryptosystem by Performing Parallel Computing on Embedded Systems with Multiprocessors. Entropy, 2019, 21, 268.	2.2	24
4	Design and Construction of an ROV for Underwater Exploration. Sensors, 2019, 19, 5387.	3.8	38
5	FPGA-based Chaotic Cryptosystem by Using Voice Recognition as Access Key. Electronics (Switzerland), 2018, 7, 414.	3.1	46
6	Flexible test bed for the behavioural modelling of power amplifiers. COMPEL - the International Journal for Computation and Mathematics in Electrical and Electronic Engineering, 2013, 33, 355-375.	0.9	11