

# JosÃ© Ricardo Cardenas-Valdez

## List of Publications by Year in descending order

Source: <https://exaly.com/author-pdf/4999214/publications.pdf>

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6  
papers

124  
citations

1937685

4  
h-index

1872680

6  
g-index

6  
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6  
docs citations

6  
times ranked

129  
citing authors

#	ARTICLE	IF	CITATIONS
1	FPGA-based Chaotic Cryptosystem by Using Voice Recognition as Access Key. Electronics (Switzerland), 2018, 7, 414.	3.1	46
2	Design and Construction of an ROV for Underwater Exploration. Sensors, 2019, 19, 5387.	3.8	38
3	Implementing a Chaotic Cryptosystem by Performing Parallel Computing on Embedded Systems with Multiprocessors. Entropy, 2019, 21, 268.	2.2	24
4	Flexible test bed for the behavioural modelling of power amplifiers. COMPEL - the International Journal for Computation and Mathematics in Electrical and Electronic Engineering, 2013, 33, 355-375.	0.9	11
5	A Weighted Linearization Method for Highly RF-PA Nonlinear Behavior Based on the Compression Region Identification. Applied Sciences (Switzerland), 2021, 11, 2942.	2.5	4
6	A Crest Factor Reduction Technique for LTE Signals with Target Relaxation in Power Amplifier Linearization. Sensors, 2022, 22, 1176.	3.8	1