Daniel Wong

List of Publications by Year
in descending order

31 papers	473 citations	1478505 6 h-index	1588992 8 g-index
32 all docs	32 docs citations	32 times ranked	305 citing authors

#	ARTICLE	IF	CITATIONS
1	KnightShift: Scaling the Energy Proportionality Wall through Server-Level Heterogeneity. , 2012, , .		77
2	Warped gates. , 2013, , .		48
3	Approximating warps with intra-warp operand value similarity. , 2016, , .		33
4	GPU-NEST: Characterizing Energy Efficiency of Multi-GPU Inference Servers. IEEE Computer Architecture Letters, 2020, 19, 139-142.	1.5	26
5	Adaptive and Speculative Slack Simulations of CMPs on CMPs. , 2010, , .		25
6	Implications of high energy proportional servers on cluster-wide energy proportionality. , 2014, , .		24
7	Peak Efficiency Aware Scheduling for Highly Energy Proportional Servers. , 2016, , .		24
8	Wireframe. , 2017, , .		24
9	DynSleep. , 2016, , .		22
10	Î¼DPM: Dynamic Power Management for the Microsecond Era. , 2019, , .		21
11	Frequency regulation service provision in data center with computational flexibility. Applied Energy, 2019, 251, 113304.	10.1	18
12	CORF. , 2019, , .		15
13	PAVER. Transactions on Architecture and Code Optimization, 2021, 18, 1-26.	2.0	15
14	Joint Server and Network Energy Saving in Data Centers for Latency-Sensitive Applications. , 2018, , .		13
15	Scaling the Energy Proportionality Wall with KnightShift. IEEE Micro, 2013, 33, 28-37.	1.8	11
16	Origami. , 2016, , .		8
17	Speeding up Collective Communications Through Inter-GPU Re-Routing. IEEE Computer Architecture Letters, 2019, 18, 128-131.	1.5	8
18	BlockMaestro: Enabling Programmer-Transparent Task-based Execution in GPU Systems. , 2021, , .		8

#	Article	IF	Citations
19	BOW: Breathing Operand Windows to Exploit Bypassing in GPUs. , 2020, , .		7
20	Peak efficiency aware scheduling for highly energy proportional servers. Computer Architecture News, 2016, 44, 481-492.	2.5	7
21	MAPA. , 2021, , .		7
22	Load-Triggered Warp Approximation on GPU. , 2018, , .		6
23	Locality-Aware GPU Register File. IEEE Computer Architecture Letters, 2019, 18, 153-156.	1.5	6
24	Invited - Cross-layer modeling and optimization for electromigration induced reliability. , 2016, , .		5
25	A Retrospective Look Back on the Road Towards Energy Proportionality. , 2015, , .		4
26	High-Performance Parallel Radix Sort on FPGA. , 2020, , .		4
27	Implementing games on pinball machines. , 2010, , .		3
28	PowerMorph: QoS-Aware Server Power Reshaping for Data Center Regulation Service. Transactions on Architecture and Code Optimization, 2022, 19, 1-27.	2.0	2
29	STOMP: Statistical Techniques for Optimizing and Modeling Performance of Blocked Sparse Matrix Vector Multiplication. , 2016, , .		1
30	Energy Efficient Task Graph Execution Using Compute Unit Masking in GPUs. , 2021, , .		1
31	ICAP: Designing Inrush Current Aware Power Gating Switch for GPGPU. , 2021, , .		0