

# Alper Buyuktosunoglu

## List of Publications by Year in descending order

Source: <https://exaly.com/author-pdf/4953229/publications.pdf>

Version: 2024-02-01

19  
papers

831  
citations

1684188

5  
h-index

1281871

11  
g-index

19  
all docs

19  
docs citations

19  
times ranked

583  
citing authors

#	ARTICLE	IF	CITATIONS
1	An Analysis of Efficient Multi-Core Global Power Management Policies: Maximizing Performance for a Given Power Budget. <i>Microarchitecture (MICRO), Proceedings of the Annual International Symposium on, 2006, , .</i>	0.0	472
2	Introducing the Adaptive Energy Management Features of the Power7 Chip. <i>IEEE Micro, 2011, 31, 60-75.</i>	1.8	95
3	Safe limits on voltage reduction efficiency in GPUs. , 2015, , .		60
4	Crank it up or dial it down. , 2013, , .		55
5	Voltage Noise in Multi-Core Processors: Empirical Characterization and Optimization Opportunities. , 2014, , .		52
6	Droop mitigation using critical-path sensors and an on-chip distributed power supply estimation engine in the z14â„ enterprise processor. , 2018, , .		16
7	Energy-Aware Accounting and Billing in Large-Scale Computing Facilities. <i>IEEE Micro, 2011, 31, 60-71.</i>	1.8	14
8	Asymmetric Resilience: Exploiting Task-Level Idempotency for Transient Error Recovery in Accelerator-Based Systems. , 2020, , .		14
9	IBM z14: Processor Characterization and Power Management for High-Reliability Mainframe Systems. <i>IEEE Journal of Solid-State Circuits, 2019, 54, 121-132.</i>	5.4	11
10	CPU Accounting in CMP Processors. <i>IEEE Computer Architecture Letters, 2009, 8, 17-20.</i>	1.5	9
11	Energy Efficiency Boost in the AI-Infused POWER10 Processor. , 2021, , .		8
12	Heterogeneity-Aware Scheduling on SoCs for Autonomous Vehicles. <i>IEEE Computer Architecture Letters, 2021, 20, 82-85.</i>	1.5	6
13	Predictive Guardbanding: Program-Driven Timing Margin Reduction for GPUs. <i>IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 2021, 40, 171-184.</i>	2.7	5
14	Cores, Cache, Content, and Characterization: IBMâ€™s Second Generation 14-nm Product, z15. <i>IEEE Journal of Solid-State Circuits, 2021, 56, 98-111.</i>	5.4	4
15	Intelligent Adaptation of Hardware Knobs for Improving Performance and Power Consumption. <i>IEEE Transactions on Computers, 2021, 70, 1-16.</i>	3.4	3
16	AI accelerator on IBM Telum processor. , 2022, , .		3
17	Program behavior prediction using a statistical metric model. <i>Performance Evaluation Review, 2010, 38, 371-372.</i>	0.6	2
18	Asymmetric Resilience for Accelerator-Rich Systems. <i>IEEE Computer Architecture Letters, 2019, 18, 83-86.</i>	1.5	2

#	ARTICLE	IF	CITATIONS
19	TokenSmart: Distributed, Scalable Power Management in the Many-Core Era. IEEE Computer Architecture Letters, 2021, 20, 42-45.	1.5	0