

# Chao Luo

## List of Publications by Year in descending order

Source: <https://exaly.com/author-pdf/494/publications.pdf>

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8  
papers

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1937685

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8  
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docs citations

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times ranked

71  
citing authors

#	ARTICLE	IF	CITATIONS
1	A cryogenic low power CMOS analog buffer at 4.2K. IEICE Electronics Express, 2021, 18, 20210183-20210183.	0.8	1
2	Characterization and Modeling of Native MOSFETs Down to 4.2 K. IEEE Transactions on Electron Devices, 2021, 68, 4267-4273.	3.0	18
3	Hot Carrier Degradation in MOSFETs at Cryogenic Temperatures Down to 4.2 K. IEEE Transactions on Device and Materials Reliability, 2021, 21, 620-626.	2.0	4
4	Characterization and Modeling of 0.18 $\mu\text{m}$ Bulk CMOS Technology at Sub-Kelvin Temperature. IEEE Journal of the Electron Devices Society, 2020, 8, 897-904.	2.1	10
5	Current Gain Enhancement for Silicon-on-Insulator Lateral Bipolar Junction Transistors Operating at Liquid-Helium Temperature. IEEE Electron Device Letters, 2020, 41, 800-803.	3.9	2
6	0.18 $\mu\text{m}$ CMOS Ring Oscillator at Liquid Helium Temperature. , 2019, , .		0
7	MOSFET characterization and modeling at cryogenic temperatures. Cryogenics, 2019, 98, 12-17.	1.7	43
8	Modelling and kink correction of bulk CMOS at liquid helium temperature. Electronics Letters, 2019, 55, 780-783.	1.0	4