

# Leonid Belostotski

## List of Publications by Year in descending order

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177  
papers

1,728  
citations

393982

19  
h-index

395343

33  
g-index

178  
all docs

178  
docs citations

178  
times ranked

964  
citing authors

#	ARTICLE	IF	CITATIONS
1	Minimizing the Noise Penalty Due to Mutual Coupling for a Receiving Array. IEEE Transactions on Antennas and Propagation, 2009, 57, 1634-1644.	3.1	92
2	High-quality factor asymmetric-slope band-pass filters: a fractional-order capacitor approach. IET Circuits, Devices and Systems, 2012, 6, 187.	0.9	86
3	Noise figure optimization of inductively degenerated CMOS LNAs with integrated gate inductors. IEEE Transactions on Circuits and Systems Part 1: Regular Papers, 2006, 53, 1409-1422.	0.1	83
4	Sub-0.2 dB Noise Figure Wideband Room-Temperature CMOS LNA With Non-50 $\Omega$ Signal-Source Impedance. IEEE Journal of Solid-State Circuits, 2007, 42, 2492-2502.	3.5	75
5	Integrated Design of a Class-J Power Amplifier. IEEE Transactions on Microwave Theory and Techniques, 2013, 61, 1639-1648.	2.9	68
6	RF Analog Beamforming Fan Filters Using CMOS All-Pass Time Delay Approximations. IEEE Transactions on Circuits and Systems I: Regular Papers, 2012, 59, 1061-1073.	3.5	57
7	10-Gb/s 0.13- $\mu\text{m}$ CMOS Inductorless Modified-RGC Transimpedance Amplifier. IEEE Transactions on Circuits and Systems I: Regular Papers, 2015, 62, 1971-1980.	3.5	57
8	A New Second-Order All-Pass Filter in 130-nm CMOS. IEEE Transactions on Circuits and Systems II: Express Briefs, 2016, 63, 249-253.	2.2	45
9	Wideband $\beta$ -Beam Arrays Using Low-Complexity Algorithms and Mixed-Signal Integrated Circuits. IEEE Journal on Selected Topics in Signal Processing, 2018, 12, 368-382.	7.3	38
10	Evaluation of Tuner-Based Noise-Parameter Extraction Methods for Very Low Noise Amplifiers. IEEE Transactions on Microwave Theory and Techniques, 2010, 58, 236-250.	2.9	36
11	Low-Noise Amplifier Design Considerations For Use in Antenna Arrays. IEEE Transactions on Antennas and Propagation, 2015, 63, 2508-2520.	3.1	30
12	5-bit 5-GS/s Noninterleaved Time-Based ADC in 65-nm CMOS for Radio-Astronomy Applications. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2016, 24, 3513-3525.	2.1	30
13	A 0.13- $\mu\text{m}$ CMOS Current-Mode All-Pass Filter for Multi-GHz Operation. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2015, 23, 2813-2818.	2.1	29
14	A Technique for Differential Noise Figure Measurement of Differential LNAs. IEEE Transactions on Instrumentation and Measurement, 2008, 57, 1298-1303.	2.4	27
15	Wide-band CMOS low noise amplifier for applications in radio astronomy. , 0, , .		26
16	On Impedance-Pattern Selection for Noise Parameter Measurement. IEEE Transactions on Microwave Theory and Techniques, 2016, 64, 258-270.	2.9	26
17	Analog Approximate-FFT 8/16-Beam Algorithms, Architectures and CMOS Circuits for 5G Beamforming MIMO Transceivers. IEEE Journal on Emerging and Selected Topics in Circuits and Systems, 2018, 8, 466-479.	2.7	24
18	Wide Band Room Temperature 0.35-dB Noise Figure LNA in 90-nm Bulk CMOS. , 2007, , .		22

#	ARTICLE	IF	CITATIONS
19	A 65-nm CMOS 10-GS/s 4-bit Background-Calibrated Noninterleaved Flash ADC for Radio Astronomy. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2014, 22, 2316-2325.	2.1	22
20	All-Pass Filter-Based 2-D IIR Filter-Enhanced Beamformers for AESA Receivers. IEEE Transactions on Circuits and Systems I: Regular Papers, 2014, 61, 1331-1342.	3.5	22
21	Highly-linear time-difference amplifier with low sensitivity to process variations. Electronics Letters, 2011, 47, 743-745.	0.5	21
22	Wideband LNA With an Active -C Element. IEEE Microwave and Wireless Components Letters, 2012, 22, 524-526.	2.0	20
23	Two-port noise figure optimization of source-degenerated cascode CMOS LNAs. Analog Integrated Circuits and Signal Processing, 2008, 55, 125-137.	0.9	19
24	Noise Performance of a Phased-Array Feed With CMOS Low-Noise Amplifiers. IEEE Antennas and Wireless Propagation Letters, 2016, 15, 1719-1722.	2.4	19
25	Wideband LNA Noise Matching. IEEE Solid-State Circuits Letters, 2020, 3, 62-65.	1.3	19
26	A CMOS Low-Power Cross-Coupled Imittance-Converter Transimpedance Amplifier. IEEE Microwave and Wireless Components Letters, 2015, 25, 403-405.	2.0	18
27	No Noise Is Good Noise: Noise Matching, Noise Canceling, and Maybe a Bit of Both for Wide-Band LNAs. IEEE Microwave Magazine, 2016, 17, 28-40.	0.7	18
28	Offset-corrected 5GHz CMOS dynamic comparator using bulk voltage trimming: Design and analysis. , 2011, , .		16
29	A Calibration Method for RF and Microwave Noise Sources. IEEE Transactions on Microwave Theory and Techniques, 2011, 59, 178-187.	2.9	16
30	A 5GS/s 4-bit time-based single-channel CMOS ADC for radio astronomy. , 2013, , .		16
31	A Steerable DC-1 GHz all-pass filter-Sum RF space-time 2-D beam filter in 65 nm CMOS. , 2013, , .		16
32	Harmonically Tuned Continuous Class-C Operation Mode for Power Amplifier Applications. IEEE Transactions on Microwave Theory and Techniques, 2014, 62, 3017-3027.	2.9	14
33	8-GHz, 6.6-mW LC-VCO with Small Die Area and FOM of 204 dBc/Hz at 1-MHz Offset. IEEE Microwave and Wireless Components Letters, 2016, 26, 936-938.	2.0	14
34	400-to-800-MHz GaAs pHEMT-Based Wideband LNA for Radio-Astronomy Antenna-Array Feed. IEEE Microwave and Wireless Components Letters, 2018, 28, 909-911.	2.0	13
35	Continuous-Time Algorithms for Solving Maxwell's Equations Using Analog Circuits. IEEE Transactions on Circuits and Systems I: Regular Papers, 2019, 66, 3941-3954.	3.5	13
36	A Wideband Delay-Tunable Fully Differential Allpass Filter in 65-nm CMOS Technology. , 2019, , .		12

#	ARTICLE	IF	CITATIONS
37	Synthesis of Wideband High-Quality Factor Delay-Tunable Fully Differential All-Pass Filters. IEEE Transactions on Microwave Theory and Techniques, 2020, 68, 4348-4360.	2.9	12
38	On the Number of Noise Parameters for Analyses of Circuits With MOSFETs. IEEE Transactions on Microwave Theory and Techniques, 2011, 59, 877-881.	2.9	11
39	Wideband CMOS Amplification Stage for a Direct-Sampling Square Kilometre Array Receiver. IEEE Transactions on Microwave Theory and Techniques, 2012, 60, 3179-3188.	2.9	11
40	A 12.5-Gb/s On-Chip Oscilloscope to Measure Eye Diagrams and Jitter Histograms of High-Speed Signals. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2014, 22, 1127-1137.	2.1	11
41	6-GHz all-pass-filter-based delay-and-sum beamformer in 130nm CMOS. , 2014, , .		11
42	Improving ADC figure-of-merit in wideband antenna array receivers using multidimensional space-time delta-sigma multiport circuits. , 2017, , .		11
43	Wideband third-order single-transistor all-pass filter. International Journal of Circuit Theory and Applications, 2020, 48, 1201-1208.	1.3	11
44	Study of Thermal Noise Generated in a Vivaldi Antenna Using the Improved Wheeler Cap Method. IEEE Antennas and Wireless Propagation Letters, 2011, 10, 1047-1050.	2.4	10
45	Multi-beam 4 GHz microwave apertures using current-mode DFT approximation on 65 nm CMOS. , 2015, , .		10
46	Single transistor RC-only second-order allpass filters. International Journal of Circuit Theory and Applications, 2020, 48, 162-169.	1.3	10
47	Down With Noise: An Introduction to a Low-Noise Amplifier Survey. IEEE Solid-State Circuits Magazine, 2020, 12, 23-29.	0.5	10
48	Figures of Merit for CMOS Low-Noise Amplifiers and Estimates for Their Theoretical Limits. IEEE Transactions on Circuits and Systems II: Express Briefs, 2022, 69, 734-738.	2.2	10
49	A dynamical economic model of sustainable agriculture and the ecosphere. Applied Mathematics and Computation, 1997, 84, 221-246.	1.4	9
50	A four-quadrant current multiplier/divider cell with four transistors. Analog Integrated Circuits and Signal Processing, 2018, 95, 173-179.	0.9	9
51	Ultra-Wideband Front-End With Tunable Notch Filter. , 2006, , .		8
52	35.5 GHz Parametric CMOS Upconverter. IEEE Microwave and Wireless Components Letters, 2012, 22, 477-479.	2.0	8
53	A bandwidth enhancement technique for CMOS TIAs driven by large photodiodes. , 2012, , .		8
54	On the use of multi-path inductorless TIAs for larger transimpedance limit. Analog Integrated Circuits and Signal Processing, 2013, 77, 221-233.	0.9	8

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55	10-GHz current-mode 1 <sup>st</sup> - and 2 <sup>nd</sup> -order allpass filters on 130nm CMOS. , 2013, , .		8
56	Parametric CMOS upconverters and downconverters. International Journal of Circuit Theory and Applications, 2014, 42, 1209-1227.	1.3	8
57	WIDE-BAND TWO-STAGE GAAS LNA FOR RADIO ASTRONOMY. Progress in Electromagnetics Research C, 2015, 56, 119-124.	0.6	8
58	Noise Parameters of Gilbert Cell Mixers. IEEE Transactions on Microwave Theory and Techniques, 2016, 64, 3163-3174.	2.9	8
59	Design methodology of an analog 9-beam squint-free wideband IF multi-beamformer for mmW applications. , 2017, , .		8
60	Automatic Generation of Differential-Input Differential-Output Second-Order Filters Based on a Differential Pair. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 2020, 39, 1258-1271.	1.9	8
61	Wideband 28-nm CMOS Variable-Gain Amplifier. IEEE Transactions on Circuits and Systems I: Regular Papers, 2020, 67, 37-47.	3.5	8
62	<title>The large adaptive reflector: a 200-m diameter wideband centimeter- to meter-wave radio telescope</title>. , 2000, 4015, 33.		7
63	A technique for microwave ranging and remote phase synchronization. IEEE Transactions on Instrumentation and Measurement, 2002, 51, 551-559.	2.4	7
64	Noise figure optimization of wide-band inductively-degenerated CMOS LNAs. Midwest Symposium on Circuits and Systems, 2007, , .	1.0	7
65	Analog 2D fan filters from discrete domain transfer functions. , 2011, , .		7
66	The first CMOS LNA on a radio telescope. , 2014, , .		7
67	A 460â€GHz CMOS substrateâ€integratedâ€waveguide slotâ€antenna design. Microwave and Optical Technology Letters, 2016, 58, 347-351.	0.9	7
68	A Stagger-Tuned Transimpedance Amplifier. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2016, 24, 1460-1469.	2.1	7
69	Antenna Two-Port Electrical and Noise Parameters. IEEE Antennas and Wireless Propagation Letters, 2017, 16, 1265-1268.	2.4	7
70	Single-Transistor Second-Order Allpass Filters. , 2019, , .		7
71	28-GHz Passive Frequency Tripler With n-Type Varactors in 45-nm SOI CMOS. IEEE Microwave and Wireless Components Letters, 2020, 30, 292-295.	2.0	7
72	Distance measurement with phase-stable CW radio link using the Chinese remainder theorem. Electronics Letters, 2001, 37, 521.	0.5	6

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73	Noise figure optimization of a noise-cancelling wide-band CMOS LNA. , 2010, , .		6
74	Comparison of LNAs fabricated in 65-nm CMOS GP and LP processes for the Square Kilometre Array. IEICE Electronics Express, 2012, 9, 636-641.	0.3	6
75	A 2-D Signal Processing Model to Predict the Effect of Mutual Coupling on Array Factor. IEEE Antennas and Wireless Propagation Letters, 2013, 12, 1264-1267.	2.4	6
76	65-nm CMOS Voltage-to-Time Converter for 5-GS/s Time-Based ADCs. Circuits, Systems, and Signal Processing, 2015, 34, 3121-3145.	1.2	6
77	Delta-sigma noise shaping in 2D spacetime for uniform linear aperture array receivers. , 2016, , .		6
78	Synthesis and analysis of fully differential filters using two port networks. , 2017, , .		6
79	<i>D</i>-Band Broadband Passive Frequency Tripler Using Antiparallel Diode-Connected nMOS Transistor Pair in 22-nm CMOS SOI. IEEE Microwave and Wireless Components Letters, 2020, 30, 689-692.	2.0	6
80	A Radio Frequency Analog Computer for Computational Electromagnetics. IEEE Journal of Solid-State Circuits, 2021, 56, 440-454.	3.5	6
81	Relations of Time-Varying Circuit Parameters and Idlerless Parametric Harmonic Generation for Reconfigurable Frequency Multipliers. IEEE Transactions on Microwave Theory and Techniques, 2021, 69, 2554-2568.	2.9	6
82	Method of Generating Unique Elementary Circuit Topologies Méthode de génération de topologies de circuits élémentaires uniques. Canadian Journal of Electrical and Computer Engineering, 2018, 41, 118-132.	1.5	6
83	STABILITY AND BIFURCATIONS IN AN ENVIRONMENTAL RECOVERY MODEL OF ECONOMIC AGRICULTURE-INDUSTRY INTERACTIONS. Natural Resource Modelling, 1998, 11, 35-79.	0.8	5
84	A technique for differential noise figure measurement with a noise figure analyzer [Technical Committee]. IEEE Microwave Magazine, 2009, 10, 158-161.	0.7	5
85	Band-pass filters with high quality factors and asymmetric-slope characteristics. , 2011, , .		5
86	Efficiency Measurement of Connected Arrays Using the Improved Wheeler Cap Method. IEEE Transactions on Antennas and Propagation, 2012, 60, 5147-5156.	3.1	5
87	Analysis of the ADC resolution for radio astronomy applications. , 2014, , .		5
88	Low-complexity N-port ADCs using 2-D $\hat{\tau}$ - $\hat{\xi}$ noise-shaping for N-element array receivers. , 2017, , .		5
89	Impact of Noise Bandwidth on Noise Figure. IEEE Transactions on Instrumentation and Measurement, 2019, 68, 2662-2664.	2.4	5
90	8-GHz Low-Power Voltage-Mode Second-Order Allpass Filter in 65-nm CMOS. , 2019, , .		5

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91	An Ultra-Low Power Wide-Band Single-Transistor Second-Order Allpass Filter in 65nm CMOS. , 2019, , .		5
92	On chip 0.5-2 GHz four-output quadrature-phase oscillator. AEU - International Journal of Electronics and Communications, 2020, 126, 153393.	1.7	5
93	FPGA-Based 2-D FIR Frost Beamformers with Digital Mutual Coupling Compensation. , 2020, , .		5
94	Cryogenic Noise-Parameter Measurements: Recent Research and a Fully Automated Measurement Application. IEEE Microwave Magazine, 2021, 22, 52-64.	0.7	5
95	Linearization Techniques for Cross-Coupled Transconductor Circuits Used in Integrated Q-Enhanced LC Filters. , 2006, , .		4
96	A broadband automatic gain control amplifier for the Square Kilometer Array. , 2010, , .		4
97	Modelling of wideband inter-element EM coupling in 2D space-time frequency domain. , 2011, , .		4
98	Modeling and measuring of antenna array s-parameters and radiation efficiency. , 2017, , .		4
99	All-Pass Filter Based Synthesis of Multifunctional Microwave Active Circuits. , 2017, , .		4
100	Design of a low-complexity wideband analog true-time-delay 5-beam array in 65nm CMOS. , 2017, , .		4
101	N-port LNAs for mmW array processors using 2-D spatio-temporal noise-shaping. , 2017, , .		4
102	Analytical Determinant of the Noise Parameter Extraction Matrix and Its Applications. , 2019, , .		4
103	A 167-172 GHz 65nm CMOS body-voltage-tuned harmonic-mode voltage-controlled oscillator. Microwave and Optical Technology Letters, 2019, 61, 546-549.	0.9	4
104	Towards a Low-SWaP 1024-Beam Digital Array: A 32-Beam Subsystem at 5.8 GHz. IEEE Transactions on Antennas and Propagation, 2020, 68, 900-912.	3.1	4
105	A Measure of Well-Spread Points in Noise Wave-Based Source Matrix for Wideband Noise Parameter Measurement: The SKA-Low Example. IEEE Transactions on Microwave Theory and Techniques, 2020, 68, 1783-1793.	2.9	4
106	Ultra-low-power compact single-transistor all-pass filter with tunable delay capability. AEU - International Journal of Electronics and Communications, 2021, 132, 153645.	1.7	4
107	On Selection of Optimum Signal Source Impedance for Inductively-Degenerated CMOS LNAs. , 2006, , .		3
108	A new class of spatially-discrete time-continuous 2D IIR filters based on wave-digital-filter theory. , 2011, , .		3

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109	A broadband Variable Gain Amplifier for the Square Kilometer Array. , 2013, , .		3
110	Electronically scanned RF-to-bits beam aperture arrays using 2-D IIR spatially bandpass digital filters. Multidimensional Systems and Signal Processing, 2014, 25, 313-335.	1.7	3
111	A 65Ånm CMOS broadband self-calibrated power detector for the square kilometre array radio telescope. Journal of Engineering, 2014, 2014, 494-502.	0.6	3
112	A Q-band high-gain substrate-integrated waveguide slot antenna. Microwave and Optical Technology Letters, 2015, 57, 1370-1374.	0.9	3
113	Measuring antenna noise parameters using a set of Wheeler caps. , 2016, , .		3
114	Low noise phased-array feed with CMOS LNAs. , 2016, , .		3
115	A highly linear wideband 0.3-to-2.7ÅGHz variable-gain amplifier. Analog Integrated Circuits and Signal Processing, 2017, 91, 473-478.	0.9	3
116	Millimeter-wave CMOS PLL using a push-push oscillator. , 2017, , .		3
117	Gain-configurable lower sideband parametric downconverter. , 2017, , .		3
118	A 28 GHz Q-Tunable Fully Differential Bandpass Filter in 65-nm CMOS Technology. , 2018, , .		3
119	Low-Power Single-Transistor Voltage-Mode Third-Order All-pass Filter in 65-nm CMOS. , 2020, , .		3
120	Antenna-Array Network Model. IEEE Transactions on Antennas and Propagation, 2020, 68, 5387-5394.	3.1	3
121	Delay-Tunable Compact RC-Only All-Pass Filter. IEEE Microwave and Wireless Components Letters, 2021, 31, 461-464.	2.0	3
122	An Image-Reject Low-Noise Amplifier with Passive Q-Enhanced Notch Filters. , 2007, , .		2
123	Signal-to-Noise-Ratio-constrained jitter optimization for wideband amplifiers. , 2012, , .		2
124	An analog-design assistant tool and an example of its application. , 2013, , .		2
125	1.6 GHz &#x2013; 3 GHz, 10W, 60% efficiency class-J PA for cognitive radio applications. , 2013, , .		2
126	Analog 2-D IIR beam filters for EARS in UAS ecosystems. , 2014, , .		2



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127	A 65nm CMOS 0.1&#x2013;2.1GHz linear-in-dB VGA with active-inductor bandwidth extension for the Square Kilometer Array. , 2014, , .		2
128	A broadband self-calibrated RMS power detector embedded in a square kilometre array receiver. Microwave and Optical Technology Letters, 2015, 57, 1426-1432.	0.9	2
129	Radiation efficiency of an individual antenna in a system of multiple non-identical antennas. , 2017, , .		2
130	Cryo-CMOS Low-Noise Amplifier for the Square Kilometre Array. , 2018, , .		2
131	RF-Rate Hybrid CNN Accelerator Based on Analog-CMOS and Xilinx RFSoc. , 2020, , .		2
132	Wideband Modeling of the Mud-Pulse Communications Channel. IEEE Communications Letters, 2021, 25, 18-22.	2.5	2
133	A Cryo-CMOS Low-Noise Amplifier With 2.3-to-8.5-K Noise Temperature at 20 K for Highly Integrated Radio-Astronomy Receivers. IEEE Microwave and Wireless Components Letters, 2022, 32, 1319-1322.	2.0	2
134	Evaluation of packaging options for very low noise amplifiers. , 2010, , .		1
135	Low-power CMOS inductorless bandwidth-enhanced transimpedance amplifier for short-haul applications. , 2013, , .		1
136	Low-power CMOS inductorless bandwidth-enhanced transimpedance amplifier for short-haul applications. , 2013, , .		1
137	University of Calgary participation in CCAT CHAI development. , 2015, , .		1
138	An overview of multi-dimensional RF signal processing for array receivers. , 2015, , .		1
139	Continuous-time 2-D IIR+time-delay linear aperture arrays. , 2015, , .		1
140	Linear RF apertures using 2-D analog beam filters. , 2016, , .		1
141	Wideband aperture array using a four-channel manifold-type planar multiplexer and digital 2-D IIR filterbank. International Journal of Circuit Theory and Applications, 2016, 44, 2085-2100.	1.3	1
142	Development of a CMOS receiver for a radio-telescope phased-array feed. , 2016, , .		1
143	Sampling H- & V-Polarized Antennas using a Single ADC for Digital Antenna Arrays by Exploiting Multi-Dimensional Signal Processing RF Circuits. , 2018, , .		1
144	Multipoint ADCs for Microwave Focal Plane Array Dish Receivers. , 2018, , .		1

#	ARTICLE	IF	CITATIONS
145	A 171 GHz harmonic-mode PLL with $\hat{a}^{\circ}14.2$ dBm output power in 65 nm CMOS. Analog Integrated Circuits and Signal Processing, 2019, 98, 643-649.	0.9	1
146	Linearity Analysis of CMOS Parametric Upconverters. IEEE Access, 2020, 8, 190906-190921.	2.6	1
147	Passive Third-, Fourth-, and Fifth-Order Reconfigurable D-Band Frequency Multipliers Based on Switched-Capacitor Varactors. , 2020, , .		1
148	A Demonstration of a Voltage-Controlled Inductor in a <i>D</i> -Band Colpitts Third-Harmonic-Extracted Injection-Locked Oscillator. IEEE Microwave and Wireless Components Letters, 2020, 30, 969-972.	2.0	1
149	Impact of bandwidth on antenna array noise matching. Electronics Letters, 2021, 57, 158-160.	0.5	1
150	Wideband High-Order All-Pass Delay Circuits. , 2021, , .		1
151	Framework for the Cosimulation of Antenna Arrays and Receivers. IEEE Transactions on Antennas and Propagation, 2021, 69, 5090-5094.	3.1	1
152	Spacetime Frequency-Multiplexed Digital-RF Array Receivers With Reduced ADC Count. IEEE Transactions on Circuits and Systems II: Express Briefs, 2021, 68, 2840-2844.	2.2	1
153	Design Equations for Closely Spaced Two-Element Interferometer for Radio Cosmology. , 0, 2, .		1
154	Automated Noise-Parameter Measurements of Cryogenic LNAs. , 2021, , .		1
155	Second-order cascode-based filters. The Integration VLSI Journal, 2022, 84, 111-121.	1.3	1
156	Correction to "Noise Figure Optimization of Inductively-Degenerated CMOS LNA's With Integrated Gate Inductors". IEEE Transactions on Circuits and Systems Part 1: Regular Papers, 2007, 54, 925-925.	0.1	0
157	Recent progress on analog/digital VLSI 2D filter circuits for beamforming antenna arrays. , 2011, , .		0
158	Efficiency measurement of 1-D connected array using the Improved Wheeler Cap method. , 2012, , .		0
159	Discrete space continuous time 2D delay block using 2D all-pass frequency planar networks. , 2012, , .		0
160	Towards RF analog IC realization of wave-discrete filters on 65nm CMOS. , 2012, , .		0
161	Rejection of interference and near-field coupled LNA-noise on FPA-fed multibeam dish antennas using 3D analog filters. , 2013, , .		0
162	Low-power CMOS inductorless bandwidth-enhanced transimpedance amplifier for short-haul applications. , 2013, , .		0

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163	Advanced RF and Analog Integrated Circuits for Fourth Generation Wireless Communications and Beyond. International Journal of Microwave Science and Technology, 2013, 2013, 1-2.	0.6	0
164	A 3-D spatially-FIR RF frustum digital filter with microwave channelization for FPAs. , 2014, , .		0
165	Tunable multiband RF CMOS active filter arrays. , 2015, , .		0
166	A 10-GS/s track-and-hold circuit for a 7-bit Square Kilometre Array ADC in 65-nm. , 2017, , .		0
167	0.96-to-5.1GHz 4-element spatially analog IIR-enhanced delay-and-sum beamformer. , 2017, , .		0
168	A Large Phased Array Feed with CMOS Low-Noise Amplifiers. , 2018, , .		0
169	Extraction of Electrical- and Noise-Parameters of Fully-Differential-Amplifier Subcircuits. IEEE Access, 2019, 7, 42123-42132.	2.6	0
170	$\Delta$ noise-shaping in 3-D space-time for 2-D wideband antenna array receivers. Multidimensional Systems and Signal Processing, 2019, 30, 1609-1631.	1.7	0
171	Spatio-Temporal $N \times N$ -Port ADC Noise Shaping for $N \times N$ Antenna Arrays. , 2020, , .		0
172	Dr. Ali Sheikholeslami's Circuit Intuitions Lecture at the University of Calgary [Chapters]. IEEE Solid-State Circuits Magazine, 2020, 12, 72-72.	0.5	0
173	A Wideband 24-29 GHz Differential All-Pass Filter in 65-nm CMOS. , 2021, , .		0
174	Analog Circuit Design Using Symbolic Math Toolboxes: Demonstrative Examples. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2021, , 1-11.	2.1	0
175	Low Noise Amplifiers. , 2007, , 305-328.		0
176	Checklist-based Software Quality Evaluation of Tango Controls. , 2021, , .		0
177	General Framework for Array Noise Analysis and Noise Performance of a Two-Element Interferometer With a Mutual-Coupling Canceler. IEEE Transactions on Antennas and Propagation, 2022, 70, 8059-8068.	3.1	0