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List of Publications by Year in descending order

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citing authors

#	ARTICLE	IF	CITATIONS
1	Inter-Tier Process-Variation-Aware Monolithic 3-D NoC Design Space Exploration. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2020, 28, 686-699.	3.1	9
2	Power Management of Monolithic 3D Manycore Chips with Inter-tier Process Variations. ACM Journal on Emerging Technologies in Computing Systems, 2021, 17, 1-19.	2.3	3